



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS C245 M8 (AMD EPYC 9454 48-Core Processor)

SPECspeed®2017\_int\_base = 14.4

SPECspeed®2017\_int\_peak = 14.6

CPU2017 License: 9019

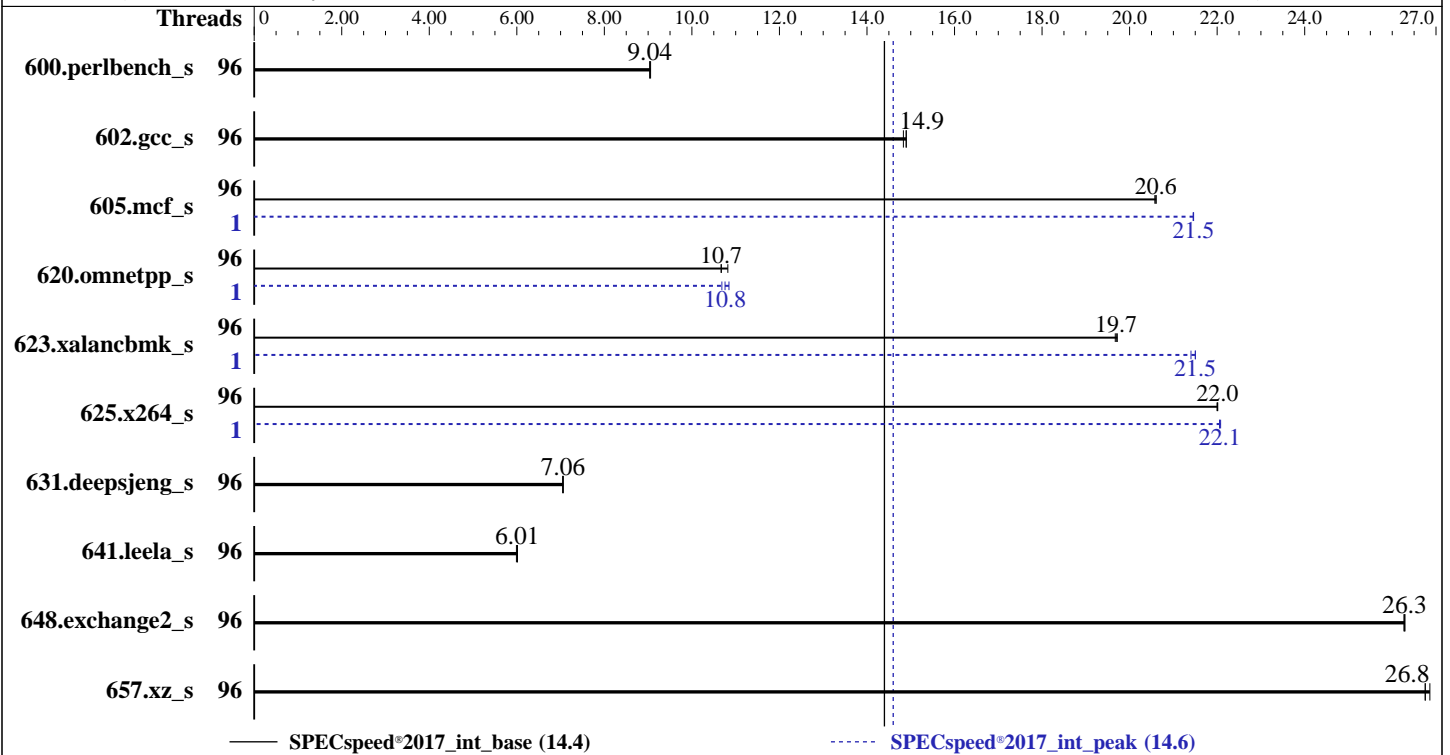
Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jul-2024

Hardware Availability: Jun-2024

Software Availability: Jun-2023



### Hardware

CPU Name: AMD EPYC 9454  
 Max MHz: 3800  
 Nominal: 2750  
 Enabled: 96 cores, 2 chips  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 256 MB I+D on chip per chip, 32 MB shared / 6 cores  
 Other: None  
 Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-5600B-R, running at 4800)  
 Storage: 1 x 1.6 TB NVME SSD  
 Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP5  
 kernel version 5.14.21-150500.53-default  
 Compiler: C/C++/Fortran: Version 4.0.0 of AOCC  
 Parallel: Yes  
 Firmware: Version 4.3.4a released May-2024  
 File System: btrfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base						Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	96	196	9.04	<b><u>196</u></b>	<b><u>9.04</u></b>	196	9.06	96	196	9.04	<b><u>196</u></b>	<b><u>9.04</u></b>	196	9.06
602.gcc_s	96	268	14.8	<b><u>267</u></b>	<b><u>14.9</u></b>	267	14.9	96	268	14.8	<b><u>267</u></b>	<b><u>14.9</u></b>	267	14.9
605.mcf_s	96	229	20.6	<b><u>229</u></b>	<b><u>20.6</u></b>	229	20.6	1	<b><u>220</u></b>	<b><u>21.5</u></b>	220	21.5	220	21.5
620.omnetpp_s	96	153	10.7	<b><u>153</u></b>	<b><u>10.7</u></b>	151	10.8	1	153	10.7	<b><u>152</u></b>	<b><u>10.8</u></b>	150	10.8
623.xalancbmk_s	96	72.0	19.7	71.9	19.7	<b><u>71.9</u></b>	<b><u>19.7</u></b>	1	66.2	21.4	65.9	21.5	<b><u>65.9</u></b>	<b><u>21.5</u></b>
625.x264_s	96	80.1	22.0	80.2	22.0	<b><u>80.2</u></b>	<b><u>22.0</u></b>	1	80.0	22.1	<b><u>80.0</u></b>	<b><u>22.1</u></b>	79.9	22.1
631.deepsjeng_s	96	<b><u>203</u></b>	<b><u>7.06</u></b>	203	7.06	203	7.05	96	<b><u>203</u></b>	<b><u>7.06</u></b>	203	7.06	203	7.05
641.leela_s	96	284	6.01	<b><u>284</u></b>	<b><u>6.01</u></b>	285	5.99	96	284	6.01	<b><u>284</u></b>	<b><u>6.01</u></b>	285	5.99
648.exchange2_s	96	112	26.3	112	26.3	<b><u>112</u></b>	<b><u>26.3</u></b>	96	112	26.3	112	26.3	<b><u>112</u></b>	<b><u>26.3</u></b>
657.xz_s	96	230	26.9	231	26.8	<b><u>231</u></b>	<b><u>26.8</u></b>	96	230	26.9	231	26.8	<b><u>231</u></b>	<b><u>26.8</u></b>

SPECspeed®2017\_int\_base = **14.4**

SPECspeed®2017\_int\_peak = **14.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.



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## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-95"
LD_LIBRARY_PATH = "/home/cpu2017/amd_speed_aocc400_znver4_A_lib/lib:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "oversize_threshold:0,retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "96"
```

Environment variables set by runcpu during the 605.mcf\_s peak run:

```
GOMP_CPU_AFFINITY = "15"
```

Environment variables set by runcpu during the 620.omnetpp\_s peak run:

```
GOMP_CPU_AFFINITY = "15"
```

Environment variables set by runcpu during the 623.xalancbmk\_s peak run:

```
GOMP_CPU_AFFINITY = "15"
```

Environment variables set by runcpu during the 625.x264\_s peak run:

```
GOMP_CPU_AFFINITY = "15"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS settings:

```
SMT Mode set to Disabled
NUMA nodes per socket set to NPS1
Determinism Slider set to Power
DF C-States set to Disabled
TDP set to 400
PPT set to 400
ACPI SRAT L3 Cache as NUMA Domain set to Enabled
```

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Mon Jul 22 20:07:54 2024
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w

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### Platform Notes (Continued)

```

3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

```

```

-----
1. uname -a
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
x86_64 x86_64 x86_64 GNU/Linux

```

```

-----
2. w
 20:07:54 up 18 min,  1 user,  load average: 0.21, 0.05, 0.02
USER      TTY      FROM          LOGIN@   IDLE   JCPU   PCPU   WHAT
root      tty1    -             20:06   25.00s  1.11s  0.12s  /bin/bash ./amd_speed_aocc400_znver4_A1.sh

```

```

-----
3. Username
From environment variable $USER:  root

```

```

-----
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size                (blocks, -f) unlimited
pending signals         (-i) 6191331
max locked memory       (kbytes, -l) 2097152
max memory size         (kbytes, -m) unlimited
open files               (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size               (kbytes, -s) unlimited
cpu time                 (seconds, -t) unlimited
max user processes      (-u) 6191331
virtual memory           (kbytes, -v) unlimited
file locks               (-x) unlimited

```

```

-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root

```

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### Platform Notes (Continued)

```

-bash
python3 ./run_amd_speed_aocc400_znver4_A1.py -b intspeed
/bin/bash ./amd_speed_aocc400_znver4_A1.sh
runcpu --config amd_speed_aocc400_znver4_A1.cfg --tune all --reportable --iterations 3 intspeed
runcpu --configfile amd_speed_aocc400_znver4_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed intspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.intspeed.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

```

#### 6. /proc/cpuinfo

```

model name      : AMD EPYC 9454 48-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 25
model          : 17
stepping       : 1
microcode      : 0xa101148
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size      : 3584 4K pages
cpu cores     : 48
siblings      : 48
2 physical ids (chips)
96 processors (hardware threads)
physical id 0: core ids 0-5,16-21,32-37,48-53,64-69,80-85,96-101,112-117
physical id 1: core ids 0-5,16-21,32-37,48-53,64-69,80-85,96-101,112-117
physical id 0: apicids 0-5,16-21,32-37,48-53,64-69,80-85,96-101,112-117
physical id 1: apicids 128-133,144-149,160-165,176-181,192-197,208-213,224-229,240-245

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

#### 7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 52 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95
Vendor ID: AuthenticAMD
Model name: AMD EPYC 9454 48-Core Processor
CPU family: 25
Model: 17
Thread(s) per core: 1
Core(s) per socket: 48
Socket(s): 2
Stepping: 1
Frequency boost: enabled
CPU max MHz: 3810.7910
CPU min MHz: 1500.0000
BogoMIPS: 5491.26
Flags: fpu_vme_de_pse_tsc_msr_pae_mce_cx8_apic_sep_mtrr_pge_mca_cmov_pat_pse36
clflush_mmx_fxsr_sse_sse2_ht_syscall_nx_mmxext_fxsr_opt_pdpelgb_rdtscp_lm
constant_tsc_rep_good_amd_lbr_v2_nopl_nonstop_tsc_cpuid_extd_apicid
aperfmpperf_rapl_pni_pclmulqdq_monitor_ssse3_fma_cx16_pcid_sse4_1_sse4_2
x2apic_movbe_popcnt_aes_xsaves_avx_f16c_rdrand_lahf_lm_cmp_legacy_svm
extapic_cr8_legacy_abm_sse4a_misalignsse_3dnowprefetch_osvw_ibs_skinit_wdt
tce_topoext_perfctr_core_perfctr_nb_bpext_perfctr_llc_mwaitx_cpb_cat_l3

```

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### Platform Notes (Continued)

cdp\_l3 invpcid\_single hw\_pstate ssbd mba perfmon\_v2 ibrs ibpb stibp  
vmmcall fsgsbase bmil avx2 smep bmi2 erms invpcid cqm rdt\_a avx512f  
avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha\_ni  
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm\_llc cqm\_occup\_llc  
cqm\_mbm\_total cqm\_mbm\_local avx512\_bf16 clzero irperf xsaveerptr rdpru  
wbnoinvd amd\_ppin cppc arat npt lbrv svm\_lock nrip\_save tsc\_scale  
vmcb\_clean flushbyasid decodeassists pausefilter pfthreshold avic  
v\_omsave\_vmload vgif v\_spec\_ctrl avx512vbmi umip pku ospke avx512\_vbmi2  
gfni vaes vpclmulqdq avx512\_vnni avx512\_bitalg avx512\_vpopcntdq la57 rdpid  
overflow\_recov succor smca fsrm flush\_lld

Virtualization: AMD-V  
L1d cache: 3 MiB (96 instances)  
L1i cache: 3 MiB (96 instances)  
L2 cache: 96 MiB (96 instances)  
L3 cache: 512 MiB (16 instances)  
NUMA node(s): 2  
NUMA node0 CPU(s): 0-47  
NUMA node1 CPU(s): 48-95  
Vulnerability Itlb multihit: Not affected  
Vulnerability L1tf: Not affected  
Vulnerability Mds: Not affected  
Vulnerability Meltdown: Not affected  
Vulnerability Mmio stale data: Not affected  
Vulnerability Retbleed: Not affected  
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp  
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and \_\_user pointer sanitization  
Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS\_FW, STIBP disabled, RSB filling, PBRSE-eIBRS Not affected  
Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected

From lscpu --cache:  
NAME ONE-SIZE ALL-SIZE WAYS TYPE LEVEL SETS PHY-LINE COHERENCY-SIZE  
L1d 32K 3M 8 Data 1 64 1 64  
L1i 32K 3M 8 Instruction 1 64 1 64  
L2 1M 96M 8 Unified 2 2048 1 64  
L3 32M 512M 16 Unified 3 32768 1 64

8. numactl --hardware  
NOTE: a numactl 'node' might or might not correspond to a physical chip.  
available: 2 nodes (0-1)  
node 0 cpus: 0-47  
node 0 size: 773829 MB  
node 0 free: 772468 MB  
node 1 cpus: 48-95  
node 1 size: 774033 MB  
node 1 free: 772569 MB  
node distances:  
node 0 1  
0: 10 32  
1: 32 10

9. /proc/meminfo  
MemTotal: 1585011492 kB

10. who -r  
run-level 3 Jul 22 19:49

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## Platform Notes (Continued)

```
-----
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
    Default Target  Status
    multi-user      running
```

```
-----
12. Services, from systemctl list-unit-files
    STATE          UNIT FILES
    enabled        YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance iscsi
                    issue-generator kbdsettings klog libvirtd lvm2-monitor nscd postfix purge-kernels rollback
                    rsyslog smartd sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6
                    wickedd-nanny
    enabled-runtime systemd-remount-fs
    disabled       autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                    chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables exchange-bmc-os-info
                    firewallld gpm grub2-once haveged haveged-switch-root hwloc-dump-hwdata ipmi ipmievd
                    iscsi-init iscsid issue-add-ssh-keys kdump kdump-early kexec-load ksm kvm_stat
                    libvirt-guests lunmask man-db-create multipathd nfs nfs-blkmap nfs-server nfsserver
                    rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd
                    strongswan strongswan-starter svnserv systemd-boot-check-no-failures
                    systemd-network-generator systemd-nspawn@ systemd-sysext systemd-time-wait-sync
                    systemd-timesyncd tcsd udisks2 virtinterfaced virtnetworkd virtnodedevd virtnwfilterd
                    virtproxyd virtqemud virtsecretd virtstoraged
    indirect       pcsd virtlockd virtlogd wickedd
```

```
-----
13. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
    root=UUID=5eac0278-b5d7-4d70-9f22-3587a0d03dd8
    splash=silent
    mitigations=auto
    quiet
    security=apparmor
```

```
-----
14. cpupower frequency-info
    analyzing CPU 0:
        current policy: frequency should be within 1.50 GHz and 2.75 GHz.
                        The governor "performance" may decide which speed to use
                        within this range.

    boost state support:
        Supported: yes
        Active: yes
```

```
-----
15. sysctl
    kernel.numa_balancing          1
    kernel.randomize_va_space     0
    vm.compaction_proactiveness    20
    vm.dirty_background_bytes      0
    vm.dirty_background_ratio     10
    vm.dirty_bytes                 0
    vm.dirty_expire_centisecs     3000
    vm.dirty_ratio                 8
    vm.dirty_writeback_centisecs   500
    vm.dirtytime_expire_seconds    43200
    vm.extfrag_threshold           500
    vm.min_unmapped_ratio         1
    vm.nr_hugepages                0
```

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### Platform Notes (Continued)

```

vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages    0
vm.swappiness                  1
vm.watermark_boost_factor     15000
vm.watermark_scale_factor     10
vm.zone_reclaim_mode          1

```

```

-----
16. /sys/kernel/mm/transparent_hugepage
defrag      [always] defer defer+madvise madvise never
enabled     [always] madvise never
hpage_pmd_size  2097152
shmem_enabled  always within_size advise [never] deny force

```

```

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs  10000

```

```

-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP5

```

```

-----
19. Disk information
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       btrfs 222G  11G  211G   5% /home

```

```

-----
20. /sys/devices/virtual/dmi/id
Vendor:         Cisco Systems Inc
Product:        UCSC-C245-M8SX
Serial:         WZP27360C65

```

```

-----
21. dmidecode
Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  4x 0xCE00 M321R8GA0PB0-CWMJH 64 GB 2 rank 5600, configured at 4800
  20x 0xCE00 M321R8GA0PB0-CWMKJ 64 GB 2 rank 5600, configured at 4800

```

```

-----
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      Cisco Systems, Inc.
BIOS Version:     C245M8.4.3.4a.0.0520240849
BIOS Date:        05/20/2024
BIOS Revision:    5.27

```





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## Compiler Version Notes

-----  
C | 600.perlbench\_s(base, peak) 602.gcc\_s(base, peak) 605.mcf\_s(base, peak) 625.x264\_s(base, peak)  
657.xz\_s(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin  
-----

-----  
C++ | 620.omnetpp\_s(base, peak) 623.xalancbmk\_s(base, peak) 631.deepsjeng\_s(base, peak)  
641.leela\_s(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin  
-----

-----  
Fortran | 648.exchange2\_s(base, peak)  
-----

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin  
-----

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LINUX\_X64 -DSPEC\_LP64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LINUX -DSPEC\_LP64  
625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64

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# SPEC CPU®2017 Integer Speed Result

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## Cisco Systems

Cisco UCS C245 M8 (AMD EPYC 9454 48-Core Processor)

SPECspeed®2017\_int\_base = 14.4

SPECspeed®2017\_int\_peak = 14.6

CPU2017 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jul-2024

Hardware Availability: Jun-2024

Software Availability: Jun-2023

## Base Portability Flags (Continued)

641.leela\_s: -DSPEC\_LP64  
648.exchange2\_s: -DSPEC\_LP64  
657.xz\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-allow-multiple-definition -O3 -march=znver4 -fveclib=AMDLIBM  
-ffast-math -fopenmp -flto -fstruct-layout=7  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3  
-DSPEC\_OPENMP -zopt -fopenmp=libomp -lomp -lamdlibm -lflang  
-lamdalloc

C++ benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver4  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto  
-mllvm -unroll-threshold=100 -finline-aggressive  
-mllvm -loop-unswitch-threshold=200000  
-mllvm -reduce-array-computations=3 -DSPEC\_OPENMP -zopt  
-fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp  
-lomp -lamdlibm -lflang -lamdalloc-ext

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop  
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fveclib=AMDLIBM  
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost  
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp  
-lomp -lamdlibm -lflang -lamdalloc

## Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

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SPECspeed®2017\_int\_peak = 14.6

**CPU2017 License:** 9019

**Test Sponsor:** Cisco Systems

**Tested by:** Cisco Systems

**Test Date:** Jul-2024

**Hardware Availability:** Jun-2024

**Software Availability:** Jun-2023

## Base Other Flags (Continued)

Fortran benchmarks:

-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

600.perlbench\_s: basepeak = yes

602.gcc\_s: basepeak = yes

605.mcf\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3

-Wl,-allow-multiple-definition -Ofast -march=znver4

-fveclib=AMDLIBM -ffast-math -fopenmp -flto

-fstruct-layout=9 -mllvm -unroll-threshold=50

-fremap-arrays -fstrip-mining

-mllvm -inline-threshold=1000

-mllvm -reduce-array-computations=3 -DSPEC\_OPENMP -zopt

-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

625.x264\_s: Same as 605.mcf\_s

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CPU2017 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jul-2024

Hardware Availability: Jun-2024

Software Availability: Jun-2023

## Peak Optimization Flags (Continued)

657.xz\_s: basepeak = yes

C++ benchmarks:

```
620.omnetpp_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdalloc-ext -lflang
```

```
623.xalancbmk_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=aggressive -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdalloc-ext -lflang
```

631.deepsjeng\_s: basepeak = yes

641.leela\_s: basepeak = yes

Fortran benchmarks:

648.exchange2\_s: basepeak = yes

## Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument



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**Tested by:** Cisco Systems

**Test Date:** Jul-2024

**Hardware Availability:** Jun-2024

**Software Availability:** Jun-2023

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-v3-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-v3-revA.xml>

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Tested with SPEC CPU®2017 v1.1.9 on 2024-07-22 20:07:53-0400.

Report generated on 2024-08-14 14:06:58 by CPU2017 PDF formatter v6716.

Originally published on 2024-08-13.