



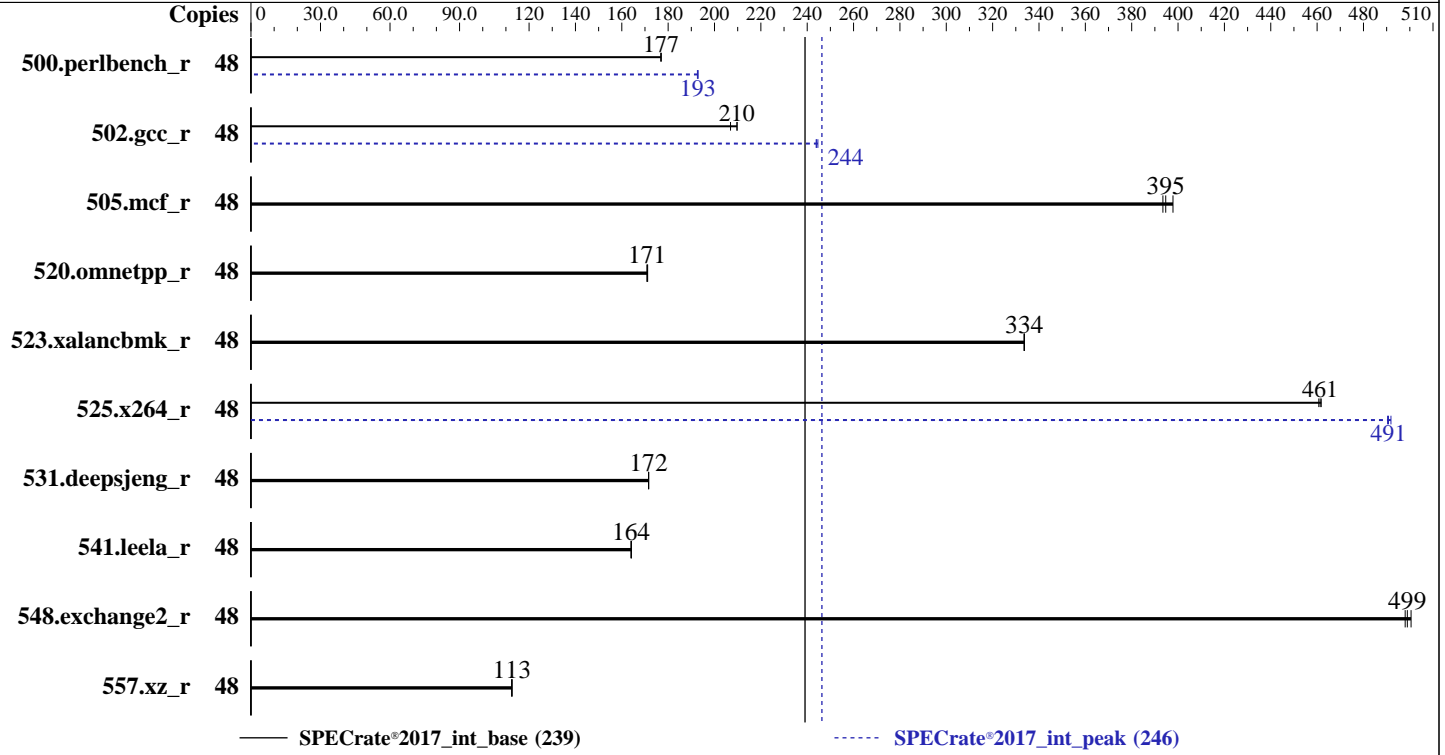
SPEC CPU®2017 Integer Rate Result

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Tyrone Systems
 (Test Sponsor: Netweb Technologies India Ltd)
 Tyrone Camarero SDI200A3N-18
 (2.40 GHz, Intel Xeon Silver 4510)

SPECrate®2017_int_base = 239
SPECrate®2017_int_peak = 246

CPU2017 License: 006802 **Test Date:** Sep-2024
Test Sponsor: Netweb Technologies India Ltd **Hardware Availability:** Dec-2023
Tested by: Tyrone Systems **Software Availability:** Dec-2023



Hardware

CPU Name: Intel Xeon Silver 4510
 Max MHz: 4100
 Nominal: 2400
 Enabled: 24 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 30 MB I+D on chip per chip
 Other: None
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R, running at 4400)
 Storage: 1 x 960 GB NVMe
 Other: CPU Cooling: Air

Software

OS: Red Hat Enterprise Linux 9.3 (Plow)
 5.14.0-362.13.1.el9_3.x86_64
 Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;
 Parallel: No
 Firmware: Version 2.1a released Mar-2024
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance at cost of additional power.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	48	432	177	432	177	432	177	48	397	193	396	193	396	193
502.gcc_r	48	328	207	324	210	324	210	48	279	244	279	244	278	245
505.mcf_r	48	197	393	195	398	197	395	48	197	393	195	398	197	395
520.omnetpp_r	48	368	171	369	171	368	171	48	368	171	369	171	368	171
523.xalancbmk_r	48	152	334	152	334	152	334	48	152	334	152	334	152	334
525.x264_r	48	182	461	182	462	182	461	48	171	490	171	491	171	492
531.deepsjeng_r	48	321	172	321	172	321	172	48	321	172	321	172	321	172
541.leela_r	48	485	164	484	164	485	164	48	485	164	484	164	485	164
548.exchange2_r	48	252	499	251	501	253	498	48	252	499	251	501	253	498
557.xz_r	48	460	113	461	112	460	113	48	460	113	461	112	460	113

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
We are using specific Kernel Version

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the

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General Notes (Continued)

system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS Settings:

Power Technology = Custom

ENERGY_PERF_BIAS_CFG mode = Maximum Performance

KTI Prefetch = Enable

LLC Dead Line Alloc = Disable

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost.localdomain Wed Sep 11 11:47:04 2024

SUT (System Under Test) info as seen by some common utilities.

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1. uname -a
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3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
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10. who -r
11. Systemd service manager version: systemd 252 (252-18.e19)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

1. uname -a
Linux localhost.localdomain 5.14.0-362.13.1.el9_3.x86_64 #1 SMP PREEMPT_DYNAMIC Fri Nov 24 01:57:57 EST 2023 x86_64 x86_64 x86_64 GNU/Linux

2. w
11:47:04 up 5 min, 1 user, load average: 0.00, 0.04, 0.02
USER TTY LOGIN@ IDLE JCPU PCPU WHAT
root tty1 11:46 7.00s 0.91s 0.02s -bash

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Platform Notes (Continued)

3. Username

From environment variable \$USER: root

4. ulimit -a

```

real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 4126772
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 4126772
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

```

5. sysinfo process ancestry

```

/usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=48 -c
ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=24 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=48 --configfile
ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=24 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
--runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

```

6. /proc/cpuinfo

```

model name      : INTEL(R) XEON(R) SILVER 4510
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping       : 8
microcode      : 0x2b000571
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores      : 12
siblings       : 24
2 physical ids (chips)
48 processors (hardware threads)
physical id 0: core ids 0-11
physical id 1: core ids 0-11
physical id 0: apicids 0-23
physical id 1: apicids 64-87

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

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Platform Notes (Continued)

7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):       32-bit, 64-bit
Address sizes:        46 bits physical, 57 bits virtual
Byte Order:           Little Endian
CPU(s):               48
On-line CPU(s) list: 0-47
Vendor ID:            GenuineIntel
BIOS Vendor ID:      Intel(R) Corporation
Model name:           INTEL(R) XEON(R) SILVER 4510
BIOS Model name:     INTEL(R) XEON(R) SILVER 4510
CPU family:           6
Model:                143
Thread(s) per core:  2
Core(s) per socket:  12
Socket(s):            2
Stepping:             8
CPU max MHz:          4100.0000
CPU min MHz:          800.0000
BogoMIPS:             4800.00
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                    clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb
                    rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl
                    xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq
                    dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
                    pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
                    avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2
                    cdp_l3 invpcid_single intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp
                    ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad fsgsbase
                    tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq
                    rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni
                    avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc
                    cqm_mbm_total cqm_mbm_local split_lock_detect avx_vnni avx512_bf16
                    wbnoinvd dtherm ida arat pln pts vnmi avx512vbmi umip pku ospke waitpkg
                    avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
                    avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
                    enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16
                    avx512_fp16 amx_tile amx_int8 flush_lld arch_capabilities

Virtualization:       VT-x
L1d cache:            1.1 MiB (24 instances)
L1i cache:            768 KiB (24 instances)
L2 cache:             48 MiB (24 instances)
L3 cache:             60 MiB (2 instances)
NUMA node(s):        2
NUMA node0 CPU(s):   0-11,24-35
NUMA node1 CPU(s):   12-23,36-47
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit:      Not affected
Vulnerability L1tf:               Not affected
Vulnerability Mds:                Not affected
Vulnerability Meltdown:           Not affected
Vulnerability Mmio stale data:    Not affected
Vulnerability Retbleed:           Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass:  Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:         Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:         Mitigation; Enhanced / Automatic IBRS, IBPB conditional, RSB filling,

```

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Platform Notes (Continued)

PBRSE-eIBRS SW sequence

Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.1M	12	Data	1	64	1	64
L1i	32K	768K	8	Instruction	1	64	1	64
L2	2M	48M	16	Unified	2	2048	1	64
L3	30M	60M	15	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0-11,24-35
node 0 size: 515727 MB
node 0 free: 514506 MB
node 1 cpus: 12-23,36-47
node 1 size: 516031 MB
node 1 free: 514852 MB
node distances:
node 0 1
0: 10 21
1: 21 10

```

9. /proc/meminfo

MemTotal: 1056521180 kB

10. who -r

run-level 3 Sep 11 11:41

11. Systemd service manager version: systemd 252 (252-18.el9)

Default Target	Status
multi-user	running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon atd auditd avahi-daemon bluetooth chronyd crond cups dbus-broker firewallld gdm getty@ insights-client-boot irqbalance iscsi iscsi-onboot kdump libstoragemgmt lm_sensors low-memory-monitor lvm2-monitor mcelog mdmonitor microcode multipathd nis-domainname nvme-fc-boot-connections ostree-remount pmcd pmie pmlogger power-profiles-daemon qemu-guest-agent rhsmcertd rpcbind rsyslog rtkit-daemon selinux-autorelabel-mark smartd sshd sssd switcheroo-control sysstat systemd-boot-update systemd-network-generator tuned udisks2 upower vgauthd virtqemud vmtoolsd
enabled-runtime	systemd-remount-fs
disabled	arp-ethers autofs blk-availability brltyt canberra-system-bootup canberra-system-shutdown canberra-system-shutdown-reboot chrony-wait cni-dhcp console-getty cpupower cups-browsed dbus-daemon debug-shell dnf-system-upgrade dnsmasq dovecot fancontrol fcoe grafana-server gssproxy httpd httpd@ ibacm iprdump iprinit ipupdate ipsec iscsid iscsiuiio kpatch kvm_stat ledmon libvirt-guests libvirtld lldpad man-db-restart-cache-update named named-chroot netavark-dhcp-proxy nfs-blkmap nfs-server nftables nmb numad nvmmf-autoconnect ostree-readonly-sysroot-migration pesign pmfind pmie_farm pmlogger_farm pmproxy podman podman-auto-update podman-clean-transient podman-kube@ podman-restart postfix powertop psacct ras-mc-ctl rasdaemon rdisc rhcd rhsm rhsm-facts rpmbd-rebuild rrdcached saslauthd

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Platform Notes (Continued)

```

indirect
selinux-check-proper-disable serial-getty@ smb snmpd snmptrapd spamassassin
speech-dispatcherd srp_daemon srp_daemon_port@ sshd-keygen@ systemd-boot-check-no-failures
systemd-nspawn@ systemd-pstore systemd-sysext target targetclid tog-pegasus trace-cmd
virtinterfaced virtnetworkd virtnodevdev virtnwfilterd virtproxyd virtsecret d virtstaged
vsftpd wpa_supplicant
pcscd spice-vdagentd sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo
systemd-sysupdate systemd-sysupdate-reboot virtlockd virtlogd vsftpd@

```

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-362.13.1.el9_3.x86_64
root=/dev/mapper/rhel-root
ro
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap
rhgb
quiet

```

```

-----
14. cpupower frequency-info
analyzing CPU 0:
  current policy: frequency should be within 4.10 GHz and 4.10 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.
  boost state support:
    Supported: yes
    Active: yes

```

```

-----
15. tuned-adm active
  Current active profile: throughput-performance

```

```

-----
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space      2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                  40
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                    10
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0

```

```

-----
17. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled        [always] madvice never
hpage_pmd_size 2097152

```

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Platform Notes (Continued)

shmem_enabled always within_size advise [never] deny force

```

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000
-----

```

```

-----
19. OS release
From /etc/*-release /etc/*-version
os-release Red Hat Enterprise Linux 9.3 (Plow)
redhat-release Red Hat Enterprise Linux release 9.3 (Plow)
system-release Red Hat Enterprise Linux release 9.3 (Plow)
-----

```

```

-----
20. Disk information
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 856G 729G 127G 86% /home
-----

```

```

-----
21. /sys/devices/virtual/dmi/id
Vendor: Tyrone Systems
Product: Tyrone Camarero SDI200A2N-18
Product Family: Family
Serial: A495115X4412722
-----

```

```

-----
22. dmidecode
Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
16x Samsung M321R8GA0BB0-CQKZJ 64 GB 2 rank 4800, configured at 4400
-----

```

```

-----
23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 2.1a
BIOS Date: 03/20/2024
BIOS Revision: 5.32
-----

```

Compiler Version Notes

C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

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Compiler Version Notes (Continued)

```
-----
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
      | 557.xz_r(base, peak)
-----
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----
```

```
-----
C      | 502.gcc_r(peak)
-----
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----
```

```
-----
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
      | 557.xz_r(base, peak)
-----
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----
```

```
-----
C++    | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
      | 541.leela_r(base, peak)
-----
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----
```

```
-----
Fortran | 548.exchange2_r(base, peak)
-----
```

```
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----
```

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx



SPEC CPU®2017 Integer Rate Result

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Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A3N-18
(2.40 GHz, Intel Xeon Silver 4510)

SPECrate®2017_int_base = 239

SPECrate®2017_int_peak = 246

CPU2017 License: 006802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2024

Hardware Availability: Dec-2023

Software Availability: Dec-2023

Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmallo
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmallo
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmallo
```

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx



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Software Availability: Dec-2023

Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: basepeak = yes
```

C++ benchmarks:

(Continued on next page)



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Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2024

Hardware Availability: Dec-2023

Software Availability: Dec-2023

Peak Optimization Flags (Continued)

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-SPR-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-SPR-revC.xml>

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Tested with SPEC CPU®2017 v1.1.9 on 2024-09-11 02:17:04-0400.

Report generated on 2024-10-09 14:01:57 by CPU2017 PDF formatter v6716.

Originally published on 2024-10-09.