



SPEC CPU®2017 Floating Point Rate Result

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IEIT Systems Co., Ltd.

SPECrate®2017_fp_base = 1310

meta brain NF3290G8 (Intel Xeon 6980P)

SPECrate®2017_fp_peak = 1360

CPU2017 License: 3358

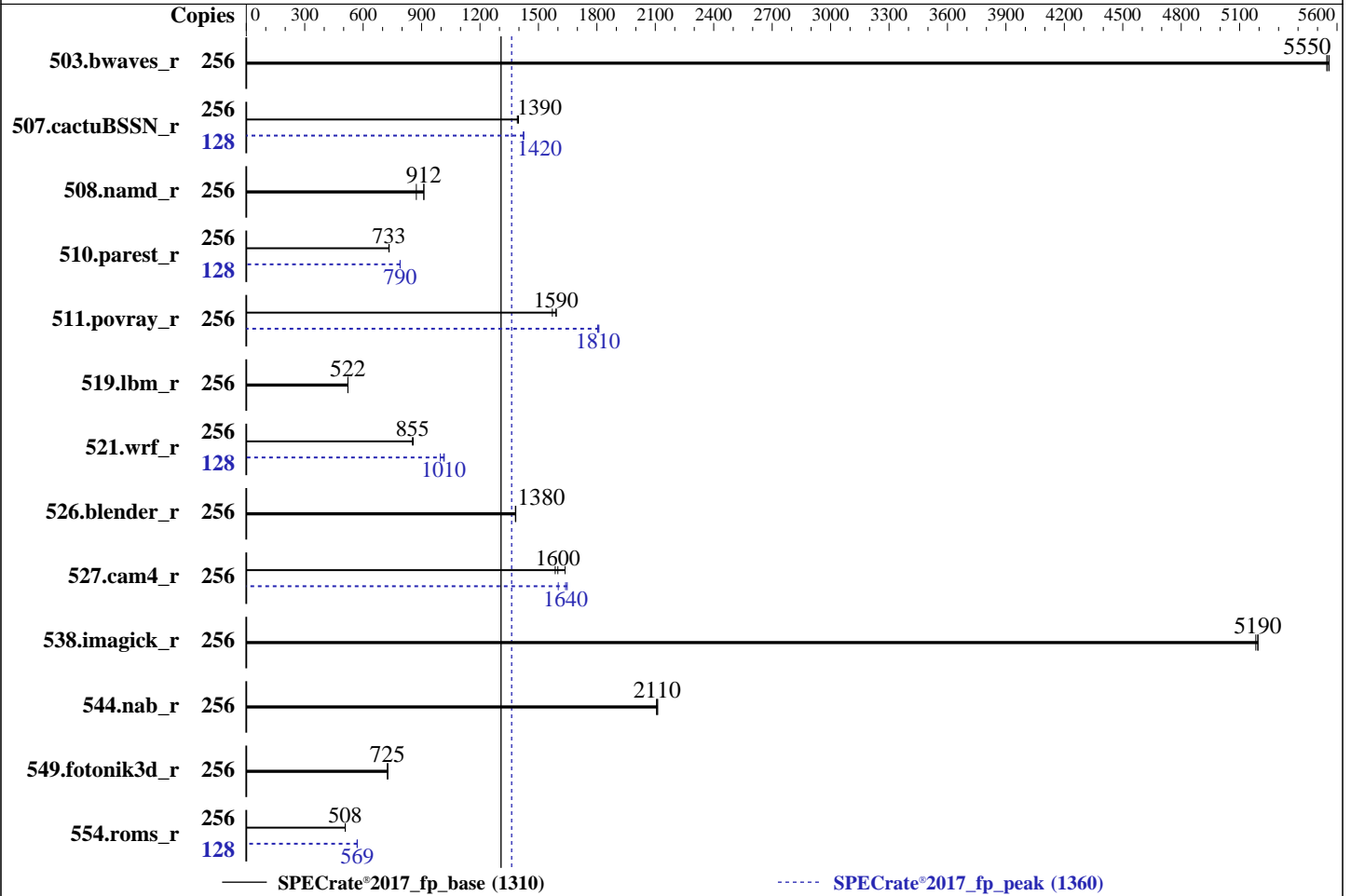
Test Date: Oct-2024

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Oct-2024

Tested by: IEIT Systems Co., Ltd.

Software Availability: Jun-2024



Hardware

CPU Name: Intel Xeon 6980P
 Max MHz: 3900
 Nominal: 2000
 Enabled: 128 cores, 1 chip, 2 threads/core
 Orderable: 1 chip
 Cache L1: 64 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 504 MB I+D on chip per chip
 Other: None
 Memory: 768 GB (12 x 64 GB 2Rx4 PC5-8800B-R)
 Storage: 1 x 960 GB SSD
 Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6
 6.4.0-150600.21-default
 Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++
 Compiler for Linux;
 Fortran: Version 2024.1 of Intel Fortran Compiler
 for Linux;
 Parallel: No
 Firmware: Version 00.13.05 released Sep-2024
 File System: btrfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS set to prefer performance at the cost
 of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	256	463	5550	462	5560	462	5550	256	463	5550	462	5560	462	5550
507.cactuBSSN_r	256	232	1400	233	1390	232	1390	128	114	1420	114	1420	114	1420
508.namd_r	256	279	873	267	912	267	912	256	279	873	267	912	267	912
510.parest_r	256	915	732	912	734	914	733	128	424	790	424	790	424	790
511.povray_r	256	380	1570	376	1590	376	1590	256	331	1800	331	1810	330	1810
519.lbm_r	256	517	522	517	522	517	522	256	517	522	517	522	517	522
521.wrf_r	256	672	853	671	855	669	857	128	283	1010	282	1020	288	996
526.blender_r	256	282	1380	282	1380	282	1380	256	282	1380	282	1380	282	1380
527.cam4_r	256	282	1590	280	1600	274	1640	256	272	1650	273	1640	280	1600
538.imagick_r	256	123	5180	123	5190	123	5190	256	123	5180	123	5190	123	5190
544.nab_r	256	204	2110	205	2110	204	2110	256	204	2110	205	2110	204	2110
549.fotonik3d_r	256	1373	726	1376	725	1376	725	256	1373	726	1376	725	1376	725
554.roms_r	256	800	508	801	508	800	508	128	357	569	357	570	357	569

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/je5.0.1-64"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)

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General Notes (Continued)

is mitigated in the system as tested and documented.
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS configuration:
 ENERGY_PERF_BIAS_CFG mode set to Performance
 Hardware Prefetch set to Disable
 VT Support set to Disable
 SNC set to SNC3

Sysinfo program /home/CPU2017/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost Fri Oct 25 18:55:45 2024

SUT (System Under Test) info as seen by some common utilities.

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18. /sys/kernel/mm/transparent_hugepage/khugepaged
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22. dmidecode
23. BIOS

1. uname -a
 Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
 x86_64 x86_64 x86_64 GNU/Linux

2. w

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Platform Notes (Continued)

```

18:55:45 up 7:02, 1 user, load average: 142.83, 225.83, 242.12
USER      TTY      FROM          LOGIN@      IDLE        JCPU        PCPU        WHAT
root      tty1     -             11:55      5:47m      1.28s      0.03s      sh runfull512+2.sh

```

3. Username

From environment variable \$USER: root

4. ulimit -a

```

core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 3092354
max locked memory       (kbytes, -l) 8192
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 3092354
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited

```

5. sysinfo process ancestry

```

/usr/lib/systemd/systemd --switched-root --system --deserialize=31
login -- root
-bash
sh runfull512+2.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=256 -c
  ic2024.1-lin-core-avx2-rate-20240308.cfg --define smt-on --define cores=128 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak -o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=256 --configfile
  ic2024.1-lin-core-avx2-rate-20240308.cfg --define smt-on --define cores=128 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
  --runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/CPU2017

```

6. /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) 6980P
vendor_id      : GenuineIntel
cpu family     : 6
model          : 173
stepping       : 1
microcode      : 0x810002a0
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores      : 128
siblings       : 256
1 physical ids (chips)
256 processors (hardware threads)
physical id 0: core ids 0-42,64-106,128-169
physical id 0: apicids 0-85,128-213,256-339
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for

```

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Platform Notes (Continued)

virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                256
On-line CPU(s) list:   0-255
Vendor ID:             GenuineIntel
BIOS Vendor ID:        Intel(R) Corporation
Model name:            Intel(R) Xeon(R) 6980P
BIOS Model name:       Intel(R) Xeon(R) 6980P  CPU @ 2.0GHz
BIOS CPU family:       179
CPU family:            6
Model:                 173
Thread(s) per core:    2
Core(s) per socket:    128
Socket(s):             1
Stepping:              1
Frequency boost:       enabled
CPU(s) scaling MHz:    40%
CPU max MHz:           2001.0000
CPU min MHz:           800.0000
BogoMIPS:              4000.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
pclmulqdq dtes64 monitor ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr
pdc_m pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer
aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb
cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp
ibrs_enhanced fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms
invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local split_lock_detect user_shstk avx_vnni avx512_bf16
wbnoinvd dtherm ida arat pln pts avx512vbmi umip pku ospke waitpkg
avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri
movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr
ibt amx_bf16 avx512_fp16 amx_tile amx_int8 flush_lld
arch_capabilities
L1d cache:            6 MiB (128 instances)
L1i cache:            8 MiB (128 instances)
L2 cache:             256 MiB (128 instances)
L3 cache:             504 MiB (1 instance)
NUMA node(s):         3
NUMA node0 CPU(s):    0-42,128-170
NUMA node1 CPU(s):    43-85,171-213
NUMA node2 CPU(s):    86-127,214-255
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit:      Not affected
Vulnerability L1tf:              Not affected
Vulnerability Mds:               Not affected
Vulnerability Meltdown:          Not affected

```

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Platform Notes (Continued)

Vulnerability Mmio stale data: Not affected
 Vulnerability Reg file data sampling: Not affected
 Vulnerability Retbleed: Not affected
 Vulnerability Spec rstack overflow: Not affected
 Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
 Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
 Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSE-eIBRS Not affected; BHI BHI_DIS_S
 Vulnerability Srbds: Not affected
 Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	6M	12	Data	1	64	1	64
L1i	64K	8M	16	Instruction	1	64	1	64
L2	2M	256M	16	Unified	2	2048	1	64
L3	504M	504M	16	Unified	3	516096	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 3 nodes (0-2)
node 0 cpus: 0-42,128-170
node 0 size: 257524 MB
node 0 free: 221824 MB
node 1 cpus: 43-85,171-213
node 1 size: 258025 MB
node 1 free: 223623 MB
node 2 cpus: 86-127,214-255
node 2 size: 257563 MB
node 2 free: 225018 MB
node distances:
node  0  1  2
  0: 10 12 12
  1: 12 10 12
  2: 12 12 10
```

9. /proc/meminfo

MemTotal: 791668068 kB

10. who -r

run-level 3 Oct 25 11:53

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

```
Default Target Status
multi-user      running
```

12. Services, from systemctl list-unit-files

```
STATE UNIT FILES
enabled apparmor auditd cron firewalld getty@ irqbalance issue-generator kbdsettings kdump
kdump-early kdump-notify nvme-fc-boot-connections nvme-f-autoconnect postfix purge-kernels
rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6
wickedd-nanny
enabled-runtime systemd-remount-fs
disabled boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell ebttables
exchange-bmc-os-info fsidd grub2-once haveged hwloc-dump-hwdata ipmievd issue-add-ssh-keys
```

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Platform Notes (Continued)

```

kexec-load nfs nfs-blkmap rpcbind rpmconfigcheck serial-getty@
systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysext
systemd-time-wait-sync systemd-timesyncd tuned
systemd-userdbd wickedd
indirect

```

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=daf71402-9a22-417d-a498-881cf3539e73
splash=silent
mitigations=auto
quiet
security=apparmor
crashkernel=384M,high
crashkernel=72M,low

```

```

-----
14. cpupower frequency-info
analyzing CPU 6:
  current policy: frequency should be within 800 MHz and 2.00 GHz.
                  The governor "ondemand" may decide which speed to use
                  within this range.

  boost state support:
    Supported: yes
    Active: yes

```

```

-----
15. tuned-adm active
It seems that tuned daemon is not running, preset profile is not activated.
Preset profile: throughput-performance

```

```

-----
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space      2
vm.compaction_proactiveness     20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                  20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold            500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                    60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0

```

```

-----
17. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force

```

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Platform Notes (Continued)

```

18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000

```

```

19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6

```

```

20. Disk information
SPEC is set to: /home/CPU2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p2 btrfs 892G 300G 591G 34% /home

```

```

21. /sys/devices/virtual/dmi/id
Vendor: IEIT SYSTEMS
Product: NF3290-M8-A0-R0-00
Product Family: Not specified
Serial: 000000000

```

```

22. dmidecode
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
12x Micron MTC40F2046S1HC88XD1 WFFFG 64 GB 2 rank 8800

```

```

23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 00.13.05
BIOS Date: 09/29/2024

```

Compiler Version Notes

```

=====
C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
=====

```

```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

```

```

=====
C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)
=====

```

```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Compiler Version Notes (Continued)

=====
C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)
=====

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Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====
Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====
Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

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Base Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
 507.cactuBSSN_r: -DSPEC_LP64
 508.namd_r: -DSPEC_LP64
 510.parest_r: -DSPEC_LP64
 511.povray_r: -DSPEC_LP64
 519.lbm_r: -DSPEC_LP64
 521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
 526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
 527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
 538.imagick_r: -DSPEC_LP64
 544.nab_r: -DSPEC_LP64
 549.fotonik3d_r: -DSPEC_LP64
 554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
 -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
 -Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
 -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
 -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math -flto
 -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
 -nostandard-realloc-lhs -align array32byte -auto -ljemalloc
 -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
 -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

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SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECrate®2017_fp_base = 1310

meta brain NF3290G8 (Intel Xeon 6980P)

SPECrate®2017_fp_peak = 1360

CPU2017 License: 3358

Test Sponsor: IEIT Systems Co., Ltd.

Tested by: IEIT Systems Co., Ltd.

Test Date: Oct-2024

Hardware Availability: Oct-2024

Software Availability: Jun-2024

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags



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Test Date: Oct-2024

Hardware Availability: Oct-2024

Software Availability: Jun-2024

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:

511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1) -fprofile-use=default.profdata(pass 2) -xCORE-AVX2 -flto -Ofast -ffast-math -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

526.blender_r: basepeak = yes

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Test Date: Oct-2024

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Hardware Availability: Oct-2024

Tested by: IEIT Systems Co., Ltd.

Software Availability: Jun-2024

Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-gopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/IEIT-Platform-Settings-intel-V1.2.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/IEIT-Platform-Settings-intel-V1.2.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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