SPEC CPU®2017 Floating Point Rate Result

Esconet Technologies Ltd.

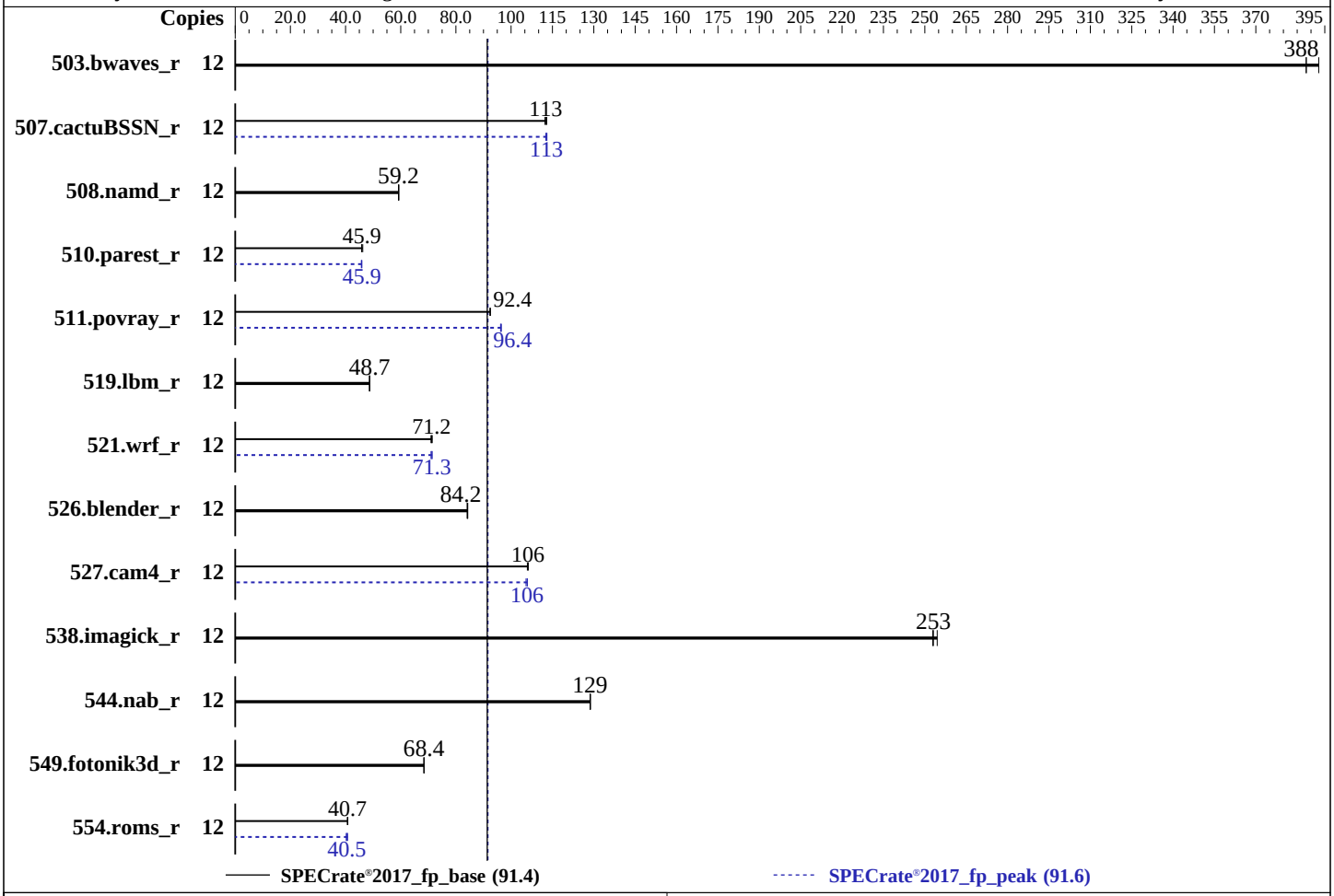
Hexadata HDR-RM2386212I Ver: RLX
(Intel Xeon E-2456)

SPECrate®2017_fp_base = 91.4

SPECrate®2017_fp_peak = 91.6

CPU2017 License: 6523
Test Sponsor: Esconet Technologies Ltd.
Tested by: Esconet Technologies Ltd.

Test Date: Jan-2025
Hardware Availability: May-2024
Software Availability: Jun-2024



Hardware

CPU Name:	Intel Xeon E-2456
Max MHz:	5100
Nominal:	3300
Enabled:	6 cores, 1 chip, 2 threads/core
Orderable:	1 chip
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	2 MB I+D on chip per core
L3:	18 MB I+D on chip per chip
Other:	None
Memory:	128 GB (4 x 32 GB 2Rx8 PC5-4800B-E, running at 4400)
Storage:	1 x 1920 GB SATA SSD
Other:	CPU Cooling: Air

Software

OS:	SUSE Linux Enterprise Server 15 SP6 6.4.0-150600.21-default
Compiler:	C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;
Parallel:	No
Firmware:	Version 1.19 released Jan-2024
File System:	btrfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	jemalloc memory allocator V5.0.1
Power Management:	OS set to prefer performance at the cost of additional power usage.

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	12	306	393	310	388	**310**	**388**	12	306	393	310	388	**310**	**388**
507.cactuBSSN_r	12	135	112	135	113	**135**	**113**	12	135	113	134	113	**135**	**113**
508.namd_r	12	193	59.2	**193**	**59.2**	192	59.3	12	193	59.2	**193**	**59.2**	192	59.3
510.parest_r	12	679	46.2	684	45.9	**684**	**45.9**	12	**684**	**45.9**	687	45.7	684	45.9
511.povray_r	12	**303**	**92.4**	303	92.5	303	92.4	12	**291**	**96.4**	291	96.2	291	96.5
519.lbm_r	12	259	48.8	**260**	**48.7**	260	48.7	12	259	48.8	**260**	**48.7**	260	48.7
521.wrf_r	12	**377**	**71.2**	379	71.0	376	71.4	12	379	71.0	377	71.4	**377**	**71.3**
526.blender_r	12	217	84.3	**217**	**84.2**	217	84.1	12	217	84.3	**217**	**84.2**	217	84.1
527.cam4_r	12	198	106	**198**	**106**	198	106	12	198	106	199	105	**198**	**106**
538.imagick_r	12	**118**	**253**	117	254	118	253	12	**118**	**253**	117	254	118	253
544.nab_r	12	157	129	157	129	**157**	**129**	12	157	129	157	129	**157**	**129**
549.fotonik3d_r	12	683	68.4	683	68.4	**683**	**68.4**	12	683	68.4	683	68.4	**683**	**68.4**
554.roms_r	12	469	40.7	**468**	**40.7**	467	40.8	12	**471**	**40.5**	468	40.7	474	40.2

SPECrate®2017_fp_base = 91.4

SPECrate®2017_fp_peak = 91.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
OS set to performance mode via cpupower frequency-set -g performance
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/root/cpu17/lib/intel64:/root/cpu17/je5.0.1-64"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>       /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

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General Notes (Continued)

```
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
```

Platform Notes

```
BIOS settings: Default

Sysinfo program /root/cpu17/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Fri Jan 17 18:25:05 2025

SUT (System Under Test) info as seen by some common utilities.

------------------------------------------------------------
Table of contents
------------------------------------------------------------
 1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. sysctl
15. /sys/kernel/mm/transparent_hugepage
16. /sys/kernel/mm/transparent_hugepage/khugepaged
17. OS release
18. Disk information
19. /sys/devices/virtual/dmi/id
20. dmidecode
21. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
  x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   18:25:05 up  4:33,  1 user,  load average: 8.95, 11.26, 11.70
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                13:54    4:27m  0.74s  0.01s sh
  reportable-ic2023.2.3-lin-core-avx2-rate-smt-on-20231121.sh

------------------------------------------------------------
3. Username
  From environment variable $USER:  root
```

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Platform Notes (Continued)

```
------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 512948
  max locked memory       (kbytes, -l) 8192
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 512948
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize=31
 login -- root
 -bash
 sh reportable-ic2023.2.3-lin-core-avx2-rate-smt-on-20231121.sh
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=12 -c
   ic2023.2.3-lin-core-avx2-rate-20231121.cfg --define smt-on --define cores=6 --define physicallogical
   --define no-numa --tune base,peak -o all --define drop_caches fprate
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=12 --configfile
   ic2023.2.3-lin-core-avx2-rate-20231121.cfg --define smt-on --define cores=6 --define physicallogical
   --define no-numa --tune base,peak --output_format all --define drop_caches --nopower --runmode rate --tune
   base:peak --size refrate fprate --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.003/templogs/preenv.fprate.003.0.log --lognum 003.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /root/cpu17

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : Intel(R) Xeon(R) E E-2456
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 183
    stepping        : 1
    microcode       : 0x122
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb bhi
    cpu cores       : 6
    siblings        : 12
    1 physical ids (chips)
    12 processors (hardware threads)
    physical id 0: core ids 0-5
    physical id 0: apicids 0-11
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.39.3:
  Architecture:                         x86_64
```

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Platform Notes (Continued)

```
  CPU op-mode(s):                  32-bit, 64-bit
  Address sizes:                   42 bits physical, 48 bits virtual
  Byte Order:                      Little Endian
  CPU(s):                          12
  On-line CPU(s) list:             0-11
  Vendor ID:                       GenuineIntel
  BIOS Vendor ID:                  Intel(R) Corporation
  Model name:                      Intel(R) Xeon(R) E E-2456
  BIOS Model name:                 Intel(R) Xeon(R) E E-2456 To Be Filled By O.E.M. CPU @ 4.5GHz
  BIOS CPU family:                 179
  CPU family:                      6
  Model:                           183
  Thread(s) per core:              2
  Core(s) per socket:              6
  Socket(s):                       1
  Stepping:                        1
  Frequency boost:                 enabled
  CPU(s) scaling MHz:              126%
  CPU max MHz:                     3301.0000
  CPU min MHz:                     800.0000
  BogoMIPS:                        6604.80
  Flags:                           fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                   pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
                                   pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
                                   nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
                                   pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
                                   xtpr pdcm sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
                                   xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb ssbd
                                   ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad
                                   fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed adx smap
                                   clflushopt clwb intel_pt sha_ni xsaveopt xsavec xgetbv1 xsaves
                                   split_lock_detect user_shstk avx_vnni dtherm ida arat pln pts hfi
                                   vnmi umip pku ospke waitpkg gfni vaes vpclmulqdq tme rdpid movdiri
                                   movdir64b fsrm md_clear serialize pconfig arch_lbr ibt flush_l1d
                                   arch_capabilities
  Virtualization:                  VT-x
  L1d cache:                       288 KiB (6 instances)
  L1i cache:                       192 KiB (6 instances)
  L2 cache:                        12 MiB (6 instances)
  L3 cache:                        18 MiB (1 instance)
  NUMA node(s):                    1
  NUMA node0 CPU(s):               0-11
  Vulnerability Gather data sampling:   Not affected
  Vulnerability Itlb multihit:          Not affected
  Vulnerability L1tf:                   Not affected
  Vulnerability Mds:                    Not affected
  Vulnerability Meltdown:               Not affected
  Vulnerability Mmio stale data:        Not affected
  Vulnerability Reg file data sampling: Not affected
  Vulnerability Retbleed:               Not affected
  Vulnerability Spec rstack overflow:   Not affected
  Vulnerability Spec store bypass:      Mitigation; Speculative Store Bypass disabled via prctl
  Vulnerability Spectre v1:             Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:             Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;
                                   PBRSB-eIBRS SW sequence; BHI BHI_DIS_S
  Vulnerability Srbds:                  Not affected
  Vulnerability Tsx async abort:        Not affected

 From lscpu --cache:
      NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
```

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Platform Notes (Continued)

```
     L1d       48K     288K   12 Data            1    64        1             64
     L1i       32K     192K    8 Instruction     1    64        1             64
     L2         2M      12M   16 Unified         2  2048        1             64
     L3        18M      18M    9 Unified         3 32768        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0-11
  node 0 size: 128262 MB
  node 0 free: 116390 MB
  node distances:
  node   0
    0:  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       131340880 kB

------------------------------------------------------------
10. who -r
  run-level 3 Jan 17 13:51

------------------------------------------------------------
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          apparmor auditd cron firewalld getty@ irqbalance issue-generator kbdsettings kdump
                   kdump-early kdump-notify postfix purge-kernels rollback sshd systemd-pstore wicked
                   wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
  enabled-runtime  systemd-remount-fs
  disabled         boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell ebtables fsidd
                   grub2-once haveged issue-add-ssh-keys kexec-load lunmask nfs nfs-blkmap rpcbind
                   rpmconfigcheck serial-getty@ systemd-boot-check-no-failures systemd-confext
                   systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd
  indirect         systemd-userdbd wickedd

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
  root=UUID=11e13a64-e1cd-43bc-b0ff-8978490b3ae4
  splash=silent
  resume=/dev/disk/by-uuid/67bbde49-0a32-420a-8ae3-edf6c17b3b2b
  mitigations=auto
  quiet
  security=apparmor
  crashkernel=342M,high
  crashkernel=72M,low

------------------------------------------------------------
14. sysctl
  kernel.numa_balancing               0
  kernel.randomize_va_space           2
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
```

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Platform Notes (Continued)

```
  vm.dirty_background_ratio          10
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                     20
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                      60
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                0

------------------------------------------------------------
15. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvise [madvise] never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
17. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP6

------------------------------------------------------------
18. Disk information
SPEC is set to: /root/cpu17
  Filesystem     Type   Size  Used Avail Use% Mounted on
  /dev/sda3      btrfs  1.7T   17G  1.7T   1% /root

------------------------------------------------------------
19. /sys/devices/virtual/dmi/id
    Vendor:         HEXADATA
    Product:        HDR-RM2386212I
    Serial:         H5FSYR003208

------------------------------------------------------------
20. dmidecode
  Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    4x V-Color Technology Inc TE532G48D840 32 GB 2 rank 4800, configured at 4400
```

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Platform Notes (Continued)

```
------------------------------------------------------------
21. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:       American Megatrends International, LLC.
   BIOS Version:      1.19
   BIOS Date:         01/05/2024
   BIOS Revision:     5.27
```

Compiler Version Notes

```
==============================================================================================================
C               | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++             | 508.namd_r(base, peak) 510.parest_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

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Base Optimization Flags (Continued)

C++ benchmarks:
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using Fortran, C, and C++:
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

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Peak Compiler Invocation (Continued)

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

(Continued on next page)

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:

511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2 -flto
-Ofast -ffast-math -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html
http://www.spec.org/cpu2017/flags/Hexadata-Platform-Flags-Intel-rev1.6.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml
http://www.spec.org/cpu2017/flags/Hexadata-Platform-Flags-Intel-rev1.6.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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