



# SPEC CPU®2017 Floating Point Speed Result

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## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### ProLiant Compute DL340 Gen12

(2.20 GHz, Intel Xeon 6760P)

SPECspeed®2017\_fp\_base = 320

SPECspeed®2017\_fp\_peak = 320

CPU2017 License: 3

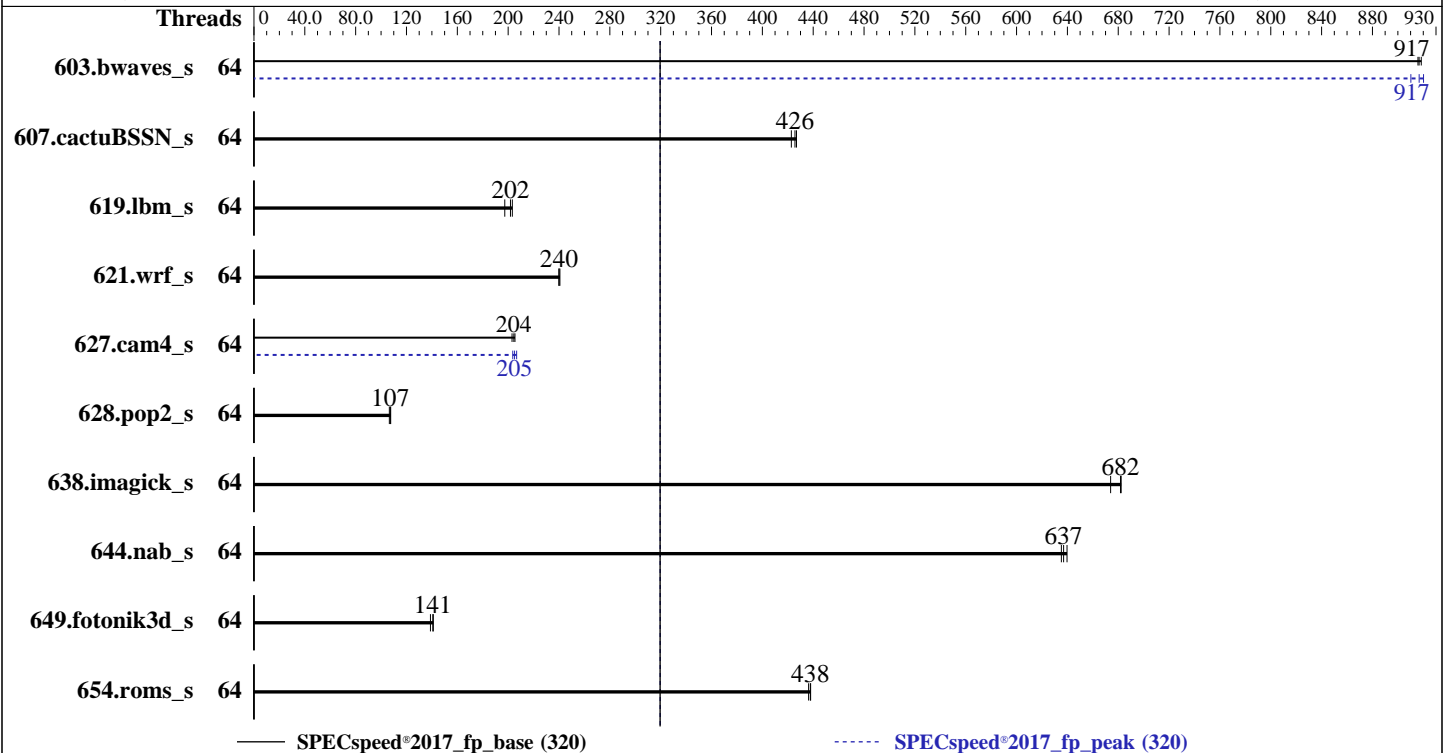
Test Sponsor: HPE

Tested by: HPE

Test Date: Mar-2025

Hardware Availability: Mar-2025

Software Availability: Jun-2024



### Hardware

CPU Name: Intel Xeon 6760P  
 Max MHz: 3800  
 Nominal: 2200  
 Enabled: 64 cores, 1 chip  
 Orderable: 1 chip  
 Cache L1: 64 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 320 MB I+D on chip per chip  
 Other: None  
 Memory: 256 GB (8 x 32 GB 2Rx8 PC5-6400B-R)  
 Storage: 1 x 1.2 TB NVMe SSD  
 Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP6  
 Kernel 6.4.0-150600.21-default  
 Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;  
 Parallel: Yes  
 Firmware: HPE BIOS Version v1.20 02/14/2025 released Feb-2025  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	64	64.2	918	<b>64.4</b>	<b>917</b>	64.4	916	64	64.1	920	<b>64.4</b>	<b>917</b>	64.8	910
607.cactuBSSN_s	64	39.1	427	<b>39.2</b>	<b>426</b>	39.4	423	64	39.1	427	<b>39.2</b>	<b>426</b>	39.4	423
619.lbm_s	64	<b>26.0</b>	<b>202</b>	25.8	203	26.5	197	64	<b>26.0</b>	<b>202</b>	25.8	203	26.5	197
621.wrf_s	64	54.9	241	55.1	240	<b>55.1</b>	<b>240</b>	64	54.9	241	55.1	240	<b>55.1</b>	<b>240</b>
627.cam4_s	64	43.6	203	43.2	205	<b>43.3</b>	<b>204</b>	64	43.5	204	<b>43.2</b>	<b>205</b>	42.9	207
628.pop2_s	64	111	107	110	108	<b>111</b>	<b>107</b>	64	111	107	110	108	<b>111</b>	<b>107</b>
638.imagick_s	64	<b>21.2</b>	<b>682</b>	21.4	674	21.1	682	64	<b>21.2</b>	<b>682</b>	21.4	674	21.1	682
644.nab_s	64	<b>27.4</b>	<b>637</b>	27.3	640	27.5	635	64	<b>27.4</b>	<b>637</b>	27.3	640	27.5	635
649.fotonik3d_s	64	65.7	139	64.6	141	<b>64.8</b>	<b>141</b>	64	65.7	139	64.6	141	<b>64.8</b>	<b>141</b>
654.roms_s	64	36.1	436	35.9	438	<b>36.0</b>	<b>438</b>	64	36.1	436	35.9	438	<b>36.0</b>	<b>438</b>

SPECspeed®2017\_fp\_base = 320

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,compact"  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"  
MALLOC\_CONF = "retain:true"  
OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
memory using Redhat Enterprise Linux 8.0  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)  
is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)  
is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)  
is mitigated in the system as tested and documented.  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



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## Platform Notes

### BIOS Configuration:

Workload Profile set to General Peak Frequency Compute  
Memory Patrol Scrubbing set to Disabled  
Thermal Configuration set to Maximum Cooling  
Enhanced Processor Performance Profile set to Aggressive  
Sub-NUMA Clustering (SNC) set to Enabled  
Last Level Cache (LLC) Prefetch set to Enabled  
Intel Hyper-Threading set to Disabled  
Minimum Processor Idle Power Core C-State set to C6 as ACPI C3 State  
Workload Profile set to Custom  
Minimum Processor Idle Power Package C-State set to No Package State

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Sun Mar 16 20:26:02 2025

SUT (System Under Test) info as seen by some common utilities.

### Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

```
1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
20:26:02 up 0 min, 3 users, load average: 0.57, 0.21, 0.08
USER      TTY      FROM          LOGIN@   IDLE   JCPU   PCPU   WHAT
```

```
3. Username
From environment variable $USER: root
```

(Continued on next page)



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## Platform Notes (Continued)

```

4. ulimit -a
   core file size          (blocks, -c) unlimited
   data seg size          (kbytes, -d) unlimited
   scheduling priority     (-e) 0
   file size              (blocks, -f) unlimited
   pending signals        (-i) 1030690
   max locked memory      (kbytes, -l) 8192
   max memory size        (kbytes, -m) unlimited
   open files             (-n) 1024
   pipe size              (512 bytes, -p) 8
   POSIX message queues   (bytes, -q) 819200
   real-time priority     (-r) 0
   stack size            (kbytes, -s) unlimited
   cpu time              (seconds, -t) unlimited
   max user processes     (-u) 1030690
   virtual memory         (kbytes, -v) unlimited
   file locks            (-x) unlimited

```

```

5. sysinfo process ancestry
   /usr/lib/systemd/systemd --switched-root --system --deserialize=31
   sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
   sshd: root [priv]
   sshd: root@notty
   bash -c cd $SPEC/ && $SPEC/fpspeedavx512off.sh
   runcpu --nobuild --action validate --define default-platform-flags -c
     ic2024.1-lin-core-avx512-speed-20240308.cfg --define cores=64 --tune base,peak -o all --define drop_caches
     fpspeed
   runcpu --nobuild --action validate --define default-platform-flags --configfile
     ic2024.1-lin-core-avx512-speed-20240308.cfg --define cores=64 --tune base,peak --output_format all
     --define drop_caches --nopower --runmode speed --tune base:peak --size refspeed fpspeed --nopreenv
     --note-preenv --logfile $SPEC/tmp/CPU2017.001/templogs/preenv.fpspeed.001.0.log --lognum 001.0
     --from_runcpu 2
   specperl $SPEC/bin/sysinfo
   $SPEC = /home/cpu2017

```

```

6. /proc/cpuinfo
   model name      : Intel(R) Xeon(R) 6760P
   vendor_id      : GenuineIntel
   cpu family     : 6
   model          : 173
   stepping       : 1
   microcode      : 0x1000380
   bugs           : spectre_v1 spectre_v2 spec_store_bypass swaps bhi
   cpu cores      : 64
   siblings       : 64
   1 physical ids (chips)
   64 processors (hardware threads)
   physical id 0: core ids 0-31,64-95
   physical id 0: apicids
   0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62,128,130,132,13
   4,136,138,140,142,144,146,148,150,152,154,156,158,160,162,164,166,168,170,172,174,176,178,180,182,184,186
   ,188,190
   Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
   virtualized systems. Use the above data carefully.

```

7. lscpu

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## Platform Notes (Continued)

From lscpu from util-linux 2.39.3:

```

Architecture:                x86_64
CPU op-mode(s):              32-bit, 64-bit
Address sizes:                46 bits physical, 57 bits virtual
Byte Order:                   Little Endian
CPU(s):                       64
On-line CPU(s) list:         0-63
Vendor ID:                    GenuineIntel
BIOS Vendor ID:              Intel(R) Corporation
Model name:                   Intel(R) Xeon(R) 6760P
BIOS Model name:             Intel(R) Xeon(R) 6760P  CPU @ 2.2GHz
BIOS CPU family:             179
CPU family:                   6
Model:                        173
Thread(s) per core:          1
Core(s) per socket:          64
Socket(s):                    1
Stepping:                     1
BogoMIPS:                     4400.00
Flags:                         fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                               pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
                               pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
                               nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
                               pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
                               xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
                               tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                               3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2
                               ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
                               vpid ept_ad fsgsbase tsc_adjust bml hle avx2 smep bmi2 erms invpcid
                               rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma cflushopt
                               clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                               xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                               split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
                               arat pln pts hfi vnni avx512vbmi umip pku ospke waitpkg avx512_vbmi2
                               gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpoperndq
                               la57 rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm
                               md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16
                               amx_tile amx_int8 flush_lld arch_capabilities
Virtualization:              VT-x
L1d cache:                   3 MiB (64 instances)
L1i cache:                   4 MiB (64 instances)
L2 cache:                     128 MiB (64 instances)
L3 cache:                     320 MiB (1 instance)
NUMA node(s):                2
NUMA node0 CPU(s):           0-31
NUMA node1 CPU(s):           32-63
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit:  Not affected
Vulnerability L1tf:          Not affected
Vulnerability Mds:           Not affected
Vulnerability Meltdown:     Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed:     Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:    Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:    Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;
                               PBRSE-eIBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds:         Not affected

```

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## Platform Notes (Continued)

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	3M	12	Data	1	64	1	64
L1i	64K	4M	16	Instruction	1	64	1	64
L2	2M	128M	16	Unified	2	2048	1	64
L3	320M	320M	16	Unified	3	327680	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0-31

node 0 size: 128742 MB

node 0 free: 127834 MB

node 1 cpus: 32-63

node 1 size: 128954 MB

node 1 free: 128277 MB

node distances:

node 0 1

0: 10 12

1: 12 10

9. /proc/meminfo

MemTotal: 263882212 kB

10. who -r

run-level 3 Mar 16 20:25

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

Default Target Status

multi-user running

12. Services, from systemctl list-unit-files

STATE UNIT FILES

enabled apparmor auditd cron getty@ irqbalance issue-generator kbdsettings nvme-fc-boot-connections  
nvme-fc-autoconnect postfix purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4  
wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny

enabled-runtime systemd-remount-fs

disabled boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell grub2-once

haveged hwloc-dump-hwdata issue-add-ssh-keys kexec-load rpmconfigcheck serial-getty@  
systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysext  
systemd-time-wait-sync systemd-timesyncd

indirect systemd-userdbd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT\_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default

root=UUID=2a556383-22da-44ff-afaf-35fc3a69bfa6

splash=silent

resume=/dev/disk/by-uuid/288d2489-1f86-416e-b1e3-5c25ecb851eb

quiet

security=apparmor

mitigations=auto

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## Platform Notes (Continued)

-----  
14. cpupower frequency-info

```
analyzing CPU 54:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes
```

-----  
15. sysctl

```
kernel.numa_balancing          1
kernel.randomize_va_space      2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                  20
vm.dirty_writeback_centisecs    500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0
```

-----  
16. /sys/kernel/mm/transparent\_hugepage

```
defrag          always defer+madvise [madvise] never
enabled         [always] madvise never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force
```

-----  
17. /sys/kernel/mm/transparent\_hugepage/khugepaged

```
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
```

-----  
18. OS release

```
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
```

-----  
19. Disk information

```
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p3 xfs 947G 134G 814G 15% /home
```

-----  
20. /sys/devices/virtual/dmi/id

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## Platform Notes (Continued)

Vendor: HPE  
Product: HPE ProLiant Compute DL340 Gen12  
Product Family: ProLiant  
Serial: 5MXSH46K47

### 21. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

8x Hynix HMC88AHBRA471N 32 GB 2 rank 6400

### 22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE  
BIOS Version: 1.20  
BIOS Date: 02/14/2025  
BIOS Revision: 1.20  
Firmware Revision: 1.11

## Compiler Version Notes

C | 619.lbm\_s(base, peak) 638.imagick\_s(base, peak) 644.nab\_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C++, C, Fortran | 607.cactuBSSN\_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak) 654.roms\_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak) 628.pop2\_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
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## Base Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64

607.cactuBSSN\_s: -DSPEC\_LP64

619.lbm\_s: -DSPEC\_LP64

621.wrf\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian

627.cam4\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG

628.pop2\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian

-assume byterecl

638.imagick\_s: -DSPEC\_LP64

644.nab\_s: -DSPEC\_LP64

649.fotonik3d\_s: -DSPEC\_LP64

654.roms\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math

-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp

-DSPEC\_OPENMP -Wno-implicit-int -L/usr/local/jemalloc64-5.0.1/lib

-ljemalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -DSPEC\_OPENMP -xCORE-AVX512 -Ofast

-ffast-math -flto -mfpmath=sse -funroll-loops

-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs

-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

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**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant Compute DL340 Gen12**

(2.20 GHz, Intel Xeon 6760P)

SPECspeed®2017\_fp\_base = 320

SPECspeed®2017\_fp\_peak = 320

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Mar-2025  
**Hardware Availability:** Mar-2025  
**Software Availability:** Jun-2024

## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
-DSPEC_OPENMP -Wno-implicit-int -nostandard-realloc-lhs  
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int  
-nostandard-realloc-lhs -align array32byte -auto  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

## Peak Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

619.lbm\_s: basepeak = yes

638.imagick\_s: basepeak = yes

644.nab\_s: basepeak = yes

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

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**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Mar-2025

**Hardware Availability:** Mar-2025

**Software Availability:** Jun-2024

## Peak Optimization Flags (Continued)

Fortran benchmarks:

```
603.bwaves_s: -w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc
```

649.fotonik3d\_s: basepeak = yes

654.roms\_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf\_s: basepeak = yes

```
627.cam4_s: -w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc
```

628.pop2\_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-GNR-rev1.1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-GNR-rev1.1.xml>

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