



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## IEIT Systems Co., Ltd.

SPECrate®2017\_fp\_base = 2450

### meta brain NF5180G8 (AMD EPYC 9755)

SPECrate®2017\_fp\_peak = 2660

CPU2017 License: 3358

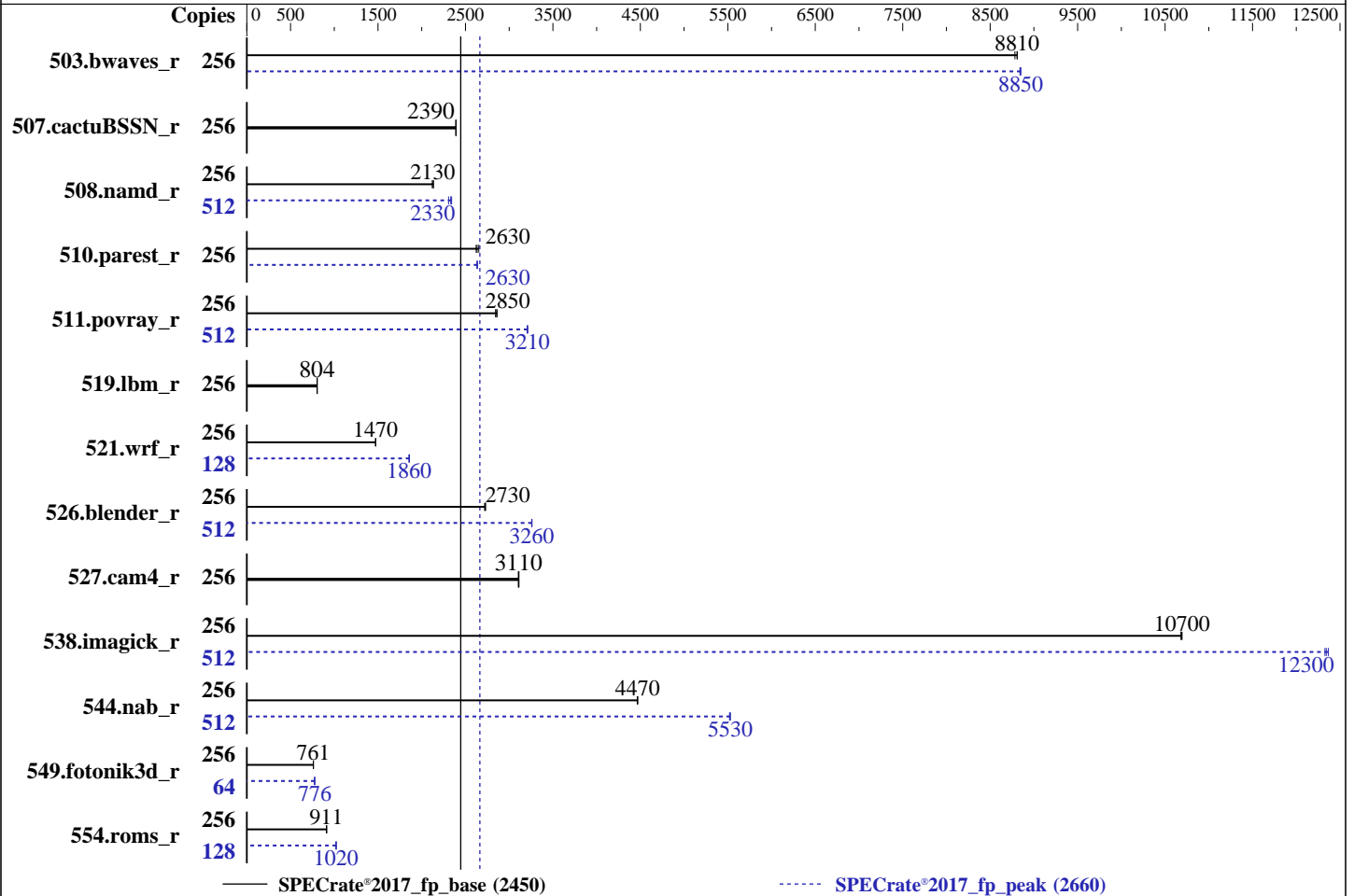
Test Sponsor: IEIT Systems Co., Ltd.

Tested by: IEIT Systems Co., Ltd.

Test Date: Mar-2025

Hardware Availability: Feb-2025

Software Availability: Oct-2024



### Hardware

CPU Name: AMD EPYC 9755  
 Max MHz: 4100  
 Nominal: 2700  
 Enabled: 256 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 512 MB I+D on chip per chip, 32 MB shared / 8 cores  
 Other: None  
 Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R)  
 Storage: 1 x 960 GB NVME SSD  
 Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP6 6.4.0-150600.21-default  
 Compiler: C/C++/Fortran: Version 5.0.0 of AOCC  
 Parallel: No  
 Firmware: Version 01.08.00 released Feb-2025  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	256	292	8780	<b><u>291</u></b>	<b><u>8810</u></b>	291	8810	256	290	8840	290	8850	<b><u>290</u></b>	<b><u>8850</u></b>
507.cactuBSSN_r	256	135	2390	136	2390	<b><u>135</u></b>	<b><u>2390</u></b>	256	135	2390	136	2390	<b><u>135</u></b>	<b><u>2390</u></b>
508.namd_r	256	<b><u>114</u></b>	<b><u>2130</u></b>	115	2120	114	2130	512	<b><u>209</u></b>	<b><u>2330</u></b>	211	2310	208	2340
510.parest_r	256	<b><u>254</u></b>	<b><u>2630</u></b>	252	2650	255	2620	256	254	2630	<b><u>254</u></b>	<b><u>2630</u></b>	254	2640
511.povray_r	256	210	2840	209	2860	<b><u>210</u></b>	<b><u>2850</u></b>	512	<b><u>372</u></b>	<b><u>3210</u></b>	372	3210	373	3210
519.lbm_r	256	336	804	336	804	<b><u>336</u></b>	<b><u>804</u></b>	256	336	804	336	804	<b><u>336</u></b>	<b><u>804</u></b>
521.wrf_r	256	389	1470	390	1470	<b><u>389</u></b>	<b><u>1470</u></b>	128	<b><u>154</u></b>	<b><u>1860</u></b>	155	1850	154	1860
526.blender_r	256	<b><u>143</u></b>	<b><u>2730</u></b>	143	2720	143	2730	512	<b><u>239</u></b>	<b><u>3260</u></b>	239	3260	239	3260
527.cam4_r	256	144	3110	144	3110	<b><u>144</u></b>	<b><u>3110</u></b>	256	144	3110	144	3110	<b><u>144</u></b>	<b><u>3110</u></b>
538.imagick_r	256	<b><u>59.5</u></b>	<b><u>10700</u></b>	59.6	10700	59.5	10700	512	103	12300	<b><u>103</u></b>	<b><u>12300</u></b>	103	12400
544.nab_r	256	96.5	4460	<b><u>96.4</u></b>	<b><u>4470</u></b>	96.4	4470	512	<b><u>156</u></b>	<b><u>5530</u></b>	156	5520	156	5530
549.fotonik3d_r	256	<b><u>1311</u></b>	<b><u>761</u></b>	1310	761	1312	761	64	321	776	<b><u>321</u></b>	<b><u>776</u></b>	322	774
554.roms_r	256	<b><u>447</u></b>	<b><u>911</u></b>	445	913	447	911	128	<b><u>199</u></b>	<b><u>1020</u></b>	199	1020	199	1020

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.



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## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
"/home/CPU2017/amd_rate_aocc500_znver5_A_lib/lib:/home/CPU2017/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOCONF = "retain:true"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:  
SVM Mode set to disable  
DRAM Scrub time set to disable  
NUMA nodes per socket set to NPS4  
Determinism Slider set to Power  
cTDP set to 500  
Package Power Limit set to 500  
APBDIS set to 1  
Memory Speed set to 6400

Sysinfo program /home/CPU2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Wed Mar 5 11:25:00 2025

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. systemctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release

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## Platform Notes (Continued)

20. Disk information

21. /sys/devices/virtual/dmi/id

22. dmidecode

23. BIOS

1. `uname -a`

```
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux
```

2. `w`

```
11:25:00 up 5:08, 1 user, load average: 246.93, 439.16, 477.77
USER      TTY      FROM          LOGIN@      IDLE        JCPU   PCPU WHAT
root      tty1    -             06:17      5:04m     1.27s  0.33s /bin/bash ./amd_rate_aocc500_znver5_A1.sh
```

3. Username

From environment variable \$USER: root

4. `ulimit -a`

```
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 6187450
max locked memory       (kbytes, -l) 2097152
max memory size         (kbytes, -m) unlimited
open files              (-n) 65535
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes     (-u) 6187450
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

5. `sysinfo process ancestry`

```
/usr/lib/systemd/systemd --switched-root --system --deserialize=31
login -- root
-bash
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/CPU2017
```

6. `/proc/cpuinfo`

```
model name      : AMD EPYC 9755 128-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 26
model          : 2
```

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## Platform Notes (Continued)

```

stepping          : 1
microcode         : 0xb00211e
bugs              : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size         : 192 4K pages
cpu cores        : 128
siblings         : 256
2 physical ids (chips)
512 processors (hardware threads)
physical id 0: core ids 0-127
physical id 1: core ids 0-127
physical id 0: apicids 0-255
physical id 1: apicids 256-511

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.39.3:

```

Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:     52 bits physical, 57 bits virtual
Byte Order:       Little Endian
CPU(s):           512
On-line CPU(s) list: 0-511
Vendor ID:        AuthenticAMD
BIOS Vendor ID:   Advanced Micro Devices, Inc.
Model name:       AMD EPYC 9755 128-Core Processor
BIOS Model name:  AMD EPYC 9755 128-Core Processor
BIOS CPU family:  107
CPU family:       26
Model:           2
Thread(s) per core: 2
Core(s) per socket: 128
Socket(s):       2
Stepping:        1
Frequency boost:  enabled
CPU(s) scaling MHz: 65%
CPU max MHz:     4121.1909
CPU min MHz:     1500.0000
BogoMIPS:        5392.18
Flags:            fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                 pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                 rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                 extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                 sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
                 cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                 osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                 perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
                 ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmil avx2
                 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                 avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                 xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                 cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
                 xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock
                 nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
                 pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi
                 avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
                 avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
                 movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect

```

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### Platform Notes (Continued)

```

flush_lld debug_swap
AMD-V
Virtualization:
L1d cache:      12 MiB (256 instances)
L1i cache:      8 MiB (256 instances)
L2 cache:       256 MiB (256 instances)
L3 cache:       1 GiB (32 instances)
NUMA node(s):   8
NUMA node0 CPU(s): 0-31,256-287
NUMA node1 CPU(s): 32-63,288-319
NUMA node2 CPU(s): 64-95,320-351
NUMA node3 CPU(s): 96-127,352-383
NUMA node4 CPU(s): 128-159,384-415
NUMA node5 CPU(s): 160-191,416-447
NUMA node6 CPU(s): 192-223,448-479
NUMA node7 CPU(s): 224-255,480-511
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit:      Not affected
Vulnerability L1tf:                Not affected
Vulnerability Mds:                 Not affected
Vulnerability Meltdown:           Not affected
Vulnerability Mmio stale data:     Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed:           Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass:   Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:         Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:         Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
always-on; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected
Vulnerability Srbds:              Not affected
Vulnerability Tsx async abort:     Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	12M	12	Data	1	64	1	64
L1i	32K	8M	8	Instruction	1	64	1	64
L2	1M	256M	16	Unified	2	1024	1	64
L3	32M	1G	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0-31,256-287
node 0 size: 192864 MB
node 0 free: 191101 MB
node 1 cpus: 32-63,288-319
node 1 size: 193519 MB
node 1 free: 191840 MB
node 2 cpus: 64-95,320-351
node 2 size: 193519 MB
node 2 free: 191835 MB
node 3 cpus: 96-127,352-383
node 3 size: 193519 MB
node 3 free: 191876 MB
node 4 cpus: 128-159,384-415
node 4 size: 193519 MB
node 4 free: 191822 MB
node 5 cpus: 160-191,416-447
node 5 size: 193519 MB
node 5 free: 191837 MB

```

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```

node 6 cpus: 192-223,448-479
node 6 size: 193519 MB
node 6 free: 191827 MB
node 7 cpus: 224-255,480-511
node 7 size: 192908 MB
node 7 free: 191216 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10 12 12 12 32 32 32 32
1:  12 10 12 12 32 32 32 32
2:  12 12 10 12 32 32 32 32
3:  12 12 12 10 32 32 32 32
4:  32 32 32 32 10 12 12 12
5:  32 32 32 32 12 10 12 12
6:  32 32 32 32 12 12 10 12
7:  32 32 32 32 12 12 12 10

```

```

-----
9. /proc/meminfo
   MemTotal:      1584012712 kB

```

```

-----
10. who -r
    run-level 3 Mar 5 06:17

```

```

-----
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
    Default Target Status
    multi-user      running

```

```

-----
12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled apparmor auditd cron firewalld getty@ irqbalance issue-generator kbdsettings kdump
        kdump-early kdump-notify nvme-fc-boot-connections nvme-fc-autoconnect postfix purge-kernels
        rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6
        wickedd-nanny
enabled-runtime systemd-remount-fs
disabled boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell ebttables
        exchange-bmc-os-info fsidd grub2-once haveged hwloc-dump-hwdata ipmievd issue-add-ssh-keys
        kexec-load nfs nfs-blkmap rpcbind rpmconfigcheck serial-getty@
        systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysexit
        systemd-time-wait-sync systemd-timesyncd tuned
generated jexec
indirect  systemd-userdbd wickedd

```

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=57526b2a-7733-4c34-8e82-bd01bcfc67e9
splash=silent
mitigations=auto
quiet
security=apparmor
crashkernel=383M,high
crashkernel=72M,low

```

```

-----
14. cpupower frequency-info
    analyzing CPU 14:

```

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### Platform Notes (Continued)

current policy: frequency should be within 1.50 GHz and 2.70 GHz.  
The governor "performance" may decide which speed to use within this range.

boost state support:

Supported: yes

Active: yes

-----  
15. tunedadm active

It seems that tuned daemon is not running, preset profile is not activated.  
Preset profile: throughput-performance

-----  
16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	1
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	1

-----  
17. /sys/kernel/mm/transparent\_hugepage

defrag	[always] defer defer+madvise madvise never
enabled	[always] madvise never
hpage_pmd_size	2097152
shmem_enabled	always within_size advise [never] deny force

-----  
18. /sys/kernel/mm/transparent\_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

-----  
19. OS release

From /etc/\*-release /etc/\*-version  
os-release SUSE Linux Enterprise Server 15 SP6

-----  
20. Disk information

SPEC is set to: /home/CPU2017  
Filesystem Type Size Used Avail Use% Mounted on

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## Platform Notes (Continued)

/dev/nvme0n1p3 xfs 854G 60G 795G 7% /home

```

-----
21. /sys/devices/virtual/dmi/id
Vendor:      IEIT SYSTEMS
Product:     NF5180-A8-A0-R0-00
Product Family: Not specified
Serial:      000000000
-----

```

```

-----
22. dmidecode
Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  24x Samsung M321R8GA0PB2-CCPEC 64 GB 2 rank 6400
-----

```

```

-----
23. BIOS
(This section combines info from /sys/devices and dmidecode.)
  BIOS Vendor:   American Megatrends International, LLC.
  BIOS Version:  01.08.00
  BIOS Date:     02/08/2025
-----

```

## Compiler Version Notes

```

=====
C          | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
-----

```

```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----

```

```

=====
C++        | 508.namd_r(base, peak) 510.parest_r(base, peak)
-----

```

```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----

```

```

=====
C++, C     | 511.povray_r(base, peak) 526.blender_r(base, peak)
-----

```

```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----

```

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meta brain NF5180G8 (AMD EPYC 9755)

SPECrate®2017\_fp\_peak = 2660

CPU2017 License: 3358

Test Sponsor: IEIT Systems Co., Ltd.

Tested by: IEIT Systems Co., Ltd.

Test Date: Mar-2025

Hardware Availability: Feb-2025

Software Availability: Oct-2024

## Compiler Version Notes (Continued)

-----  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)  
-----

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----

-----  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)  
-----

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----

-----  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)  
-----

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

(Continued on next page)



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Test Date: Mar-2025

Hardware Availability: Feb-2025

Software Availability: Oct-2024

## Base Compiler Invocation (Continued)

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
 507.cactuBSSN\_r: -DSPEC\_LP64  
 508.namd\_r: -DSPEC\_LP64  
 510.parest\_r: -DSPEC\_LP64  
 511.povray\_r: -DSPEC\_LP64  
 519.lbm\_r: -DSPEC\_LP64  
 521.wrf\_r: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
 526.blender\_r: -funsigned-char -DSPEC\_LP64  
 527.cam4\_r: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
 538.imagick\_r: -DSPEC\_LP64  
 544.nab\_r: -DSPEC\_LP64  
 549.fotonik3d\_r: -DSPEC\_LP64  
 554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
 -Wl,-mllvm -Wl,-reduce-array-computations=3  
 -Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3  
 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto  
 -fstruct-layout=7 -mllvm -unroll-threshold=50  
 -mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
 -mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc  
 -lflang -ldl

C++ benchmarks:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
 -Wl,-mllvm -Wl,-reduce-array-computations=3  
 -Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner  
 -O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
 -mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000  
 -mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc  
 -lflang -ldl

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Hardware Availability: Feb-2025

Software Availability: Oct-2024

## Base Optimization Flags (Continued)

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
```

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Tested by: IEIT Systems Co., Ltd.

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Hardware Availability: Feb-2025

Software Availability: Oct-2024

## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions  
-lamdlibm -lamdalloc -lflang -ldl

## Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

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Hardware Availability: Feb-2025

Software Availability: Oct-2024

## Peak Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

538.imagick\_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdalloc -ldl

544.nab\_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand  
-fenable-aggressive-gather -Ofast -march=znver5  
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7  
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdalloc -ldl

C++ benchmarks:

508.namd\_r: -m64 -std=c++14  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-mllvm -unroll-threshold=100  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdalloc -ldl

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## Peak Optimization Flags (Continued)

```
510.parest_r: -m64 -std=c++14 -flto -Wl,-mllvm -Wl,-suppress-fmas
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

Fortran benchmarks:

```
503.bwaves_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm
-lamdalloc -ldl -lflang
```

```
549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdalloc -ldl -lflang
```

554.roms\_r: Same as 503.bwaves\_r

Benchmarks using both Fortran and C:

```
521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang
```

527.cam4\_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false
```

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Software Availability: Oct-2024

## Peak Optimization Flags (Continued)

511.povray\_r (continued):

```
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm
-lamdalloc -ldl
```

526.blender\_r: -m64 -std=c++14

```
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100 -lamdlibm -lamdalloc -ldl
```

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes

## Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument





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**Test Sponsor:** IEIT Systems Co., Ltd.

**Hardware Availability:** Feb-2025

**Tested by:** IEIT Systems Co., Ltd.

**Software Availability:** Oct-2024

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.00.html>

<http://www.spec.org/cpu2017/flags/IEIT-Platform-Settings-amd-V1.1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.00.xml>

<http://www.spec.org/cpu2017/flags/IEIT-Platform-Settings-amd-V1.1.xml>

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