



SPEC CPU®2017 Floating Point Rate Result

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xFusion

FusionServer 2158H V8
(AMD EPYC 9755)

SPECrate®2017_fp_base = 1210

SPECrate®2017_fp_peak = 1310

CPU2017 License: 6488

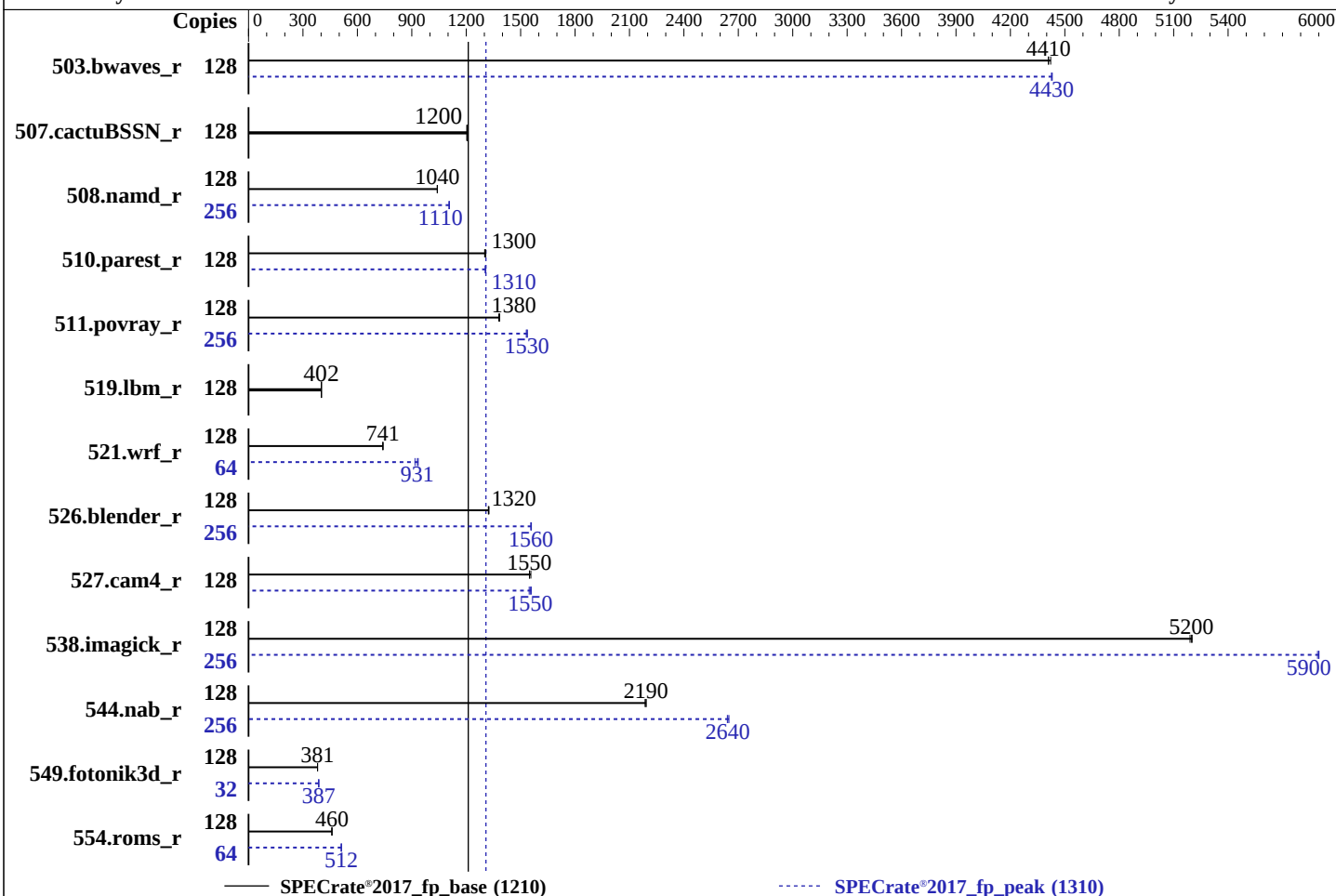
Test Sponsor: xFusion

Tested by: xFusion

Test Date: Jun-2025

Hardware Availability: Apr-2025

Software Availability: Oct-2024



Hardware

CPU Name: AMD EPYC 9755
Max MHz: 4100
Nominal: 2700
Enabled: 128 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 512 MB I+D on chip per chip, 32 MB shared / 8 cores
Other: None
Memory: 768 GB (12 x 64 GB 2Rx4 PC5-6400B-R)
Storage: 1 x 1.6 TB PCIe NVMe SSD
Other: CPU Cooling: Air

Software

OS: Ubuntu 24.04 LTS
6.8.0-38-generic
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: No
Firmware: Version 00.13.02.12 released May-2025
File System: ext4
System State: Run level 5 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	128	<u>291</u>	<u>4410</u>	291	4410	290	4420	128	290	4430	<u>290</u>	<u>4430</u>	290	4430
507.cactuBSSN_r	128	135	1200	<u>135</u>	<u>1200</u>	134	1210	128	135	1200	<u>135</u>	<u>1200</u>	134	1210
508.namd_r	128	117	1040	117	1040	<u>117</u>	<u>1040</u>	256	220	1110	<u>220</u>	<u>1110</u>	220	1110
510.parest_r	128	<u>257</u>	<u>1300</u>	257	1300	256	1310	128	256	1310	257	1300	<u>256</u>	<u>1310</u>
511.povray_r	128	216	1380	217	1380	<u>216</u>	<u>1380</u>	256	<u>389</u>	<u>1530</u>	389	1540	390	1530
519.lbm_r	128	336	402	336	402	<u>336</u>	<u>402</u>	128	336	402	336	402	<u>336</u>	<u>402</u>
521.wrf_r	128	387	742	<u>387</u>	<u>741</u>	387	740	64	<u>154</u>	<u>931</u>	156	918	153	936
526.blender_r	128	147	1330	<u>147</u>	<u>1320</u>	147	1320	256	251	1560	250	1560	<u>250</u>	<u>1560</u>
527.cam4_r	128	<u>144</u>	<u>1550</u>	145	1550	144	1560	128	144	1560	<u>144</u>	<u>1550</u>	145	1550
538.imagick_r	128	61.2	5200	<u>61.2</u>	<u>5200</u>	61.3	5190	256	<u>108</u>	<u>5900</u>	108	5890	108	5900
544.nab_r	128	98.5	2190	98.3	2190	<u>98.3</u>	<u>2190</u>	256	163	2640	163	2650	<u>163</u>	<u>2640</u>
549.fotonik3d_r	128	1311	381	1312	380	<u>1311</u>	<u>381</u>	32	322	387	<u>322</u>	<u>387</u>	323	387
554.roms_r	128	442	460	<u>442</u>	<u>460</u>	443	459	64	199	511	199	512	<u>199</u>	<u>512</u>

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit
The kernel stops sending timer ticks to CPUs by using "nohz_full=1-255"
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

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Operating System Notes (Continued)

'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
"/home/Uniautos/cpu2017/amd_rate_aocc500_znver5_A_lib/lib:/home/Uniautos/cpu2017/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS settings:
Determinism Control = Manual
Determinism Enable = Power
TDP Control = Manual
TDP = 500
PPT Control = Manual
PPT = 500
NUMA Nodes Per Socket = Auto
ACPI SRAT L3 Cache As NUMA Domain = Auto

Sysinfo program /home/Uniautos/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on xfusion Wed Jun 11 17:12:12 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r

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Platform Notes (Continued)

```
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS
```

```
1. uname -a
Linux xfusion 6.8.0-38-generic #38-Ubuntu SMP PREEMPT_DYNAMIC Fri Jun  7 15:25:01 UTC 2024 x86_64 x86_64
x86_64 GNU/Linux
```

```
2. w
17:12:12 up 18:31,  2 users,  load average: 97.74, 210.13, 235.84
USER      TTY      FROM            LOGIN@   IDLE   JCPU   PCPU WHAT
root      tty1      -               Tue22    2:32m  0.08s  0.01s -bash
```

```
3. Username
From environment variable $USER:  root
```

```
4. ulimit -a
time(seconds)      unlimited
file(blocks)       unlimited
data(kbytes)       unlimited
stack(kbytes)      unlimited
coredump(blocks)   0
memory(kbytes)     unlimited
locked memory(kbytes) 2097152
process            3093612
nofiles            1024
vmemory(kbytes)    unlimited
locks              unlimited
rtprio             0
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --system --deserialize=133
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.014/templogs/preenv.fprate.014.0.log --lognum 014.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/Uniautos/cpu2017
```

```
6. /proc/cpuinfo
model name      : AMD EPYC 9755 128-Core Processor
```

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Platform Notes (Continued)

vendor_id : AuthenticAMD
cpu family : 26
model : 2
stepping : 1
microcode : 0xb002147
bugs : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size : 192 4K pages
cpu cores : 128
siblings : 256
1 physical ids (chips)
256 processors (hardware threads)
physical id 0: core ids 0-127
physical id 0: apicids 0-255

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	52 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	256
On-line CPU(s) list:	0-255
Vendor ID:	AuthenticAMD
BIOS Vendor ID:	Advanced Micro Devices, Inc.
Model name:	AMD EPYC 9755 128-Core Processor
BIOS Model name:	AMD EPYC 9755 128-Core Processor Unknown CPU @ 2.7GHz
BIOS CPU family:	107
CPU family:	26
Model:	2
Thread(s) per core:	2
Core(s) per socket:	128
Socket(s):	1
Stepping:	1
Frequency boost:	enabled
CPU(s) scaling MHz:	66%
CPU max MHz:	4121.1909
CPU min MHz:	1500.0000
BogoMIPS:	5391.76
Flags:	fpu_vme_de_pse_tsc_msr_pae_mce_cx8_apic_sep_mtrr_pge_mca_cmov_pat pse36_clflush_mmx_fxsr_sse_sse2_ht_syscall_nx_mmxext_fxsr_opt_pdpe1gb rdtscp_lm_constant_tsc_rep_good_amd_lbr_v2_nopl_nonstop_tsc_cpuid extd_apicid_aperfmpperf_rapl_pni_pclmulqdq_monitor_ssse3_fma_cx16_pcid sse4_1_sse4_2_x2apic_movbe_popcnt_aes_xsave_avx_f16c_rdrand_lahf_lm cmp_legacy_svm_extapic_cr8_legacy_abm_sse4a_misalignsse_3dnowprefetch osvw_ibs_skinit_wdt_tce_topoext_perfctr_core_perfctr_nb_bpext perfctr_llc_mwaitx_cpb_cat_l3_cdp_l3_hw_pstate_ssbd_mba_perfmon_v2 ibrs_ibpb_stibp_ibrs_enhanced_vmmcall_fsgsbase_tsc_adjust_bmi1_avx2 smep_bmi2_erms_invpcid_cqm_rdt_a_avx512f_avx512dq_rdseed_adx_smap avx512ifma_clflushopt_clwb_avx512cd_sha_ni_avx512bw_avx512vl_xsaveopt xsavec_xgetbv1_xsaves_cqm_llc_cqm_occup_llc_cqm_mbm_total cqm_mbm_local_user_shstk_avx_vnni_avx512_bf16_clzero_irperf xsaveerptr_rdprr_wbnoinvd_amd_ppin_cppc_arat_npt_lbrv_svm_lock nrip_save_tsc_scale_vmc_b_clean_flushbyasid_decodeassists_pausefilter pfthreshold_avic_v_vmsave_vmload_vgif_x2avic_v_spec_ctrl_vnmi avx512vbmi_umip_pku_ospke_avx512_vbmi2_gfni_vaes_vpclmulqdq avx512_vnni_avx512_bitalg_avx512_vpopcntdq_la57_rdpid_bus_lock_detect

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Platform Notes (Continued)

```
movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect
flush_l1d debug_swap
AMD-V
L1d cache: 6 MiB (128 instances)
L1i cache: 4 MiB (128 instances)
L2 cache: 128 MiB (128 instances)
L3 cache: 512 MiB (16 instances)
NUMA node(s): 4
NUMA node0 CPU(s): 0-31,128-159
NUMA node1 CPU(s): 32-63,160-191
NUMA node2 CPU(s): 64-95,192-223
NUMA node3 CPU(s): 96-127,224-255
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
always-on; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected
```

```
From lscpu --cache:
NAME ONE-SIZE ALL-SIZE WAYS TYPE LEVEL SETS PHY-LINE COHERENCY-SIZE
L1d 48K 6M 12 Data 1 64 1 64
L1i 32K 4M 8 Instruction 1 64 1 64
L2 1M 128M 16 Unified 2 1024 1 64
L3 32M 512M 16 Unified 3 32768 1 64
```

```
-----
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0-31,128-159
node 0 size: 193045 MB
node 0 free: 169103 MB
node 1 cpus: 32-63,160-191
node 1 size: 193514 MB
node 1 free: 169402 MB
node 2 cpus: 64-95,192-223
node 2 size: 193514 MB
node 2 free: 170584 MB
node 3 cpus: 96-127,224-255
node 3 size: 193400 MB
node 3 free: 169633 MB
node distances:
node 0 1 2 3
0: 10 12 12 12
1: 12 10 12 12
2: 12 12 10 12
3: 12 12 12 10
-----
```

```
9. /proc/meminfo
```

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Platform Notes (Continued)

MemTotal: 792037424 kB

10. who -r
run-level 5 Jun 10 22:41

11. Systemd service manager version: systemd 255 (255.4-1ubuntu8)
Default Target Status
graphical degraded

12. Failed units, from systemctl list-units --state=failed
UNIT LOAD ACTIVE SUB DESCRIPTION
* fwupd-refresh.service loaded failed failed Refresh fwupd metadata and update motd
Legend: LOAD -> Reflects whether the unit definition was properly loaded.
ACTIVE -> The high-level unit activation state, i.e. generalization of SUB.
SUB -> The low-level unit activation state, values depend on unit type.
1 loaded units listed.

13. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager apparmor appport blk-availability cloud-config cloud-final cloud-init
cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager
grub-common grub-initrd-fallback keyboard-setup lvm2-monitor multipathd
networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb
snapd sysstat systemd-networkd systemd-networkd-wait-online systemd-pstore
systemd-resolved systemd-timesyncd thermald ua-reboot-cmds ubuntu-advantage udisks2 ufw
unattended-upgrades vgauth
enabled-runtime netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
disabled console-getty debug-shell iscsid nftables rsync serial-getty@ ssh
systemd-boot-check-no-failures systemd-confext systemd-network-generator
systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code
systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy
systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext
systemd-time-wait-sync upower
indirect systemd-sysupdate systemd-sysupdate-reboot uuid
masked cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common

14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/vmlinuz-6.8.0-38-generic
root=/dev/mapper/ubuntu--vg-ubuntu--lv
ro
nohz_full=1-255

15. cpupower frequency-info
analyzing CPU 133:
current policy: frequency should be within 1.50 GHz and 2.70 GHz.
The governor "performance" may decide which speed to use
within this range.
boost state support:
Supported: yes
Active: yes
Boost States: 0
Total States: 3
Pstate-P0: 2800MHZ

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Platform Notes (Continued)

16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	1
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	1

17. /sys/kernel/mm/transparent_hugepage

defrag	[always] defer defer+madvise madvise never
enabled	[always] madvise never
hpage_pmd_size	2097152
shmem_enabled	always within_size advise [never] deny force

18. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

19. OS release

From /etc/*-release /etc/*-version
os-release Ubuntu 24.04 LTS

20. Disk information

SPEC is set to: /home/Uniautos/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/ubuntu--vg-ubuntu--lv	ext4	295G	17G	264G	6%	/

21. /sys/devices/virtual/dmi/id

Vendor: XFUSION
Product: 2158H V8
Product Family: Turin

22. dmidecode

Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately

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Platform Notes (Continued)

determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

12x SK Hynix HMC94AHBRA485N 64 GB 2 rank 6400

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: XFUSION
BIOS Version: 00.13.02.12
BIOS Date: 05/14/2025
BIOS Revision: 2.12

Compiler Version Notes

=====
C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

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CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Jun-2025
Hardware Availability: Apr-2025
Software Availability: Oct-2024

Compiler Version Notes (Continued)

Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

Benchmarks using both C and C++:
clang++ clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang



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Software Availability: Oct-2024

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

C++ benchmarks:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc

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Tested by: xFusion

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Base Optimization Flags (Continued)

Fortran benchmarks (continued):

-lflang -ldl

Benchmarks using both Fortran and C:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl

Benchmarks using both C and C++:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=2000000 -lamdlibm -lamdalloc -lflang
-ldl

Benchmarks using Fortran, C, and C++:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=2000000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl

Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

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Tested by: xFusion

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Hardware Availability: Apr-2025

Software Availability: Oct-2024

Base Other Flags (Continued)

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl

544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl

C++ benchmarks:

508.namd_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl

510.parest_r: -m64 -std=c++14 -flto -Wl,-mllvm -Wl,-suppress-fmas
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl

Fortran benchmarks:

503.bwaves_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm

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Peak Optimization Flags (Continued)

503.bwaves_r (continued):

-lamdalloc -ldl -lflang

549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdalloc -ldl -lflang

554.roms_r: Same as 503.bwaves_r

Benchmarks using both Fortran and C:

521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang

527.cam4_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang

Benchmarks using both C and C++:

511.povray_r: -m64 -std=c++14

-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -reduce-array-computations=3 -zopt

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Peak Optimization Flags (Continued)

511.povray_r (continued):

```
-mllvm -unroll-threshold=100  
-mllvm -loop-unswitch-threshold=2000000 -lamdlibm  
-lamdalloc -ldl
```

526.blender_r: -m64 -std=c++14

```
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt  
-mllvm -unroll-threshold=100 -lamdlibm -lamdalloc -ldl
```

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-AMD-V1.5.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-AMD-V1.5.xml>

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