



SPEC CPU®2017 Floating Point Rate Result

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Supermicro

SuperServer SYS-242H-NR
(X14QBH+ , Intel Xeon 6788P)

SPECrate®2017_fp_base = 2910

SPECrate®2017_fp_peak = 3060

CPU2017 License: 001176

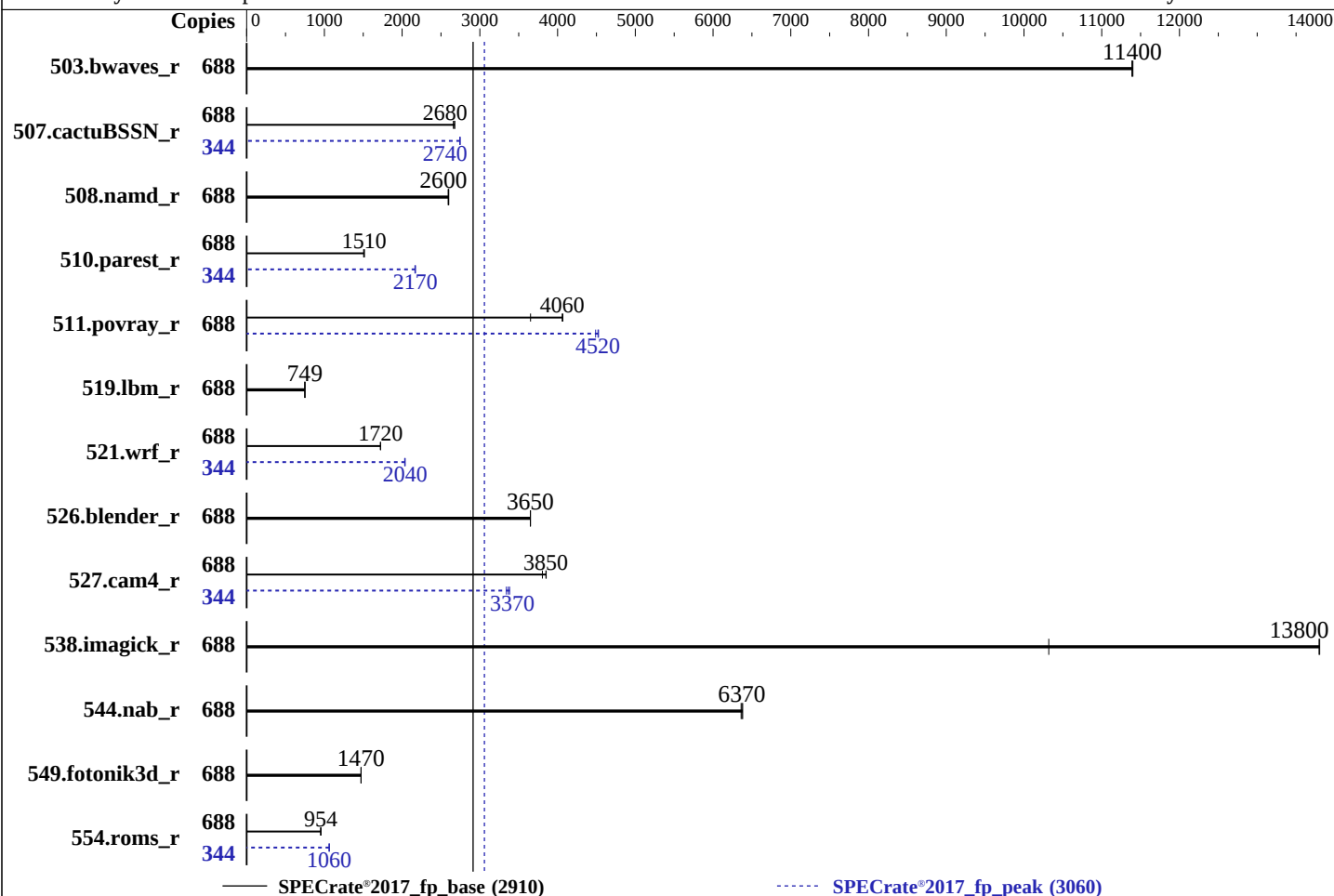
Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Jun-2025

Hardware Availability: Jul-2025

Software Availability: Jun-2024



Hardware

CPU Name: Intel Xeon 6788P
Max MHz: 3800
Nominal: 2000
Enabled: 344 cores, 4 chips, 2 threads/core
Orderable: 2,4 chips
Cache L1: 64 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 336 MB I+D on chip per chip
Other: None
Memory: 2 TB (32 x 64 GB 2Rx4 PC5-6400B-R)
Storage: 1 x 1.6TB NVMe SSD
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6
6.4.0-150600.21-default
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++
Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran Compiler
for Linux;
Parallel: No
Firmware: Version 1.3 released May-2025
File System: btrfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of
additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	688	606	11400	605	11400	606	11400	688	606	11400	605	11400	606	11400
507.cactuBSSN_r	688	326	2680	328	2660	326	2680	344	159	2740	159	2740	159	2750
508.namd_r	688	252	2590	252	2600	251	2600	688	252	2590	252	2600	251	2600
510.parest_r	688	1196	1510	1190	1510	1189	1510	344	416	2160	415	2170	415	2170
511.povray_r	688	396	4060	440	3650	395	4070	688	358	4490	355	4530	355	4520
519.lbm_r	688	967	750	968	749	968	749	688	967	750	968	749	968	749
521.wrf_r	688	895	1720	895	1720	895	1720	344	379	2030	378	2040	378	2040
526.blender_r	688	287	3650	287	3650	287	3650	688	287	3650	287	3650	287	3650
527.cam4_r	688	312	3850	313	3850	316	3800	344	178	3380	180	3340	179	3370
538.imagick_r	688	166	10300	124	13800	124	13800	688	166	10300	124	13800	124	13800
544.nab_r	688	182	6360	181	6380	182	6370	688	182	6360	181	6380	182	6370
549.fotonik3d_r	688	1822	1470	1821	1470	1822	1470	688	1822	1470	1821	1470	1822	1470
554.roms_r	688	1145	955	1148	952	1145	954	344	515	1060	514	1060	515	1060

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOCONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

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General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS Settings:

Power Performance Tuning = BIOS Controls EPB
ENERGY_PERF_BIAS_CFG mode = Extreme Performance
KTI Prefetch = Enable
DCU Streamer Prefetcher = Disable
LLC Dead Line Alloc = Disable
Stale AtoS = Disable
SNC = Enable

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on 144-21 Wed Jun 11 23:15:38 2025

SUT (System Under Test) info as seen by some common utilities.

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1. uname -a
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5. sysinfo process ancestry
6. /proc/cpuinfo
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10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux 144-21 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09) x86_64
x86_64 x86_64 GNU/Linux

2. w

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Platform Notes (Continued)

```
23:15:39 up 6:36, 1 user, load average: 412.18, 616.70, 653.47
USER      TTY      FROM          LOGIN@      IDLE   JCPU   PCPU   WHAT
root      tty1      -             16:43       6:32m  1.20s  0.05s  -bash
```

3. Username
From environment variable \$USER: root

4. ulimit -a

core file size	(blocks, -c)	unlimited
data seg size	(kbytes, -d)	unlimited
scheduling priority	(-e)	0
file size	(blocks, -f)	unlimited
pending signals	(-i)	8253433
max locked memory	(kbytes, -l)	8192
max memory size	(kbytes, -m)	unlimited
open files	(-n)	1024
pipe size	(512 bytes, -p)	8
POSIX message queues	(bytes, -q)	819200
real-time priority	(-r)	0
stack size	(kbytes, -s)	unlimited
cpu time	(seconds, -t)	unlimited
max user processes	(-u)	8253433
virtual memory	(kbytes, -v)	unlimited
file locks	(-x)	unlimited

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=688 -c
ic2024.1-lin-core-avx512-rate-20240308.cfg --define smt-on --define cores=344 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=688 --configfile
ic2024.1-lin-core-avx512-rate-20240308.cfg --define smt-on --define cores=344 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
--runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
\$SPEC/tmp/CPU2017.015/temlogs/preenv.fprate.015.0.log --lognum 015.0 --from_runcpu 2
specperl \$SPEC/bin/sysinfo
\$SPEC = /home/cpu2017

6. /proc/cpuinfo

model name	: Intel(R) Xeon(R) 6788P
vendor_id	: GenuineIntel
cpu family	: 6
model	: 173
stepping	: 1
microcode	: 0x10003c2
bugs	: spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores	: 86
siblings	: 172
4 physical ids (chips)	
688 processors (hardware threads)	
physical id 0: core ids 0-42,64-106	
physical id 1: core ids 0-42,64-106	
physical id 2: core ids 0-42,64-106	

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Platform Notes (Continued)

physical id 3: core ids 0-42,64-106
physical id 0: apicids 0-85,128-213
physical id 1: apicids 256-341,384-469
physical id 2: apicids 512-597,640-725
physical id 3: apicids 768-853,896-981

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                688
On-line CPU(s) list:   0-687
Vendor ID:             GenuineIntel
BIOS Vendor ID:        Intel(R) Corporation
Model name:            Intel(R) Xeon(R) 6788P
BIOS Model name:       Intel(R) Xeon(R) 6788P  CPU @ 2.0GHz
BIOS CPU family:       179
CPU family:            6
Model:                 173
Thread(s) per core:    2
Core(s) per socket:    86
Socket(s):             4
Stepping:              1
BogoMIPS:              4000.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                        pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
                        pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
                        nopl xtopology nonstop_tsc cpuid aperfperf tsc_known_freq pni
                        pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
                        xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
                        tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                        3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2
                        ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
                        vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid
                        rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
                        clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                        xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                        split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
                        arat pln pts vnmi avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni
                        vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57
                        rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear
                        serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile
                        amx_int8 flush_l1d arch_capabilities
Virtualization:        VT-x
L1d cache:             16.1 MiB (344 instances)
L1i cache:             21.5 MiB (344 instances)
L2 cache:              688 MiB (344 instances)
L3 cache:              1.3 GiB (4 instances)
NUMA node(s):          8
NUMA node0 CPU(s):    0-42, 344-386
NUMA node1 CPU(s):    43-85, 387-429
NUMA node2 CPU(s):    86-128, 430-472
NUMA node3 CPU(s):    129-171, 473-515
NUMA node4 CPU(s):    172-214, 516-558
```

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Platform Notes (Continued)

NUMA node5 CPU(s): 215-257,559-601
NUMA node6 CPU(s): 258-300,602-644
NUMA node7 CPU(s): 301-343,645-687
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSE-eIBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	16.1M	12	Data	1	64	1	64
L1i	64K	21.5M	16	Instruction	1	64	1	64
L2	2M	688M	16	Unified	2	2048	1	64
L3	336M	1.3G	16	Unified	3	344064	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)
node 0 cpus: 0-42,344-386
node 0 size: 257409 MB
node 0 free: 219501 MB
node 1 cpus: 43-85,387-429
node 1 size: 258025 MB
node 1 free: 223896 MB
node 2 cpus: 86-128,430-472
node 2 size: 258025 MB
node 2 free: 224332 MB
node 3 cpus: 129-171,473-515
node 3 size: 257986 MB
node 3 free: 224143 MB
node 4 cpus: 172-214,516-558
node 4 size: 258025 MB
node 4 free: 224142 MB
node 5 cpus: 215-257,559-601
node 5 size: 258025 MB
node 5 free: 222602 MB
node 6 cpus: 258-300,602-644
node 6 size: 258025 MB
node 6 free: 224419 MB
node 7 cpus: 301-343,645-687
node 7 size: 257863 MB
node 7 free: 224226 MB
node distances:
node 0 1 2 3 4 5 6 7
0: 10 12 21 21 21 21 21 21
1: 12 10 21 21 21 21 21 21
2: 21 21 10 12 21 21 21 21
3: 21 21 12 10 21 21 21 21

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Platform Notes (Continued)

```
4: 21 21 21 21 10 12 21 21
5: 21 21 21 21 12 10 21 21
6: 21 21 21 21 21 21 10 12
7: 21 21 21 21 21 21 12 10
```

9. /proc/meminfo
MemTotal: 2112908860 kB

10. who -r
run-level 3 Jun 11 16:40

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
Default Target Status
multi-user running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron display-manager getty@ irqbalance issue-generator kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections nvme-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	accounts-daemon autofs autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info firewalld fsidd gpm grub2-once haveged ipmi ipmievdev issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nmb ostree-remount rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd-generate-opts smb snmpd snmptrapd speech-dispatcherd systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2 update-system-flatpaks upower vncserver@
indirect	systemd-userdbd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=fc50b064-a874-4c17-8c04-787042c5bfd6
splash=silent
mitigations=auto
quiet
security=apparmor

14. cpupower frequency-info
analyzing CPU 7:
Unable to determine current policy
boost state support:
Supported: yes
Active: yes

15. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10

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```
vm.dirty_bytes          0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio          20
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold    500
vm.min_unmapped_ratio   1
vm.nr_hugepages         0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness           60
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode    0
```

```
-----
16. /sys/kernel/mm/transparent_hugepage
defrag          always defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force
-----
```

```
-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag                1
max_ptes_none         511
max_ptes_shared       256
max_ptes_swap         64
pages_to_scan         4096
scan_sleep_millisecs  10000
-----
```

```
-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
-----
```

```
-----
19. Disk information
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p2 btrfs 892G 717G 175G 81% /home
-----
```

```
-----
20. /sys/devices/virtual/dmi/id
Vendor: Supermicro
Product: Super Server
Product Family: SMC X14
-----
```

```
-----
21. dmidecode
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
28x Samsung M321R8GA0EB2-CCPKC 64 GB 2 rank 6400
4x Samsung M321R8GA0PB2-CCPKC 64 GB 2 rank 6400
-----
```

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Platform Notes (Continued)

22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 1.3
BIOS Date: 05/27/2025
BIOS Revision: 5.35

Compiler Version Notes

=====

C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

C++, C, Fortran | 507.cactuBSSN_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

SuperServer SYS-242H-NR
(X14QBH+ , Intel Xeon 6788P)

SPECrate®2017_fp_base = 2910

SPECrate®2017_fp_peak = 3060

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Jun-2025
Hardware Availability: Jul-2025
Software Availability: Jun-2024

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

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Tested by: Supermicro

Test Date: Jun-2025

Hardware Availability: Jul-2025

Software Availability: Jun-2024

Peak Compiler Invocation (Continued)

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs  
-fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse  
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-GNR-revC.html>
<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-GNR-revC.xml>
<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

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