



# SPEC CPU®2017 Floating Point Rate Result

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## Lenovo Global Technology

ThinkSystem SD665 V3  
(4.20 GHz, AMD EPYC 9175F)

SPECrate®2017\_fp\_base = 892

SPECrate®2017\_fp\_peak = 913

CPU2017 License: 9017

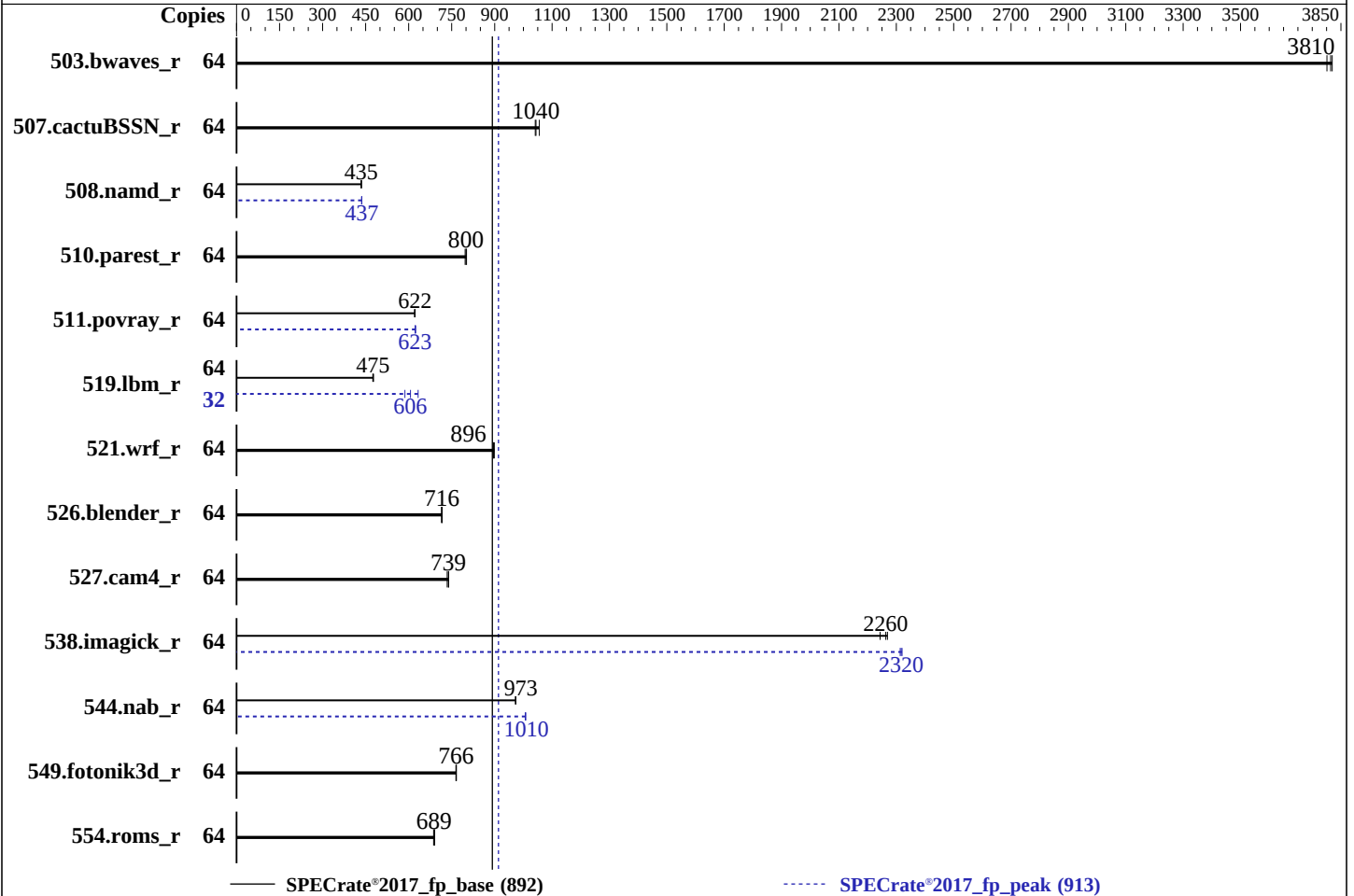
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jun-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024



### Hardware

CPU Name: AMD EPYC 9175F  
Max MHz: 5000  
Nominal: 4200  
Enabled: 32 cores, 2 chips, 2 threads/core  
Orderable: 2 chips  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 1 MB I+D on chip per core  
L3: 512 MB I+D on chip per chip,  
32 MB shared / 1 cores  
Other: None  
Memory: 768 GB (24 x 32 GB 2Rx8 PC5-6400B-R)  
Storage: 1 x 3.84 TB NVME SSD  
Other: CPU Cooling: DLC

### Software

OS: SUSE Linux Enterprise Server 15 SP6  
Kernel 6.4.0-150600.21-default  
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC  
Parallel: No  
Firmware: Lenovo BIOS Version QGE139H 8.21 released May-2025  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: None  
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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## Results Table

| Benchmark       | Base   |                   |                   |                    |                    |                    |                    | Peak   |                   |                   |                   |                    |                    |                    |
|-----------------|--------|-------------------|-------------------|--------------------|--------------------|--------------------|--------------------|--------|-------------------|-------------------|-------------------|--------------------|--------------------|--------------------|
|                 | Copies | Seconds           | Ratio             | Seconds            | Ratio              | Seconds            | Ratio              | Copies | Seconds           | Ratio             | Seconds           | Ratio              | Seconds            | Ratio              |
| 503.bwaves_r    | 64     | 168               | 3820              | <b><u>168</u></b>  | <b><u>3810</u></b> | 169                | 3800               | 64     | 168               | 3820              | <b><u>168</u></b> | <b><u>3810</u></b> | 169                | 3800               |
| 507.cactuBSSN_r | 64     | 76.8              | 1060              | 77.8               | 1040               | <b><u>77.6</u></b> | <b><u>1040</u></b> | 64     | 76.8              | 1060              | 77.8              | 1040               | <b><u>77.6</u></b> | <b><u>1040</u></b> |
| 508.namd_r      | 64     | 140               | 435               | <b><u>140</u></b>  | <b><u>435</u></b>  | 140                | 434                | 64     | 139               | 436               | <b><u>139</u></b> | <b><u>437</u></b>  | 139                | 437                |
| 510.parest_r    | 64     | 209               | 802               | <b><u>209</u></b>  | <b><u>800</u></b>  | 210                | 797                | 64     | 209               | 802               | <b><u>209</u></b> | <b><u>800</u></b>  | 210                | 797                |
| 511.povray_r    | 64     | 241               | 620               | <b><u>240</u></b>  | <b><u>622</u></b>  | 240                | 622                | 64     | <b><u>240</u></b> | <b><u>623</u></b> | 239               | 624                | 240                | 622                |
| 519.lbm_r       | 64     | 141               | 478               | 142                | 475                | <b><u>142</u></b>  | <b><u>475</u></b>  | 32     | 57.5              | 587               | 53.3              | 633                | <b><u>55.6</u></b> | <b><u>606</u></b>  |
| 521.wrf_r       | 64     | <b><u>160</u></b> | <b><u>896</u></b> | 160                | 895                | 160                | 899                | 64     | <b><u>160</u></b> | <b><u>896</u></b> | 160               | 895                | 160                | 899                |
| 526.blender_r   | 64     | 136               | 715               | <b><u>136</u></b>  | <b><u>716</u></b>  | 136                | 717                | 64     | 136               | 715               | <b><u>136</u></b> | <b><u>716</u></b>  | 136                | 717                |
| 527.cam4_r      | 64     | 152               | 734               | <b><u>152</u></b>  | <b><u>739</u></b>  | 151                | 739                | 64     | 152               | 734               | <b><u>152</u></b> | <b><u>739</u></b>  | 151                | 739                |
| 538.imagick_r   | 64     | 70.9              | 2240              | <b><u>70.3</u></b> | <b><u>2260</u></b> | 70.1               | 2270               | 64     | 68.6              | 2320              | 68.8              | 2310               | <b><u>68.7</u></b> | <b><u>2320</u></b> |
| 544.nab_r       | 64     | <b><u>111</u></b> | <b><u>973</u></b> | 111                | 973                | 111                | 973                | 64     | 107               | 1010              | <b><u>107</u></b> | <b><u>1010</u></b> | 107                | 1010               |
| 549.fotonik3d_r | 64     | 326               | 765               | <b><u>326</u></b>  | <b><u>766</u></b>  | 325                | 766                | 64     | 326               | 765               | <b><u>326</u></b> | <b><u>766</u></b>  | 325                | 766                |
| 554.roms_r      | 64     | <b><u>148</u></b> | <b><u>689</u></b> | 147                | 691                | 148                | 687                | 64     | <b><u>148</u></b> | <b><u>689</u></b> | 147               | 691                | 148                | 687                |

SPECrate®2017\_fp\_base = **892**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

cpupower set to performance mode

(Continued on next page)



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## Operating System Notes (Continued)

cpupower frequency-set -r -g performance  
To enable Transparent Hugepages (THP) for all allocations:  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH =  
"/home/cpu2017-1.1.9-amd-aocc500\_znver5\_A1.2/amd\_rate\_aocc500\_znver5\_A\_lib/lib:/home/cpu2017-1.1.9-amd-aocc500\_znver5\_A1.2/amd\_rate\_aocc500\_znver5\_A\_lib/lib32:"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:  
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode  
P-State set to Enabled  
NUMA Nodes per Socket set to NPS4

Sysinfo program /home/cpu2017-1.1.9-amd-aocc500\_znver5\_A1.2/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Thu Jun 5 13:28:16 2025

SUT (System Under Test) info as seen by some common utilities.

### Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info

(Continued on next page)



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## Platform Notes (Continued)

```
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
```

```
1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
13:28:16 up 3:11, 1 user, load average: 4.94, 29.74, 42.50
USER      TTY      FROM      LOGIN@   IDLE   JCPU   PCPU WHAT
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size                (blocks, -f) unlimited
pending signals         (-i) 3094186
max locked memory       (kbytes, -l) 2097152
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 3094186
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
/bin/bash ./02.remote_local_SPECcpu_1.01.sh
/bin/bash ./Run026-compliant-amd-ratefp.sh
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.493/temlogs/preenv.fprate.493.0.log --lognum 493.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2
```

(Continued on next page)



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## Platform Notes (Continued)

### 6. /proc/cpuinfo

```
model name      : AMD EPYC 9175F 16-Core Processor
vendor_id       : AuthenticAMD
cpu family      : 26
model           : 2
stepping        : 1
microcode       : 0xb002147
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 192 4K pages
cpu cores       : 16
siblings        : 32
2 physical ids (chips)
64 processors (hardware threads)
physical id 0:  core ids 0,8,16,24,32,40,48,56,64,72,80,88,96,104,112,120
physical id 1:  core ids 0,8,16,24,32,40,48,56,64,72,80,88,96,104,112,120
physical id 0:  apicids
0-1,16-17,32-33,48-49,64-65,80-81,96-97,112-113,128-129,144-145,160-161,176-177,192-193,208-209,224-225,2
40-241
physical id 1:  apicids
256-257,272-273,288-289,304-305,320-321,336-337,352-353,368-369,384-385,400-401,416-417,432-433,448-449,4
64-465,480-481,496-497
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:      52 bits physical, 57 bits virtual
Byte Order:         Little Endian
CPU(s):             64
On-line CPU(s) list: 0-63
Vendor ID:          AuthenticAMD
BIOS Vendor ID:     Advanced Micro Devices, Inc.
Model name:         AMD EPYC 9175F 16-Core Processor
BIOS Model name:    AMD EPYC 9175F 16-Core Processor
BIOS CPU family:    107
CPU family:         26
Model:              2
Thread(s) per core: 2
Core(s) per socket: 16
Socket(s):          2
Stepping:           1
Frequency boost:    enabled
CPU(s) scaling MHz: 103%
CPU max MHz:        4200.0000
CPU min MHz:        1500.0000
BogoMIPS:           8400.77
Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                    pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                    rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                    extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                    sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
                    cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                    osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                    perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
                    ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
```

(Continued on next page)



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## Platform Notes (Continued)

smeop bmi2 erms invpcid cqm rdt\_a avx512f avx512dq rdseed adx smap  
avx512ifma clflushopt clwb avx512cd sha\_ni avx512bw avx512vl xsaveopt  
xsavvec xgetbv1 xsaves cqm\_llc cqm\_occup\_llc cqm\_mbm\_total  
cqm\_mbm\_local user\_shstk avx\_vnni avx512\_bf16 clzero irperf  
xsaveerptr rdpru wbnoinvd amd\_ppin cppc arat npt lbrv svm\_lock  
nrip\_save tsc\_scale vmcb\_clean flushbyasid decodeassists pausefilter  
pfthreshold avic v\_vmsave\_vmload vgif x2avic v\_spec\_ctrl vnmi  
avx512vbmi umip pku ospke avx512\_vbmi2 gfni vaes vpclmulqdq  
avx512\_vnni avx512\_bitalg avx512\_vpopcntdq la57 rdpid bus\_lock\_detect  
movdiri movdir64b overflow\_recov succor smca fsrm avx512\_vp2intersect  
flush\_l1d debug\_swap

AMD-V

Virtualization:

L1d cache:

1.5 MiB (32 instances)

L1i cache:

1 MiB (32 instances)

L2 cache:

32 MiB (32 instances)

L3 cache:

1 GiB (32 instances)

NUMA node(s):

8

NUMA node0 CPU(s):

0-3,32-35

NUMA node1 CPU(s):

4-7,36-39

NUMA node2 CPU(s):

8-11,40-43

NUMA node3 CPU(s):

12-15,44-47

NUMA node4 CPU(s):

16-19,48-51

NUMA node5 CPU(s):

20-23,52-55

NUMA node6 CPU(s):

24-27,56-59

NUMA node7 CPU(s):

28-31,60-63

Vulnerability Gather data sampling:

Not affected

Vulnerability Itlb multihit:

Not affected

Vulnerability L1tf:

Not affected

Vulnerability Mds:

Not affected

Vulnerability Meltdown:

Not affected

Vulnerability Mmio stale data:

Not affected

Vulnerability Reg file data sampling:

Not affected

Vulnerability Retbleed:

Not affected

Vulnerability Spec rstack overflow:

Not affected

Vulnerability Spec store bypass:

Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1:

Mitigation; usercopy/swaps barriers and \_\_user pointer sanitization

Vulnerability Spectre v2:

Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP

always-on; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected

Vulnerability Srbds:

Not affected

Vulnerability Tsx async abort:

Not affected

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE        | LEVEL | SETS  | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|-------|----------|----------------|
| L1d  | 48K      | 1.5M     | 12   | Data        | 1     | 64    | 1        | 64             |
| L1i  | 32K      | 1M       | 8    | Instruction | 1     | 64    | 1        | 64             |
| L2   | 1M       | 32M      | 16   | Unified     | 2     | 1024  | 1        | 64             |
| L3   | 32M      | 1G       | 16   | Unified     | 3     | 32768 | 1        | 64             |

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)

node 0 cpus: 0-3,32-35

node 0 size: 96348 MB

node 0 free: 95773 MB

node 1 cpus: 4-7,36-39

node 1 size: 96765 MB

node 1 free: 96174 MB

node 2 cpus: 8-11,40-43

node 2 size: 96765 MB

(Continued on next page)



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## Platform Notes (Continued)

```
node 2 free: 96285 MB
node 3 cpus: 12-15,44-47
node 3 size: 96727 MB
node 3 free: 95918 MB
node 4 cpus: 16-19,48-51
node 4 size: 96765 MB
node 4 free: 96312 MB
node 5 cpus: 20-23,52-55
node 5 size: 96765 MB
node 5 free: 96307 MB
node 6 cpus: 24-27,56-59
node 6 size: 96765 MB
node 6 free: 96313 MB
node 7 cpus: 28-31,60-63
node 7 size: 96669 MB
node 7 free: 96179 MB
node distances:
node  0  1  2  3  4  5  6  7
 0: 10 12 12 12 32 32 32 32
 1: 12 10 12 12 32 32 32 32
 2: 12 12 10 12 32 32 32 32
 3: 12 12 12 10 32 32 32 32
 4: 32 32 32 32 10 12 12 12
 5: 32 32 32 32 12 10 12 12
 6: 32 32 32 32 12 12 10 12
 7: 32 32 32 32 12 12 12 10
```

```
-----
9. /proc/meminfo
   MemTotal:      792138700 kB
```

```
-----
10. who -r
    run-level 3 Jun 5 10:17
```

```
-----
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
    Default Target   Status
    multi-user       running
```

```
-----
12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance issue-generator
kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections nvme-fc-autoconnect postfix
purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked wickedd-auto4
wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs
disabled autofs autostart-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
firewalld fsiddd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievd issue-add-ssh-keys
kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd
serial-getty@ smartd-generate_opts snmpd snmptrapd systemd-boot-check-no-failures
systemd-confext systemd-network-generator systemd-sysext systemd-time-wait-sync
systemd-timesyncd
indirect systemd-userdbd wickedd
```

```
-----
13. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
```

(Continued on next page)



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### Platform Notes (Continued)

```
root=UUID=9444ad63-6600-41de-9db7-026811eaf134
splash=silent
mitigations=auto
quiet
security=apparmor
```

#### 14. cpupower frequency-info

```
analyzing CPU 44:
  current policy: frequency should be within 1.50 GHz and 4.20 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.

  boost state support:
    Supported: yes
    Active: yes
```

#### 15. sysctl

```
kernel.numa_balancing          1
kernel.randomize_va_space      0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                  1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1
```

#### 16. /sys/kernel/mm/transparent\_hugepage

```
defrag      [always] defer defer+madvise madvise never
enabled     [always] madvise never
hpage_pmd_size  2097152
shmem_enabled always within_size advise [never] deny force
```

#### 17. /sys/kernel/mm/transparent\_hugepage/khugepaged

```
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
```

#### 18. OS release

```
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
```

(Continued on next page)



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SPECrate®2017\_fp\_peak = 913

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

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Hardware Availability: Jul-2025

Software Availability: Oct-2024

## Platform Notes (Continued)

### 19. Disk information

SPEC is set to: /home/cpu2017-1.1.9-amd-aocc500\_znver5\_A1.2

| Filesystem     | Type | Size | Used | Avail | Use% | Mounted on |
|----------------|------|------|------|-------|------|------------|
| /dev/nvme0n1p3 | xfs  | 3.5T | 98G  | 3.4T  | 3%   | /          |

### 20. /sys/devices/virtual/dmi/id

Vendor: Lenovo  
Product: ThinkSystem SD665 V3  
Product Family: ThinkSystem  
Serial: 1234567890

### 21. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

#### Memory:

12x SK Hynix HMC88AHB8A471N 32 GB 2 rank 6400  
2x SK Hynix HMC88AHB8A472N 32 GB 2 rank 6400  
10x SK Hynix HMC88AHB8A478N 32 GB 2 rank 6400

### 22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Lenovo  
BIOS Version: QGE139H-8.21  
BIOS Date: 05/14/2025  
BIOS Revision: 8.21  
Firmware Revision: 13.10

## Compiler Version Notes

C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

(Continued on next page)



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## Compiler Version Notes (Continued)

Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)

=====  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)

=====  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)

=====  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

## Base Compiler Invocation

C benchmarks:  
clang

C++ benchmarks:  
clang++

(Continued on next page)



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CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jun-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

## Base Compiler Invocation (Continued)

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
526.blender\_r: -funsigned-char -DSPEC\_LP64  
527.cam4\_r: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3  
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc  
-lflang -ldl

C++ benchmarks:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

(Continued on next page)



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**Hardware Availability:** Jul-2025

**Software Availability:** Oct-2024

## Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
```

(Continued on next page)



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## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-Wl, -mllvm -Wl, -x86-use-vzeroupper=false -Wl, -mllvm -Wl, -extra-inliner  
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie  
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100  
-mllvm -loop-unswitch-threshold=200000 -mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions  
-lamdlibm -lamdalloc -lflang -ldl
```

## Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

(Continued on next page)



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Software Availability: Oct-2024

## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

538.imagick\_r: Same as 519.lbm\_r

```
544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

C++ benchmarks:

```
508.namd_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
```

(Continued on next page)



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## Peak Optimization Flags (Continued)

508.namd\_r (continued):

```
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-mllvm -unroll-threshold=100  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdalloc -ldl
```

510.parest\_r: basepeak = yes

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

554.roms\_r: basepeak = yes

Benchmarks using both Fortran and C:

521.wrf\_r: basepeak = yes

527.cam4\_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -m64 -std=c++14  
-Wl, -mllvm -Wl, -align-all-nofallthru-blocks=6  
-Wl, -mllvm -Wl, -reduce-array-computations=3  
-Wl, -mllvm -Wl, -x86-use-vzeroupper=false  
-Wl, -mllvm -Wl, -extra-inliner -Ofast -march=znver5  
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -mllvm -reduce-array-computations=3 -zopt  
-mllvm -unroll-threshold=100  
-mllvm -loop-unswitch-threshold=200000 -lamdlibm  
-lamdalloc -ldl
```

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes



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## Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-F.html>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-F.xml>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

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