



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

xFusion

FusionServer 2158H V8
(AMD EPYC 9745)

SPECCrate®2017_fp_base = 1040

SPECCrate®2017_fp_peak = 1100

CPU2017 License: 6488

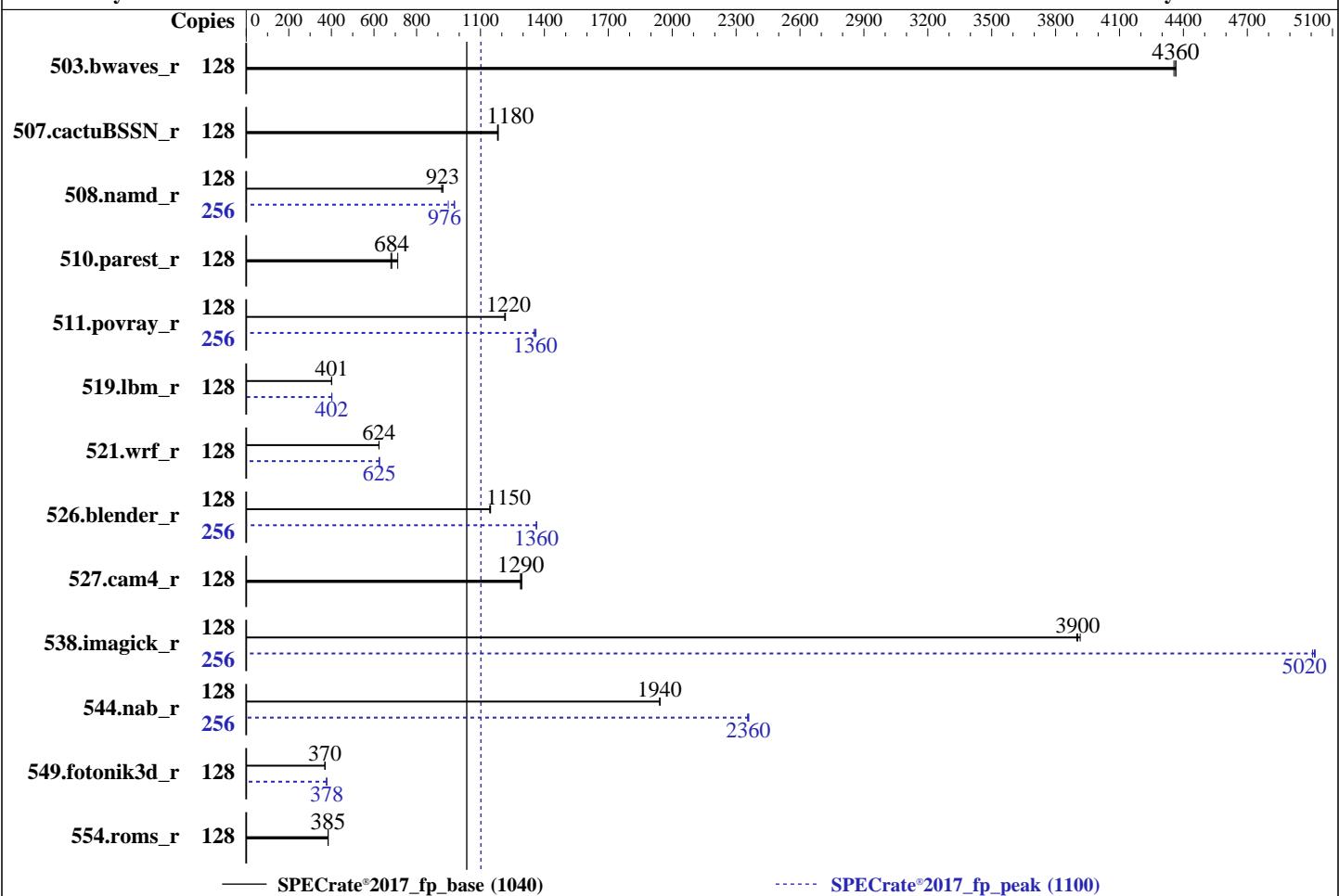
Test Date: Jun-2025

Test Sponsor: xFusion

Hardware Availability: Apr-2025

Tested by: xFusion

Software Availability: Oct-2024



— SPECCrate®2017_fp_base (1040)

----- SPECCrate®2017_fp_peak (1100)

Hardware

CPU Name: AMD EPYC 9745
Max MHz: 3700
Nominal: 2400
Enabled: 128 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 256 MB I+D on chip per chip, 32 MB shared / 16 cores
Other: None
Memory: 768 GB (12 x 64 GB 2Rx4 PC5-6400B-R)
Storage: 1 x 1.6 TB PCIe NVMe SSD
Other: CPU Cooling: Air

Software

OS: Ubuntu 24.04 LTS
Compiler: 6.8.0-31-generic
Parallel: C/C++/Fortran: Version 5.0.0 of AOCC
Firmware: No
File System: Version 00.13.02.12 released May-2025
System State: ext4
Base Pointers: Run level 5 (multi-user)
Peak Pointers: 64-bit
Other: 64-bit
Power Management: Peak Pointers: None
BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	128	295	4360	294	4360	294	4360	128	295	4360	294	4360	294	4360
507.cactusBSSN_r	128	137	1180	137	1180	137	1180	128	137	1180	137	1180	137	1180
508.namd_r	128	133	917	132	923	132	923	256	248	979	256	949	249	976
510.parest_r	128	490	684	471	712	493	679	128	490	684	471	712	493	679
511.povray_r	128	246	1220	246	1210	246	1220	256	439	1360	441	1360	442	1350
519.lbm_r	128	336	401	336	402	337	400	128	336	402	335	402	336	402
521.wrf_r	128	460	623	460	624	460	624	128	458	626	459	624	459	625
526.blender_r	128	170	1140	170	1150	170	1150	256	286	1360	286	1360	286	1360
527.cam4_r	128	174	1290	173	1290	174	1290	128	174	1290	173	1290	174	1290
538.imagick_r	128	81.6	3900	81.5	3900	81.3	3920	256	127	5020	127	5010	127	5020
544.nab_r	128	111	1940	111	1940	111	1940	256	183	2350	183	2360	183	2360
549.fotonik3d_r	128	1349	370	1347	370	1349	370	128	1321	378	1321	378	1321	378
554.roms_r	128	528	385	529	384	529	385	128	528	385	529	384	529	385

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit
The kernel stops sending timer ticks to CPUs by using "nohz_full=1-127"
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

(Continued on next page)



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Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib32:  
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS settings:
Determinism Control = Manual
Determinism Enable = Power
TDP Control = Manual
TDP = 500
PPT Control = Manual
PPT = 500
NUMA Nodes Per Socket = NPS4
ACPI SRAT L3 Cache As NUMA Domain = Auto

```
Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on 2158hv8 Tue Jun 17 17:48:34 2025
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

- ```
1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8)
```

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## Platform Notes (Continued)

```
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS
```

---

```
1. uname -a
Linux 2158hv8 6.8.0-31-generic #31-Ubuntu SMP PREEMPT_DYNAMIC Sat Apr 20 00:40:06 UTC 2024 x86_64 x86_64
x86_64 GNU/Linux
```

---

```
2. w
17:48:34 up 6:12, 1 user, load average: 136.56, 224.22, 242.36
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root tty1 - 11:45 5:50m 1.23s 0.12s -bash
```

---

```
3. Username
From environment variable $USER: root
```

---

```
4. ulimit -a
time(seconds) unlimited
file(blocks) unlimited
data(kbytes) unlimited
stack(kbytes) unlimited
coredump(blocks) 0
memory(kbytes) unlimited
locked memory(kbytes) 2097152
process 3093635
nofiles 1024
vmemory(kbytes) unlimited
locks unlimited
rtprio 0
```

---

```
5. sysinfo process ancestry
/sbin/init
/bin/login -p --
-bash
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

---

```
6. /proc/cpuinfo
```

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## Platform Notes (Continued)

```

model name : AMD EPYC 9745 128-Core Processor
vendor_id : AuthenticAMD
cpu family : 26
model : 17
stepping : 0
microcode : 0xb101047
bugs : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size : 192 4K pages
cpu cores : 128
siblings : 256
1 physical ids (chips)
256 processors (hardware threads)
physical id 0: core ids 0-127
physical id 0: apicids 0-255

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

-----  
7. lscpu

```

From lscpu from util-linux 2.39.3:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 52 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 256
On-line CPU(s) list: 0-255
Vendor ID: AuthenticAMD
BIOS Vendor ID: Advanced Micro Devices, Inc.
Model name: AMD EPYC 9745 128-Core Processor
BIOS Model name: AMD EPYC 9745 128-Core Processor Unknown CPU @ 2.4GHz
BIOS CPU family: 107
CPU family: 26
Model: 17
Thread(s) per core: 2
Core(s) per socket: 128
Socket(s): 1
Stepping: 0
Frequency boost: enabled
CPU(s) scaling MHz: 65%
CPU max MHz: 3707.8120
CPU min MHz: 1500.0000
BogoMIPS: 4792.40
Flags:
fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
extd_apicid aperfmpfperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnnowprefetch
osw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_llc mwaitx cpb cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2
ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bml1 avx2
smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total
cqmq_mbm_local user_shstk avx_vnmi avx512_bf16 clzero iperf
xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock
nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi
avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq

```

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## Platform Notes (Continued)

avx512\_vnni avx512\_bitalg avx512\_vpopcntdq la57 rdpid bus\_lock\_detect  
movdiri movdir64b overflow\_recov succor smca fsrm avx512\_vp2intersect  
flush\_lld debug\_swap  
AMD-V

|                                       |                                                                                                                                   |
|---------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------|
| Virtualization:                       |                                                                                                                                   |
| L1d cache:                            | 6 MiB (128 instances)                                                                                                             |
| L1i cache:                            | 4 MiB (128 instances)                                                                                                             |
| L2 cache:                             | 128 MiB (128 instances)                                                                                                           |
| L3 cache:                             | 256 MiB (8 instances)                                                                                                             |
| NUMA node(s):                         | 4                                                                                                                                 |
| NUMA node0 CPU(s):                    | 0-31,128-159                                                                                                                      |
| NUMA node1 CPU(s):                    | 32-63,160-191                                                                                                                     |
| NUMA node2 CPU(s):                    | 64-95,192-223                                                                                                                     |
| NUMA node3 CPU(s):                    | 96-127,224-255                                                                                                                    |
| Vulnerability Gather data sampling:   | Not affected                                                                                                                      |
| Vulnerability Itlb multihit:          | Not affected                                                                                                                      |
| Vulnerability Llftf:                  | Not affected                                                                                                                      |
| Vulnerability Mds:                    | Not affected                                                                                                                      |
| Vulnerability Meltdown:               | Not affected                                                                                                                      |
| Vulnerability Mmio stale data:        | Not affected                                                                                                                      |
| Vulnerability Reg file data sampling: | Not affected                                                                                                                      |
| Vulnerability Retbleed:               | Not affected                                                                                                                      |
| Vulnerability Spec rstack overflow:   | Not affected                                                                                                                      |
| Vulnerability Spec store bypass:      | Mitigation; Speculative Store Bypass disabled via prctl                                                                           |
| Vulnerability Spectre v1:             | Mitigation; usercopy/swapgs barriers and __user pointer sanitization                                                              |
| Vulnerability Spectre v2:             | Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP always-on; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected |
| Vulnerability Srbds:                  | Not affected                                                                                                                      |
| Vulnerability Tsx async abort:        | Not affected                                                                                                                      |

```
From lscpu --cache:
 NAME ONE-SIZE ALL-SIZE WAYS TYPE LEVEL SETS PHY-LINE COHERENCY-SIZE
 L1d 48K 6M 12 Data 1 64 1 64
 L1i 32K 4M 8 Instruction 1 64 1 64
 L2 1M 128M 16 Unified 2 1024 1 64
 L3 32M 256M 16 Unified 3 32768 1 64
```

### 8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0-31,128-159
node 0 size: 193052 MB
node 0 free: 179363 MB
node 1 cpus: 32-63,160-191
node 1 size: 193514 MB
node 1 free: 180105 MB
node 2 cpus: 64-95,192-223
node 2 size: 193471 MB
node 2 free: 180122 MB
node 3 cpus: 96-127,224-255
node 3 size: 193442 MB
node 3 free: 180144 MB
node distances:
node 0 1 2 3
 0: 10 12 12 12
 1: 12 10 12 12
 2: 12 12 10 12
 3: 12 12 12 10
```

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## Platform Notes (Continued)

9. /proc/meminfo  
MemTotal: 792043372 kB

10. who -r  
run-level 5 Jun 17 11:36

11. Systemd service manager version: systemd 255 (255.4-1ubuntu8)  
Default Target Status  
graphical degraded

12. Failed units, from systemctl list-units --state=failed  
UNIT LOAD ACTIVE SUB DESCRIPTION  
\* fwupd-refresh.service loaded failed failed Refresh fwupd metadata and update motd  
Legend: LOAD -> Reflects whether the unit definition was properly loaded.  
ACTIVE -> The high-level unit activation state, i.e. generalization of SUB.  
SUB -> The low-level unit activation state, values depend on unit type.  
1 loaded units listed.

13. Services, from systemctl list-unit-files  
STATE UNIT FILES  
enabled ModemManager apparmor apport blk-availability cloud-config cloud-final cloud-init  
cloud-init-local console-setup cron dmesg e2scrub\_reap finalrd getty@ gpu-manager  
grub-common grub-initrd-fallback keyboard-setup lvm2-monitor multipathd  
networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb  
snapd sysstat systemd-networkd systemd-networkd-wait-online systemd-pstore  
systemd-resolved systemd-timesyncd thermald ua-reboot-cmds ubuntu-advantage udisks2 ufw  
unattended-upgrades vgaauth  
enabled-runtime netplan-ovs-cleanupsystemd-fsck-root systemd-remount-fs  
disabled console-getty debug-shell iscsid nftables rsync serial-getty@ ssh  
systemd-boot-check-no-failures systemd-confext systemd-network-generator  
systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code  
systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy  
systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext  
systemd-time-wait-sync upower  
indirect systemd-sysupdate systemd-sysupdate-reboot uidd  
masked cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common

14. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=/vmlinuz-6.8.0-31-generic  
root=/dev/mapper/ubuntu--vg-ubuntu--lv  
ro  
console=tty0  
consolt=ttyS0,115200  
nohz\_full=1-127

15. cpupower frequency-info  
analyzing CPU 3:  
current policy: frequency should be within 1.50 GHz and 2.40 GHz.  
The governor "performance" may decide which speed to use  
within this range.  
boost state support:  
Supported: yes  
Active: yes  
Boost States: 0

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## Platform Notes (Continued)

Total States: 3  
Pstate-P0: 44800MHz

-----  
16. sysctl  
kernel.numa\_balancing 1  
kernel.randomize\_va\_space 0  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000  
vm.dirty\_ratio 8  
vm.dirty\_writeback\_centisecs 500  
vm.dirtytime\_expire\_seconds 43200  
vm.extfrag\_threshold 500  
vm.min\_unmapped\_ratio 1  
vm.nr\_hugepages 0  
vm.nr\_hugepages\_mempolicy 0  
vm.nr\_overcommit\_hugepages 0  
vm.swappiness 1  
vm.watermark\_boost\_factor 15000  
vm.watermark\_scale\_factor 10  
vm.zone\_reclaim\_mode 1

-----  
17. /sys/kernel/mm/transparent\_hugepage  
defrag [always] defer defer+madvise madvise never  
enabled [always] madvise never  
hpage\_pmd\_size 2097152  
shmem\_enabled always within\_size advise [never] deny force

-----  
18. /sys/kernel/mm/transparent\_hugepage/khugepaged  
alloc\_sleep\_millisecs 60000  
defrag 1  
max\_ptes\_none 511  
max\_ptes\_shared 256  
max\_ptes\_swap 64  
pages\_to\_scan 4096  
scan\_sleep\_millisecs 10000

-----  
19. OS release  
From /etc/\*-release /etc/\*-version  
os-release Ubuntu 24.04 LTS

-----  
20. Disk information  
SPEC is set to: /home/cpu2017  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/mapper/ubuntu--vg-ubuntu--lv ext4 196G 18G 169G 10% /

-----  
21. /sys/devices/virtual/dmi/id  
Vendor: XFUSION  
Product: 2158H V8  
Product Family: Turin  
Serial: 2106185194XFM2506130

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## Platform Notes (Continued)

### 22. dmidecode

Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

#### Memory:

12x SK Hynix HMCG94AHBRA480N 64 GB 2 rank 6400

### 23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: XFUSION  
BIOS Version: 00.13.02.12  
BIOS Date: 05/14/2025  
BIOS Revision: 2.12

## Compiler Version Notes

=====

C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++, C, Fortran | 507.cactusBSSN\_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

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# SPEC CPU®2017 Floating Point Rate Result

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## xFusion

FusionServer 2158H V8  
(AMD EPYC 9745)

SPECrate®2017\_fp\_base = 1040

SPECrate®2017\_fp\_peak = 1100

CPU2017 License: 6488

Test Date: Jun-2025

Test Sponsor: xFusion

Hardware Availability: Apr-2025

Tested by: xFusion

Software Availability: Oct-2024

## Compiler Version Notes (Continued)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang



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Test Sponsor: xFusion

Tested by: xFusion

Test Date: Jun-2025

Hardware Availability: Apr-2025

Software Availability: Oct-2024

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_CASE\_FLAG -Mbyteswapi -DSPEC\_LP64  
526.blender\_r: -funsigned-char -DSPEC\_LP64  
527.cam4\_r: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
```

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## xFusion

FusionServer 2158H V8  
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SPECrate®2017\_fp\_base = 1040

SPECrate®2017\_fp\_peak = 1100

CPU2017 License: 6488

Test Date: Jun-2025

Test Sponsor: xFusion

Hardware Availability: Apr-2025

Tested by: xFusion

Software Availability: Oct-2024

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

-lflang -ldl

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

## Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

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## xFusion

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Tested by: xFusion

SPECrate®2017\_fp\_base = 1040

SPECrate®2017\_fp\_peak = 1100

Test Date: Jun-2025

Hardware Availability: Apr-2025

Software Availability: Oct-2024

## Base Other Flags (Continued)

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags



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SPECrate®2017\_fp\_base = 1040

SPECrate®2017\_fp\_peak = 1100

Test Date: Jun-2025

Hardware Availability: Apr-2025

Software Availability: Oct-2024

## Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

538.imagick\_r: Same as 519.lbm\_r

```
544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

C++ benchmarks:

```
508.namd_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

510.parest\_r: basepeak = yes

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

```
549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdaloc -ldl -lflang
```

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Tested by: xFusion

SPECrate®2017\_fp\_base = 1040

SPECrate®2017\_fp\_peak = 1100

Test Date: Jun-2025

Hardware Availability: Apr-2025

Software Availability: Oct-2024

## Peak Optimization Flags (Continued)

554.roms\_r: basepeak = yes

Benchmarks using both Fortran and C:

```
521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-epilog-vectorization-of-inductions -lamdlibm -lamdalloc
-lld -lflang
```

527.cam4\_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm
-lamdalloc -lld
```

```
526.blender_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100 -lamdlibm -lamdalloc -lld
```

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes



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Tested by: xFusion

SPECrate®2017\_fp\_base = 1040

SPECrate®2017\_fp\_peak = 1100

Test Date: Jun-2025

Hardware Availability: Apr-2025

Software Availability: Oct-2024

## Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-AMD-V1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-AMD-V1.6.xml>

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