



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL345 Gen12
(2.25 GHz, AMD EPYC 9965)

SPECrate®2017_fp_base = 1090

SPECrate®2017_fp_peak = 1090

CPU2017 License: 3

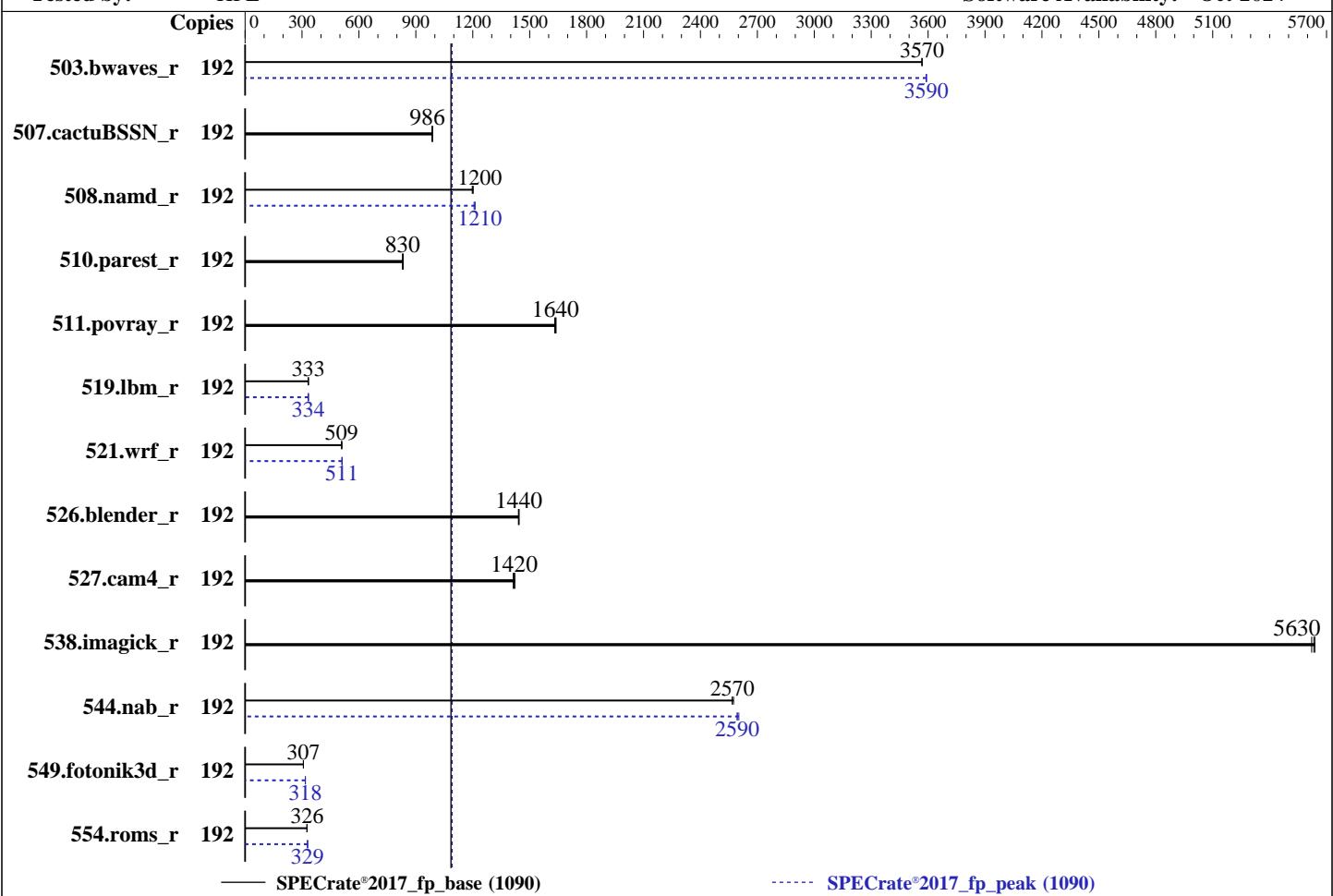
Test Date: Jul-2025

Test Sponsor: HPE

Hardware Availability: Aug-2025

Tested by: HPE

Software Availability: Oct-2024



Hardware	
CPU Name:	AMD EPYC 9965
Max MHz:	3700
Nominal:	2250
Enabled:	192 cores, 1 chip, 2 threads/core
Orderable:	1 chip
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	1 MB I+D on chip per core
L3:	384 MB I+D on chip per chip, 32 MB shared / 16 cores
Other:	None
Memory:	768 GB (12 x 64 GB 2Rx4 PC5-6400B-R, running at 5200)
Storage:	1 x 2.9 TB NVMe SSD
Other:	CPU Cooling: Air

Software	
OS:	SUSE Linux Enterprise Server 15 SP6
Compiler:	Kernel 6.4.0-150600.21-default
Parallel:	C/C++/Fortran: Version 5.0.0 of AOCC
Firmware:	No
File System:	HPE BIOS Version v1.10 05/27/2025 released
System State:	May-2025
Base Pointers:	xfs
Peak Pointers:	Run level 5 (multi-user)
Other:	64-bit
Power Management:	64-bit
	None
	BIOS and OS set to prefer performance at
	the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	192	540	3570	540	3570	540	3570	192	536	3590	536	3590	536	3590
507.cactusBSSN_r	192	246	986	246	988	247	986	192	246	986	246	988	247	986
508.namd_r	192	152	1200	152	1200	152	1200	192	151	1210	151	1210	150	1210
510.parest_r	192	603	833	605	830	605	830	192	603	833	605	830	605	830
511.povray_r	192	274	1640	274	1640	275	1630	192	274	1640	274	1640	275	1630
519.lbm_r	192	608	333	607	334	607	333	192	607	334	607	333	607	334
521.wrf_r	192	843	510	845	509	846	509	192	841	511	841	511	843	510
526.blender_r	192	203	1440	203	1440	203	1440	192	203	1440	203	1440	203	1440
527.cam4_r	192	236	1420	238	1410	237	1420	192	236	1420	238	1410	237	1420
538.imagick_r	192	84.7	5640	84.9	5620	84.8	5630	192	84.7	5640	84.9	5620	84.8	5630
544.nab_r	192	126	2570	126	2570	126	2570	192	125	2590	125	2590	124	2600
549.fotonik3d_r	192	2436	307	2433	308	2436	307	192	2355	318	2356	318	2356	318
554.roms_r	192	934	326	936	326	934	327	192	927	329	927	329	927	329

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.
tuned-adm profile was set to throughput-performance using "tuned-adm profile throughput-performance"

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Operating System Notes (Continued)

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
  "/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configurations : Parameters are selected in the order shown below
Workload Profile set to High Performance Compute (HPC)

Determinism Control set to Manual

Performance Determinism set to Power Deterministic

Memory Patrol Scrubbing set to Disabled

ACPI CST C2 Latency set to 18 microseconds

NUMA memory domains per socket set to Four memory domains per socket

Thermal Configuration set to Maximum Cooling

AMD Periodic Directory Rinse set to Periodic

Workload Profile set to Custom

Power Regulator set to OS Control Mode

L2 HW Prefetcher set to Disabled

The reference code/AGESA version used in this ROM is version Turin-PI 1.0.0.5

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Thu Jul 10 12:27:50 2025
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu

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Platform Notes (Continued)

```
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

-----
1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux

-----
2. w
12:27:50 up 14:21, 3 users, load average: 0.12, 0.03, 2.96
USER      TTY      FROM          LOGIN@    IDLE      JCPU      PCPU WHAT
root      pts/0    172.17.1.96    14Jun24 13.00s  1.02s  0.12s /bin/bash ./amd_rate_aocc500_znver5_A1.sh

-----
3. Username
From environment variable $USER: root

-----
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size            (kbytes, -d) unlimited
scheduling priority      (-e) 0
file size                (blocks, -f) unlimited
pending signals           (-i) 3093795
max locked memory         (kbytes, -l) 2097152
max memory size          (kbytes, -m) unlimited
open files                 (-n) 1024
pipe size                  (512 bytes, -p) 8
POSIX message queues       (bytes, -q) 819200
real-time priority        (-r) 0
stack size                (kbytes, -s) unlimited
cpu time                  (seconds, -t) unlimited
max user processes         (-u) 3093795
virtual memory              (kbytes, -v) unlimited
file locks                 (-x) unlimited

-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@pts/0
-bash
python3 ./run_fprate.py
```

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Platform Notes (Continued)

```
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.005/templogs/preenv.fprate.005.0.log --lognum 005.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

```
6. /proc/cpuinfo
model name      : AMD EPYC 9965 192-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 26
model          : 17
stepping        : 0
microcode       : 0xb101047
bugs            : sysret_ss_atrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 192 4K pages
cpu cores       : 192
siblings        : 384
1 physical ids (chips)
384 processors (hardware threads)
physical id 0: core ids 0-191
physical id 0: apicids 0-383
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

7. lscpu

```
From lscpu from util-linux 2.39.3:
Architecture:           x86_64
CPU op-mode(s):         32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 384
On-line CPU(s) list:    0-383
Vendor ID:              AuthenticAMD
BIOS Vendor ID:         Advanced Micro Devices, Inc.
Model name:              AMD EPYC 9965 192-Core Processor
BIOS Model name:         AMD EPYC 9965 192-Core Processor
CPU family:              107
CPU model:               26
CPU stepping:            17
Thread(s) per core:     2
Core(s) per socket:      192
Socket(s):               1
Stepping:                0
Frequency boost:         enabled
CPU(s) scaling MHz:     101%
CPU max MHz:             2250.0000
CPU min MHz:             1500.0000
BogoMIPS:                4493.33
Flags:                   fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                        pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                        rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                        extd_apicid aperfmpfperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                        sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
                        cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                        osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
CPU @ 2.2GHz
```

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Platform Notes (Continued)

```
perfctr_llc mwaitx cpb cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2
ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total
cqmq_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
xsaverptr rdpru wbnoinvd amd_ppin cpc_arat npt lbrv svm_lock
nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
pfthreshold avic v_vmsave_vnload vgif x2avic v_spec_ctrl vnmi
avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
movdiri movdir64b overflow_recov succor smca fsmr avx512_vp2intersect
flush_lld debug_swap
AMD-V
```

Virtualization:

L1d cache: 9 MiB (192 instances)
L1i cache: 6 MiB (192 instances)
L2 cache: 192 MiB (192 instances)
L3 cache: 384 MiB (12 instances)

NUMA node(s):

NUMA node0 CPU(s):	0-15,192-207
NUMA node1 CPU(s):	16-31,208-223
NUMA node2 CPU(s):	32-47,224-239
NUMA node3 CPU(s):	48-63,240-255
NUMA node4 CPU(s):	64-79,256-271
NUMA node5 CPU(s):	80-95,272-287
NUMA node6 CPU(s):	96-111,288-303
NUMA node7 CPU(s):	112-127,304-319
NUMA node8 CPU(s):	128-143,320-335
NUMA node9 CPU(s):	144-159,336-351
NUMA node10 CPU(s):	160-175,352-367
NUMA node11 CPU(s):	176-191,368-383

Vulnerability Gather data sampling:

Vulnerability Itlb multihit: Not affected

Vulnerability Llft: Not affected

Vulnerability Mds: Not affected

Vulnerability Meltdown: Not affected

Vulnerability Mmio stale data: Not affected

Vulnerability Reg file data sampling: Not affected

Vulnerability Retbleed: Not affected

Vulnerability Spec rstack overflow: Not affected

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization

Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP

always-on; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected

Vulnerability Srbds: Not affected

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	9M	12	Data	1	64	1	64
L1i	32K	6M	8	Instruction	1	64	1	64
L2	1M	192M	16	Unified	2	1024	1	64
L3	32M	384M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 12 nodes (0-11)

node 0 cpus: 0-15,192-207

node 0 size: 64124 MB

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Platform Notes (Continued)

```
node 0 free: 62958 MB
node 1 cpus: 16-31,208-223
node 1 size: 64503 MB
node 1 free: 63482 MB
node 2 cpus: 32-47,224-239
node 2 size: 64503 MB
node 2 free: 63657 MB
node 3 cpus: 48-63,240-255
node 3 size: 64503 MB
node 3 free: 63587 MB
node 4 cpus: 64-79,256-271
node 4 size: 64465 MB
node 4 free: 63612 MB
node 5 cpus: 80-95,272-287
node 5 size: 64503 MB
node 5 free: 63641 MB
node 6 cpus: 96-111,288-303
node 6 size: 64503 MB
node 6 free: 63667 MB
node 7 cpus: 112-127,304-319
node 7 size: 64503 MB
node 7 free: 63649 MB
node 8 cpus: 128-143,320-335
node 8 size: 64503 MB
node 8 free: 63001 MB
node 9 cpus: 144-159,336-351
node 9 size: 64503 MB
node 9 free: 63364 MB
node 10 cpus: 160-175,352-367
node 10 size: 64503 MB
node 10 free: 63636 MB
node 11 cpus: 176-191,368-383
node 11 size: 64358 MB
node 11 free: 63437 MB
node distances:
node   0   1   2   3   4   5   6   7   8   9   10  11
  0: 10  11  11  12  12  12  12  12  12  12  12  12
  1: 11  10  11  12  12  12  12  12  12  12  12  12
  2: 11  11  10  12  12  12  12  12  12  12  12  12
  3: 12  12  12  10  11  11  12  12  12  12  12  12
  4: 12  12  12  11  10  11  12  12  12  12  12  12
  5: 12  12  12  11  11  10  12  12  12  12  12  12
  6: 12  12  12  12  12  12  10  11  11  12  12  12
  7: 12  12  12  12  12  11  10  11  12  12  12  12
  8: 12  12  12  12  12  11  11  10  12  12  12  12
  9: 12  12  12  12  12  12  12  12  12  10  11  11
 10: 12  12  12  12  12  12  12  12  11  10  11  11
 11: 12  12  12  12  12  12  12  12  11  11  10  10
```

9. /proc/meminfo
MemTotal: 792042668 kB

10. who -r
run-level 5 Jun 14 05:37

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
Default Target Status

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Platform Notes (Continued)

graphical running

```
-----  
12. Services, from systemctl list-unit-files  
STATE          UNIT FILES  
enabled        ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd  
                bluetooth cron display-manager getty@ irqbalance issue-generator kbdsettings klog  
                lvm2-monitor nsqd nvmefc-boot-connections nvmf-autoconnect postfix purge-kernels rollback  
                rsyslog smartd sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6  
                wickedd-nanny wpa_supplicant  
enabled-runtime systemd-remount-fs  
disabled       NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon autofs  
autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl ca-certificates  
chrony-wait chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables  
exchange-bmc-os-info firewalld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmiev  
issue-add-ssh-keys kexec-load lummask man-db-create multipathd nfs nfs-blkmap nmb openvpn@  
ostree-remount rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@  
smartd_generate_opts smb snmpd snmptrapd speech-dispatcherd systemd-boot-check-no-failures  
systemd-confext systemd-network-generator systemd-sysext systemd-time-wait-sync  
systemd-timesyncd tuned udisks2 update-system-flatpaks upower vncserver@ wpa_supplicant@  
indirect       pcscd saned@ systemd-userdbd wickedd
```

```
-----  
13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default  
root=UUID=3e491841-6770-49cb-971b-19b95280c956  
splash=silent  
resume=/dev/disk/by-uuid/191e4613-2a6c-45aa-ad42-fb8ad9d4619a  
mitigations=auto  
quiet  
security=apparmor
```

```
-----  
14. cpupower frequency-info  
analyzing CPU 345:  
    current policy: frequency should be within 1.50 GHz and 2.25 GHz.  
        The governor "performance" may decide which speed to use  
        within this range.  
    boost state support:  
        Supported: yes  
        Active: yes
```

```
-----  
15. tuned-adm active  
Current active profile: throughput-performance
```

```
-----  
16. sysctl  
kernel.numa_balancing          1  
kernel.randomize_va_space      0  
vm.compaction_proactiveness   20  
vm.dirty_background_bytes     0  
vm.dirty_background_ratio     10  
vm.dirty_bytes                 0  
vm.dirty_expire_centisecs    3000  
vm.dirty_ratio                 8  
vm.dirty_writeback_centisecs  500  
vm.dirtytime_expire_seconds   43200  
vm.extfrag_threshold          500  
vm.min_unmapped_ratio         1
```

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```
vm.nr_hugepages          0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness             1
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode      1

-----
17. /sys/kernel/mm/transparent_hugepage
    defrag      [always] defer defer+madvise madvise never
    enabled     [always] madvise never
    hpage_pmd_size 2097152
    shmem_enabled always within_size advise [never] deny force

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs 60000
    defrag                 1
    max_ptes_none          511
    max_ptes_shared         256
    max_ptes_swap           64
    pages_to_scan           4096
    scan_sleep_millisecs   10000

-----
19. OS release
    From /etc/*-release /etc/*-version
    os-release SUSE Linux Enterprise Server 15 SP6

-----
20. Disk information
    SPEC is set to: /home/cpu2017
    Filesystem      Type  Size  Used Avail Use% Mounted on
    /dev/nvme0n1p3  xfs   2.2T  64G  2.1T   3% /home

-----
21. /sys/devices/virtual/dmi/id
    Vendor:          HPE
    Product:         HPE ProLiant Compute DL345 Gen12
    Product Family:  ProLiant
    Serial:          SANJACSCM

-----
22. dmidecode
    Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
    The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
    "DMTF SMBIOS" standard.
    Memory:
        12x Hynix HMC94AHBRA480N 64 GB 2 rank 6400, configured at 5200

-----
23. BIOS
    (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:          HPE
    BIOS Version:          1.10
    BIOS Date:             05/27/2025
    BIOS Revision:         1.10
```

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Tested by: HPE

Test Date: Jul-2025

Hardware Availability: Aug-2025

Software Availability: Oct-2024

Platform Notes (Continued)

Firmware Revision: 1.13

Compiler Version Notes

=====

C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++, C, Fortran | 507.cactusBSSN_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
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AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

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Compiler Version Notes (Continued)

```
=====
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
-----
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====
```

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

```
503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
```

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Base Portability Flags (Continued)

538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
```

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument
```

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Base Other Flags (Continued)

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fsto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
```

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Peak Optimization Flags (Continued)

519.lbm_r (continued):

```
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdaloc -ldl
```

538.imagick_r: basepeak = yes

```
544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand  
-fenable-aggressive-gather -Ofast -march=znver5  
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7  
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdaloc -ldl
```

C++ benchmarks:

```
508.namd_r: -m64 -std=c++14  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-mllvm -unroll-threshold=100  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdaloc -ldl
```

510.parest_r: basepeak = yes

Fortran benchmarks:

```
503.bwaves_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-Mrecursive -mllvm -reduce-array-computations=3  
-fepilog-vectorization-of-inductions -zopt -lamdlibm  
-lamdaloc -ldl -lflang
```

```
549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-Mrecursive -mllvm -reduce-array-computations=3  
-fepilog-vectorization-of-inductions -fvector-transform  
-fscalar-transform -lamdlibm -lamdaloc -ldl -lflang
```

554.roms_r: Same as 503.bwaves_r

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fsto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdaloc
-lld -lflang
```

```
527.cam4_r: basepeak = yes
```

Benchmarks using both C and C++:

```
511.povray_r: basepeak = yes
```

```
526.blender_r: basepeak = yes
```

Benchmarks using Fortran, C, and C++:

```
507.cactuBSSN_r: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument
```

Benchmarks using both C and C++:

```
-Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-unused-command-line-argument
```



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Turin-rev1.6.html>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Turin-rev1.6.xml>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

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