



SPEC CPU®2017 Floating Point Rate Result

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xFusion

FusionServer 2158H V8
(AMD EPYC 9654)

SPECrate®2017_fp_base = 840

SPECrate®2017_fp_peak = 843

CPU2017 License: 6488

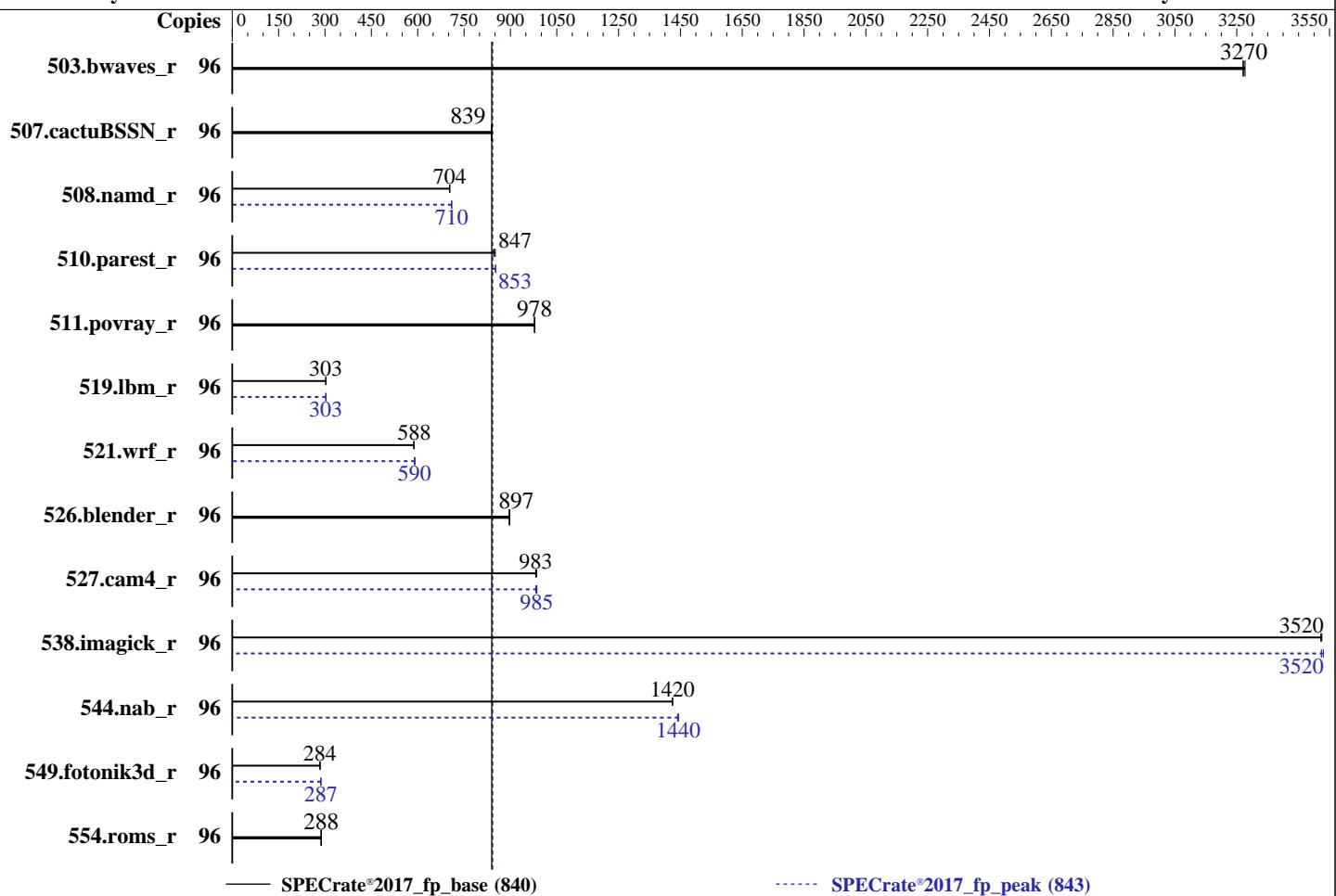
Test Date: Jul-2025

Test Sponsor: xFusion

Hardware Availability: Apr-2025

Tested by: xFusion

Software Availability: Oct-2024



Hardware

CPU Name: AMD EPYC 9654
Max MHz: 3700
Nominal: 2400
Enabled: 96 cores, 1 chip
Orderable: 1 chip
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 384 MB I+D on chip per chip, 32 MB shared / 8 cores
Other: None
Memory: 768 GB (12 x 64 GB 2Rx4 PC5-6400B-R, running at 4800)
Storage: 1 x 1.6 TB PCIe NVMe SSD
Other: CPU Cooling: Air

Software

OS: Red Hat Enterprise Linux release 9.0 (Plow) kernel version 5.14.0-70.13.1.el9_0.x86_64
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: No
Firmware: Version 00.11.01.06 released May-2025
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|-----------------------------|--------|-------------|-------------|------------|------------|-------------|------------|--------|------------|-------------|-------------|-------------|------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 503.bwaves_r | 96 | 294 | 3270 | 294 | 3280 | 294 | 3270 | 96 | 294 | 3270 | 294 | 3280 | 294 | 3270 |
| 507.cactusBSSN_r | 96 | 145 | 839 | 145 | 839 | 145 | 840 | 96 | 145 | 839 | 145 | 839 | 145 | 840 |
| 508.namd_r | 96 | 130 | 704 | 130 | 703 | 130 | 704 | 96 | 128 | 710 | 128 | 710 | 129 | 709 |
| 510.parest_r | 96 | 295 | 851 | 297 | 846 | 296 | 847 | 96 | 294 | 853 | 295 | 851 | 295 | 853 |
| 511.povray_r | 96 | 229 | 979 | 229 | 978 | 229 | 977 | 96 | 229 | 979 | 229 | 978 | 229 | 977 |
| 519.lbm_r | 96 | 334 | 303 | 334 | 303 | 334 | 303 | 96 | 334 | 303 | 334 | 303 | 334 | 303 |
| 521.wrf_r | 96 | 365 | 589 | 365 | 588 | 366 | 587 | 96 | 365 | 589 | 364 | 591 | 364 | 590 |
| 526.blender_r | 96 | 163 | 897 | 163 | 896 | 163 | 897 | 96 | 163 | 897 | 163 | 896 | 163 | 897 |
| 527.cam4_r | 96 | 171 | 982 | 171 | 983 | 170 | 985 | 96 | 170 | 985 | 171 | 982 | 170 | 986 |
| 538.imagick_r | 96 | 67.8 | 3520 | 67.8 | 3520 | 67.7 | 3530 | 96 | 67.6 | 3530 | 67.7 | 3520 | 67.8 | 3520 |
| 544.nab_r | 96 | 113 | 1420 | 113 | 1420 | 113 | 1420 | 96 | 112 | 1440 | 112 | 1440 | 112 | 1440 |
| 549.fotonik3d_r | 96 | 1317 | 284 | 1319 | 284 | 1317 | 284 | 96 | 1304 | 287 | 1305 | 287 | 1306 | 287 |
| 554.roms_r | 96 | 530 | 288 | 528 | 289 | 532 | 287 | 96 | 530 | 288 | 528 | 289 | 532 | 287 |
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

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Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib32:  
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS settings:
Determinism Control = Manual
Determinism Enable = Power
TDP Control = Manual
TDP = 400
PPT Control = Manual
PPT = 400
SMT = Disabled
ACPI SRAT L3 Cache As NUMA Domain = Enable

```
Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost.localdomain Tue Jul  8 15:08:47 2025
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

- ```
1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 250 (250-6.el9_0)
```

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## Platform Notes (Continued)

12. Services, from systemctl list-unit-files  
13. Linux kernel boot-time arguments, from /proc/cmdline  
14. cpupower frequency-info  
15. sysctl  
16. /sys/kernel/mm/transparent\_hugepage  
17. /sys/kernel/mm/transparent\_hugepage/khugepaged  
18. OS release  
19. Disk information  
20. /sys/devices/virtual/dmi/id  
21. dmidecode  
22. BIOS

---

1. uname -a  
Linux localhost.localdomain 5.14.0-70.13.1.el9\_0.x86\_64 #1 SMP PREEMPT Thu Apr 14 12:42:38 EDT 2022 x86\_64  
x86\_64 x86\_64 GNU/Linux

---

2. w  
15:08:47 up 16 min, 1 user, load average: 0.00, 0.01, 0.00  
USER TTY LOGIN@ IDLE JCPU PCPU WHAT  
root pts/0 14:56 1:03 0.93s 0.00s tail -100f nohup.out

---

3. Username  
From environment variable \$USER: root

---

4. ulimit -a  
real-time non-blocking time (microseconds, -R) unlimited  
core file size (blocks, -c) 0  
data seg size (kbytes, -d) unlimited  
scheduling priority (-e) 0  
file size (blocks, -f) unlimited  
pending signals (i) 3092400  
max locked memory (kbytes, -l) 2097152  
max memory size (kbytes, -m) unlimited  
open files (-n) 1024  
pipe size (512 bytes, -p) 8  
POSIX message queues (bytes, -q) 819200  
real-time priority (-r) 0  
stack size (kbytes, -s) unlimited  
cpu time (seconds, -t) unlimited  
max user processes (u) 3092400  
virtual memory (kbytes, -v) unlimited  
file locks (-x) unlimited

---

5. sysinfo process ancestry  
/usr/lib/systemd/systemd --switched-root --system --deserialize 18  
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups  
sshd: root [priv]  
sshd: root@pts/0  
-bash  
python3 ./run\_amd\_rate\_aocc500\_znver5\_A1.py  
/bin/bash ./amd\_rate\_aocc500\_znver5\_A1.sh  
runcpu --config amd\_rate\_aocc500\_znver5\_A1.cfg --tune all --reportable --iterations 3 fprate  
runcpu --configfile amd\_rate\_aocc500\_znver5\_A1.cfg --tune all --reportable --iterations 3 --nopower  
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile

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## Platform Notes (Continued)

```
$SPEC/tmp/CPU2017.001/templogs/preenv.fprate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

```

6. /proc/cpuinfo
model name : AMD EPYC 9654 96-Core Processor
vendor_id : AuthenticAMD
cpu family : 25
model : 17
stepping : 1
microcode : 0xa101154
bugs : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size : 3584 4K pages
cpu cores : 96
siblings : 96
1 physical ids (chips)
96 processors (hardware threads)
physical id 0: core_ids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183
physical id 0: apic_ids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

```

7. lscpu
```

From lscpu from util-linux 2.37.4:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 52 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95
Vendor ID: AuthenticAMD
BIOS Vendor ID: Advanced Micro Devices, Inc.
Model name: AMD EPYC 9654 96-Core Processor
BIOS Model name: AMD EPYC 9654 96-Core Processor
CPU family: 25
Model: 17
Thread(s) per core: 1
Core(s) per socket: 96
Socket(s): 1
Stepping: 1
Frequency boost: enabled
CPU max MHz: 3707.8120
CPU min MHz: 1500.0000
BogoMIPS: 4799.79
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
 constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmpfperf rapl
 pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
 popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy
 abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext
 perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3
 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmil
 avx2 smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap
 avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
 xsaves xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local
 avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin arat npt lbrv
 svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
```

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## Platform Notes (Continued)

```
pausefilter pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi
umip pkv ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_lld
```

### Virtualization:

|            |                        |
|------------|------------------------|
| L1d cache: | 3 MiB (96 instances)   |
| L1i cache: | 3 MiB (96 instances)   |
| L2 cache:  | 96 MiB (96 instances)  |
| L3 cache:  | 384 MiB (12 instances) |

### NUMA node(s):

|                     |       |
|---------------------|-------|
| NUMA node0 CPU(s):  | 0-7   |
| NUMA node1 CPU(s):  | 8-15  |
| NUMA node2 CPU(s):  | 16-23 |
| NUMA node3 CPU(s):  | 24-31 |
| NUMA node4 CPU(s):  | 32-39 |
| NUMA node5 CPU(s):  | 40-47 |
| NUMA node6 CPU(s):  | 48-55 |
| NUMA node7 CPU(s):  | 56-63 |
| NUMA node8 CPU(s):  | 64-71 |
| NUMA node9 CPU(s):  | 72-79 |
| NUMA node10 CPU(s): | 80-87 |
| NUMA node11 CPU(s): | 88-95 |

### Vulnerability Itlb multihit:

Not affected

### Vulnerability Llrf:

Not affected

### Vulnerability Mds:

Not affected

### Vulnerability Meltdown:

Not affected

### Vulnerability Spec store bypass:

Mitigation; Speculative Store Bypass disabled via prctl

### Vulnerability Spectre v1:

Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization

### Vulnerability Spectre v2:

Mitigation; Retpolines, IBPB conditional, IBRS\_FW, STIBP disabled, RSB

### Vulnerability Srbds:

Not affected

### Vulnerability Tsx async abort:

Not affected

### From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE        | LEVEL | SETS  | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|-------|----------|----------------|
| L1d  | 32K      | 3M       | 8    | Data        | 1     | 64    | 1        | 64             |
| L1i  | 32K      | 3M       | 8    | Instruction | 1     | 64    | 1        | 64             |
| L2   | 1M       | 96M      | 8    | Unified     | 2     | 2048  | 1        | 64             |
| L3   | 32M      | 384M     | 16   | Unified     | 3     | 32768 | 1        | 64             |

### 8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 12 nodes (0-11)

node 0 cpus: 0-7

node 0 size: 63679 MB

node 0 free: 63185 MB

node 1 cpus: 8-15

node 1 size: 64510 MB

node 1 free: 64236 MB

node 2 cpus: 16-23

node 2 size: 64510 MB

node 2 free: 64253 MB

node 3 cpus: 24-31

node 3 size: 64510 MB

node 3 free: 64300 MB

node 4 cpus: 32-39

node 4 size: 64473 MB

node 4 free: 64247 MB

node 5 cpus: 40-47

node 5 size: 64510 MB

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## Platform Notes (Continued)

```
node 5 free: 64302 MB
node 6 cpus: 48-55
node 6 size: 64510 MB
node 6 free: 64298 MB
node 7 cpus: 56-63
node 7 size: 64510 MB
node 7 free: 64284 MB
node 8 cpus: 64-71
node 8 size: 64510 MB
node 8 free: 64287 MB
node 9 cpus: 72-79
node 9 size: 64510 MB
node 9 free: 64298 MB
node 10 cpus: 80-87
node 10 size: 64510 MB
node 10 free: 64306 MB
node 11 cpus: 88-95
node 11 size: 64459 MB
node 11 free: 64231 MB
node distances:
node 0 1 2 3 4 5 6 7 8 9 10 11
 0: 10 11 11 12 12 12 12 12 12 12 12 12
 1: 11 10 11 12 12 12 12 12 12 12 12 12
 2: 11 11 10 12 12 12 12 12 12 12 12 12
 3: 12 12 12 10 11 11 12 12 12 12 12 12
 4: 12 12 12 11 10 11 12 12 12 12 12 12
 5: 12 12 12 11 11 10 12 12 12 12 12 12
 6: 12 12 12 12 12 12 10 11 11 12 12 12
 7: 12 12 12 12 12 12 11 10 11 12 12 12
 8: 12 12 12 12 12 12 11 11 10 12 12 12
 9: 12 12 12 12 12 12 12 12 12 10 11 11
10: 12 12 12 12 12 12 12 12 12 11 10 11
11: 12 12 12 12 12 12 12 12 11 11 11 10
```

```

9. /proc/meminfo
MemTotal: 791761252 kB
```

```

10. who -r
run-level 3 Jul 8 14:52
```

```

11. Systemd service manager version: systemd 250 (250-6.el9_0)
Default Target Status
multi-user running
```

```

12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online audited crond
 dbus-broker firewalld getty@ irqbalance kdump lvm2-monitor mdmonitor microcode
 nis-domainname rhsmcertd rsyslog selinux-autorelabel-mark sshd sssd
 systemd-network-generator udisks2 upower
enabled-runtime systemd-remount-fs
disabled blk-availability canberra-system-bootup canberra-system-shutdown
 canberra-system-shutdown-reboot console-getty cpupower debug-shell hwloc-dump-hwdata
 kvm_stat man-db-restart-cache-update nftables rdisc rhsm rhsm-facts rpmbdb-rebuild
 serial-getty@ sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysext
indirect sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo
```

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## Platform Notes (Continued)

```
13. Linux kernel boot-time arguments, from /proc/cmdline
 BOOT_IMAGE=(hd1,gpt2)/vmlinuz-5.14.0-70.13.1.e19_0.x86_64
 root=/dev/mapper/rhel-root
 ro
 crashkernel=1G-4G:192M,4G-64G:256M,64G-:512M
 resume=/dev/mapper/rhel-swap
 rd.lvm.lv=rhel/root
 rd.lvm.lv=rhel/swap
```

```
14. cpupower frequency-info
analyzing CPU 0:
 current policy: frequency should be within 1.50 GHz and 2.40 GHz.
 The governor "performance" may decide which speed to use
 within this range.

 boost state support:
 Supported: yes
 Active: yes
 Boost States: 0
 Total States: 3
 Pstate-P0: 2400MHz
```

```
15. sysctl
kernel.numa_balancing 1
kernel.randomize_va_space 0
vm.compression_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 8
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 1
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 1
```

```
16. /sys/kernel/mm/transparent_hugepage
 defrag [always] defer defer+madvise madvice never
 enabled [always] madvice never
 hpage_pmd_size 2097152
 shmem_enabled always within_size advise [never] deny force
```

```
17. /sys/kernel/mm/transparent_hugepage/khugepaged
 alloc_sleep_millisecs 60000
 defrag 1
 max_ptes_none 511
 max_ptes_shared 256
 max_ptes_swap 64
```

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## Platform Notes (Continued)

```
pages_to_scan 4096
scan_sleep_millisecs 10000
```

```

18. OS release
From /etc/*-release /etc/*-version
os-release Red Hat Enterprise Linux 9.0 (Plow)
redhat-release Red Hat Enterprise Linux release 9.0 (Plow)
system-release Red Hat Enterprise Linux release 9.0 (Plow)
```

```

19. Disk information
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 1.0T 12G 1012G 2% /home
```

```

20. /sys/devices/virtual/dmi/id
Product: 2158H V8
Product Family: Genoa
```

```

21. dmidecode
Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
 12x SK Hynix HMCG94AHBRA485N 64 GB 2 rank 6400, configured at 4800
```

```

22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: INSYDE Corp.
BIOS Version: 00.11.01.06
BIOS Date: 05/14/2025
BIOS Revision: 0.11
```

## Compiler Version Notes

```
=====
C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
```

```
=====
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
```

```
=====
C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)
```

```
=====
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
```

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## xFusion

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(AMD EPYC 9654)

CPU2017 License: 6488

Test Sponsor: xFusion

Tested by: xFusion

SPECrate®2017\_fp\_base = 840

SPECrate®2017\_fp\_peak = 843

Test Date: Jul-2025

Hardware Availability: Apr-2025

Software Availability: Oct-2024

## Compiler Version Notes (Continued)

```
=====
C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

```

```
=====
C++, C, Fortran | 507.cactuBSSN_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

```

```
=====
Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

```

```
=====
Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

```

## Base Compiler Invocation

C benchmarks:  
clang

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## xFusion

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Tested by: xFusion

SPECrate®2017\_fp\_base = 840

SPECrate®2017\_fp\_peak = 843

Test Date: Jul-2025

Hardware Availability: Apr-2025

Software Availability: Oct-2024

## Base Compiler Invocation (Continued)

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
526.blender\_r: -funsigned-char -DSPEC\_LP64  
527.cam4\_r: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3  
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc  
-lflang -ldl

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SPECrate®2017\_fp\_base = 840

SPECrate®2017\_fp\_peak = 843

Test Date: Jul-2025

Hardware Availability: Apr-2025

Software Availability: Oct-2024

## Base Optimization Flags (Continued)

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdaloc
-lflang -ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdaloc
-lflang -ldl
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdaloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdaloc -lflang
-ldl
```

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# SPEC CPU®2017 Floating Point Rate Result

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## xFusion

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SPECrate®2017\_fp\_base = 840

SPECrate®2017\_fp\_peak = 843

CPU2017 License: 6488

Test Date: Jul-2025

Test Sponsor: xFusion

Hardware Availability: Apr-2025

Tested by: xFusion

Software Availability: Oct-2024

## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -flang -ldl
```

## Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument
```

Benchmarks using both C and C++:

```
-Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-unused-command-line-argument
```

## Peak Compiler Invocation

C benchmarks:

```
clang
```

C++ benchmarks:

```
clang++
```

Fortran benchmarks:

```
flang
```

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Tested by: xFusion

SPECrate®2017\_fp\_base = 840

SPECrate®2017\_fp\_peak = 843

Test Date: Jul-2025

Hardware Availability: Apr-2025

Software Availability: Oct-2024

## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fsto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

538.imagick\_r: Same as 519.lbm\_r

```
544.nab_r: -m64 -fsto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

C++ benchmarks:

```
508.namd_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
```

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SPECrate®2017\_fp\_base = 840

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Test Date: Jul-2025

Hardware Availability: Apr-2025

Software Availability: Oct-2024

## Peak Optimization Flags (Continued)

508.namd\_r (continued):

```
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

510.parest\_r: -m64 -std=c++14 -flto -Wl,-mllvm -Wl,-suppress-fmas
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdaloc -ldl -lflang

554.roms\_r: basepeak = yes

Benchmarks using both Fortran and C:

521.wrf\_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdaloc
-ldl -lflang

527.cam4\_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50

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Tested by: xFusion

SPECrate®2017\_fp\_base = 840

SPECrate®2017\_fp\_peak = 843

Test Date: Jul-2025

Hardware Availability: Apr-2025

Software Availability: Oct-2024

## Peak Optimization Flags (Continued)

527.cam4\_r (continued):

```
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdaloc
-lldl -lflang
```

Benchmarks using both C and C++:

511.povray\_r: basepeak = yes

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes

## Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-AMD-V1.6.html>



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## xFusion

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**Test Date:** Jul-2025

**Test Sponsor:** xFusion

**Hardware Availability:** Apr-2025

**Tested by:** xFusion

**Software Availability:** Oct-2024

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-AMD-V1.6.xml>

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