



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge XE7745 (AMD EPYC 9845 160-Core Processor)

SPECrate®2017_fp_base = 2040

SPECrate®2017_fp_peak = 2050

CPU2017 License: 6573

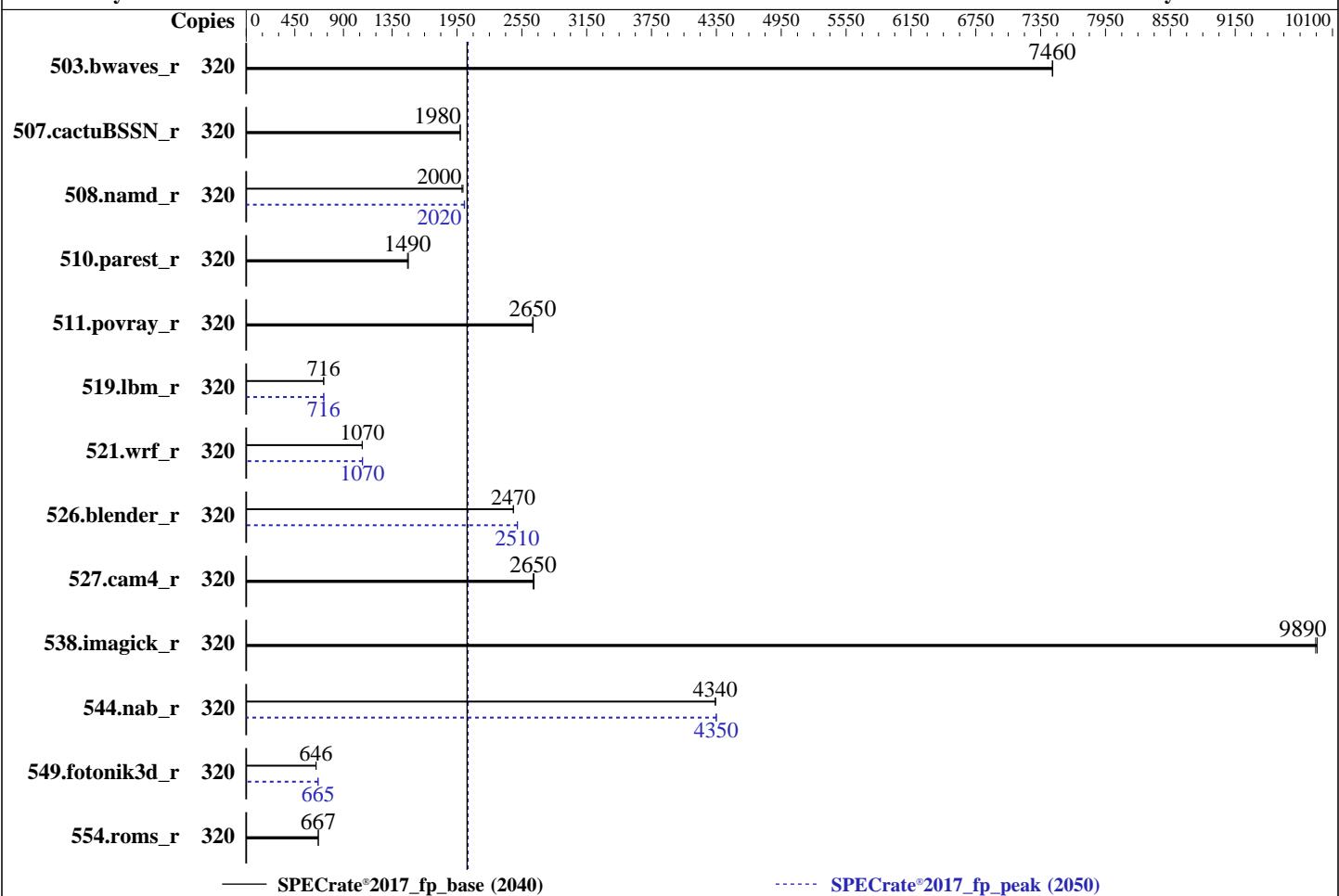
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jul-2025

Hardware Availability: Jul-2025

Software Availability: Jun-2025



— SPECrate®2017_fp_base (2040)

- - - - - SPECrate®2017_fp_peak (2050)

Hardware

CPU Name: AMD EPYC 9845
 Max MHz: 3700
 Nominal: 2100
 Enabled: 320 cores, 2 chips
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 320 MB I+D on chip per chip, 32 MB shared / 16 cores
 Other: None
 Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R)
 Storage: 160 GB on tmpfs
 Other: CPU Cooling: DLC

Software

OS: Ubuntu 24.04 LTS
 Compiler: 6.8.0-63-generic
 C/C++/Fortran: Version 5.0.0 of AOCC
 Parallel: No
 Firmware: Version 1.2.1 released Jun-2025
 File System: tmpfs
 System State: Run level 5 (graphical multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: None
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	320	430	7460	430	7460			320	430	7460	430	7460		
507.cactusBSSN_r	320	204	1980	205	1980			320	204	1980	205	1980		
508.namd_r	320	152	2000	152	2010			320	151	2020	150	2020		
510.parest_r	320	558	1500	561	1490			320	558	1500	561	1490		
511.povray_r	320	281	2660	282	2650			320	281	2660	282	2650		
519.lbm_r	320	470	718	471	716			320	471	716	469	719		
521.wrf_r	320	668	1070	668	1070			320	666	1080	667	1070		
526.blender_r	320	197	2470	197	2470			320	194	2510	194	2510		
527.cam4_r	320	210	2660	211	2650			320	210	2660	211	2650		
538.imagick_r	320	80.4	9890	80.3	9910			320	80.4	9890	80.3	9910		
544.nab_r	320	124	4340	124	4340			320	124	4350	124	4350		
549.fotonik3d_r	320	1930	646	1929	647			320	1876	665	1875	665		
554.roms_r	320	763	667	763	667			320	763	667	763	667		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
 'numactl' was used to bind copies to the cores.
 See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
 To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
 To free node-local memory and avoid remote memory usage,
 'sysctl -w vm.zone_reclaim_mode=1' run as root.
 To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
 To disable address space layout randomization (ASLR) to reduce run-to-run
 variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

(Continued on next page)



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Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4/amd_rate_aocc500_znver5_A_lib/lib:/mnt/ramdisk/cpu2017  
    -1.1.9-aocc500-znerv5_A1.4/amd_rate_aocc500_znver5_A_lib/lib32:  
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

Benchmark run from a 160 GB ramdisk created with the cmd: "mount -t tmpfs -o size=160G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:

```
Logical Processor : Disabled  
Virtualization Technology : Disabled  
NUMA Nodes Per Socket : 2  
  
System Profile : Custom  
C-States : Disabled  
Memory Patrol Scrub : Disabled  
PCI ASPM L1 Link Power Management : Disabled  
Periodic Directory Rinse Tuning : Blended  
Determinism Control : Manual  
Determinism Slider : Power Determinism  
Optimizer Mode : Enabled  
Adaptive Allocation : Enabled  
Dram Refresh Delay : Performance  
DIMM Self Healing -  
on Uncorrectable Memory Error : Disabled
```

```
Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on 1234567-XE7745 Thu Jul 17 12:33:39 2025
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu

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Platform Notes (Continued)

```
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.8)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. tuned-adm active
17. sysctl
18. /sys/kernel/mm/transparent_hugepage
19. /sys/kernel/mm/transparent_hugepage/khugepaged
20. OS release
21. Disk information
22. /sys/devices/virtual/dmi/id
23. dmidecode
24. BIOS
-----
-----
1. uname -a
Linux 1234567-XE7745 6.8.0-63-generic #66-Ubuntu SMP PREEMPT_DYNAMIC Fri Jun 13 20:25:30 UTC 2025 x86_64
x86_64 x86_64 GNU/Linux
-----
2. w
12:33:39 up 2:51, 2 users, load average: 192.29, 287.40, 305.78
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root 100.71.228.68 09:44 43days 0.00s 0.05s sshd: root@notty
root ttym1 - 09:44 2:48m 1.89s 0.63s /bin/bash ./amd_rate_aocc500_znver5_A1.sh
-----
3. Username
From environment variable $USER: root
-----
4. ulimit -a
time(seconds) unlimited
file(blocks) unlimited
data(kbytes) unlimited
stack(kbytes) unlimited
coredump(blocks) 0
memory(kbytes) unlimited
locked memory(kbytes) 2097152
process 6188744
nofiles 1024
vmemory(kbytes) unlimited
locks unlimited
rtprio 0
-----
5. sysinfo process ancestry
/sbin/init
/bin/login -f --
-bash
/bin/bash /home/DellFiles/bin/DELL_rate.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh rate --define DL-VERS=6.3a --output_format
html,txt
```

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Platform Notes (Continued)

```
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define DL-VERS=6.3a
--output_format html,pdf,txt fprate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
DL-VERS=6.3a --output_format html,pdf,txt --nopower --runmode rate --tune base:peak --size
test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4
```

```
-----  
6. /proc/cpuinfo  
model name      : AMD EPYC 9845 160-Core Processor  
vendor_id       : AuthenticAMD  
cpu family     : 26  
model          : 17  
stepping        : 0  
microcode       : 0xb101047  
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass  
TLB size        : 192 4K pages  
cpu cores       : 160  
siblings        : 160  
2 physical ids (chips)  
320 processors (hardware threads)  
physical id 0: core ids 0-159  
physical id 1: core ids 0-159  
physical id 0: apicids 0-159  
physical id 1: apicids 256-415
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

```
-----  
7. lscpu
```

```
From lscpu from util-linux 2.39.3:  
Architecture:           x86_64  
CPU op-mode(s):        32-bit, 64-bit  
Address sizes:         52 bits physical, 57 bits virtual  
Byte Order:            Little Endian  
CPU(s):                320  
On-line CPU(s) list:   0-319  
Vendor ID:             AuthenticAMD  
BIOS Vendor ID:       AMD  
Model name:            AMD EPYC 9845 160-Core Processor  
BIOS Model name:      AMD EPYC 9845 160-Core Processor  
CPU family:            107  
Model:                 26  
Thread(s) per core:    1  
Core(s) per socket:    160  
Socket(s):             2  
Stepping:              0  
Frequency boost:       enabled  
CPU(s) scaling MHz:   57%  
CPU max MHz:          3718.0659  
CPU min MHz:          1500.0000  
BogoMIPS:              4194.39  
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat  
                           pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
```

CPU @ 2.1GHz

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Platform Notes (Continued)

```

rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
extd_apicid aperfmpfperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnopprefetch
osw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_llc mwaitx cpb cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2
ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bml1 avx2
smep bmi2 invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total
cqmq_mbm_local user_shstk avx_vnni avx512_bf16 clzero iperf
xsaveerptr rdpru wbnoinvd amd_ppin cppc amd_ibpb_ret arat npt lbrv
svm_lock nrrip_save tsc_scale vmcb_clean flushbyasid decodeassists
pausefilter pfthreshold avic v_vmsave_vmlload vgif x2avic v_spec_ctrl
vnmi avx512vbmi umip pku ospke avx512_vbmi2 gfnr vaes vpcimulqdq
avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
movdiri movdir64b overflow_recov succor smca avx512_vp2intersect
flush_ll1d debug_swap

```

L1d cache: 15 MiB (320 instances)
L1i cache: 10 MiB (320 instances)
L2 cache: 320 MiB (320 instances)
L3 cache: 640 MiB (20 instances)

NUMA node(s): 4

NUMA node0 CPU(s): 0-79

NUMA node1 CPU(s): 80-159

NUMA node2 CPU(s): 160-239

NUMA node3 CPU(s): 240-319

Vulnerability Gather data sampling: Not affected

Vulnerability Itlb multihit: Not affected

Vulnerability Lltf: Not affected

Vulnerability Mds: Not affected

Vulnerability Meltdown: Not affected

Vulnerability Mmio stale data: Not affected

Vulnerability Reg file data sampling: Not affected

Vulnerability Retbleed: Not affected

Vulnerability Spec rstack overflow: Not affected

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization

Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP

disabled; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected

Vulnerability Srbds: Not affected

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	15M	12	Data	1	64	1	64
L1i	32K	10M	8	Instruction	1	64	1	64
L2	1M	320M	16	Unified	2	1024	1	64
L3	32M	640M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)

node 0 cpus: 0-79

node 0 size: 386262 MB

node 0 free: 379190 MB

node 1 cpus: 80-159

node 1 size: 387018 MB

node 1 free: 383777 MB

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Platform Notes (Continued)

```
node 2 cpus: 160-239
node 2 size: 387034 MB
node 2 free: 383849 MB
node 3 cpus: 240-319
node 3 size: 386948 MB
node 3 free: 383810 MB
node distances:
node 0 1 2 3
 0: 10 12 32 32
 1: 12 10 32 32
 2: 32 32 10 12
 3: 32 32 12 10

-----
9. /proc/meminfo
MemTotal:      1584398432 kB

-----
10. who -r
run-level 5 Jul 17 09:44

-----
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.8)
Default Target     Status
graphical          degraded

-----
12. Failed units, from systemctl list-units --state=failed
   UNIT           LOAD    ACTIVE SUB     DESCRIPTION
* systemd-networkd-wait-online.service loaded failed failed Wait for Network to be Configured
Legend: LOAD  -> Reflects whether the unit definition was properly loaded.
        ACTIVE -> The high-level unit activation state, i.e. generalization of SUB.
        SUB   -> The low-level unit activation state, values depend on unit type.
1 loaded units listed.

-----
13. Services, from systemctl list-unit-files
STATE          UNIT FILES
enabled        ModemManager apparmor apport blk-availability cloud-config cloud-final cloud-init
                cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager
                grub-common grub-initrd-fallback keyboard-setup lm-sensors lvm2-monitor multipathd
                networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb
                sysstat systemd-networkd systemd-networkd-wait-online systemd-pstore systemd-resolved
                systemd-timesyncd thermald tuned ua-reboot-cmds ubuntu-advantage udisks2 ufw vgaauth
enabled-runtime netplan-ovs-cleanups systemd-fsck-root systemd-remount-fs
disabled       console-getty debug-shell iscsid nftables rsync serial-getty@ ssh
                systemd-boot-check-no-failures systemd-confext systemd-network-generator
                systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code
                systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy
                systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext
                systemd-time-wait-sync upower
indirect       systemd-sysupdate systemd-sysupdate-reboot uuidd
masked        cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common

-----
14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/vmlinuz-6.8.0-63-generic
root=/dev/mapper/ubuntu--vg-ubuntu--lv
ro
```

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Platform Notes (Continued)

```
-----  
15. cpupower frequency-info  
analyzing CPU 309:  
    current policy: frequency should be within 1.50 GHz and 2.10 GHz.  
        The governor "performance" may decide which speed to use  
        within this range.  
    boost state support:  
        Supported: yes  
        Active: yes  
        Boost States: 0  
        Total States: 3  
        Pstate-P0: 2100MHz
```

```
-----  
16. tuned-adm active  
Current active profile: latency-performance
```

```
-----  
17. sysctl  
kernel.numa_balancing          1  
kernel.randomize_va_space       0  
vm.compaction_proactiveness   20  
vm.dirty_background_bytes      0  
vm.dirty_background_ratio      3  
vm.dirty_bytes                 0  
vm.dirty_expire_centisecs     3000  
vm.dirty_ratio                 8  
vm.dirty_writeback_centisecs  500  
vm.dirtytime_expire_seconds   43200  
vm.extfrag_threshold          500  
vm.min_unmapped_ratio         1  
vm.nr_hugepages                0  
vm.nr_hugepages_mempolicy      0  
vm.nr_overcommit_hugepages    0  
vm.swappiness                  1  
vm.watermark_boost_factor     15000  
vm.watermark_scale_factor      10  
vm.zone_reclaim_mode          1
```

```
-----  
18. /sys/kernel/mm/transparent_hugepage  
defrag           [always] defer defer+madvise madvise never  
enabled          [always] madvise never  
hpage_pmd_size  2097152  
shmem_enabled   always within_size advise [never] deny force
```

```
-----  
19. /sys/kernel/mm/transparent_hugepage/khugepaged  
alloc_sleep_millisecs  60000  
defrag                 1  
max_ptes_none          511  
max_ptes_shared         256  
max_ptes_swap           64  
pages_to_scan           4096  
scan_sleep_millisecs   10000
```

```
-----  
20. OS release  
From /etc/*-release /etc/*-version  
os-release Ubuntu 24.04 LTS
```

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Platform Notes (Continued)

21. Disk information

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 160G 3.3G 157G 3% /mnt/ramdisk

22. /sys/devices/virtual/dmi/id

Vendor: Dell Inc.
Product: PowerEdge XE7745
Product Family: PowerEdge
Serial: 1234567

23. dmidecode

Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

24x 80AD000080AD HMC94AHBRA480N 64 GB 2 rank 6400

24. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Dell Inc.
BIOS Version: 1.2.1
BIOS Date: 06/23/2025
BIOS Revision: 1.2

Compiler Version Notes

=====

C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

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Hardware Availability: Jul-2025

Tested by: Dell Inc.

Software Availability: Jun-2025

Compiler Version Notes (Continued)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
C++, C, Fortran | 507.cactuBSSN_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

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Dell Inc.

PowerEdge XE7745 (AMD EPYC 9845 160-Core Processor)

SPECrate®2017_fp_base = 2040

SPECrate®2017_fp_peak = 2050

CPU2017 License: 6573

Test Date: Jul-2025

Test Sponsor: Dell Inc.

Hardware Availability: Jul-2025

Tested by: Dell Inc.

Software Availability: Jun-2025

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactubSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

C++ benchmarks:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3

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Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner  
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdaloc  
-lflang -ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching  
-Wl,-mllvm -Wl,-enable-aggressive-gather=true  
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3  
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdaloc  
-lflang -ldl
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching  
-Wl,-mllvm -Wl,-enable-aggressive-gather=true  
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions  
-lamdlibm -lamdaloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner  
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie  
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100  
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdaloc -lflang  
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
```

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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie  
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100  
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops  
-mllvm -lsrc-in-nested-loop -fepilog-vectorization-of-inductions  
-lamdlibm -lamdaloc -flang -ldl
```

Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument
```

Benchmarks using both C and C++:

```
-Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-unused-command-line-argument
```

Peak Compiler Invocation

C benchmarks:

```
clang
```

C++ benchmarks:

```
clang++
```

Fortran benchmarks:

```
flang
```

Benchmarks using both Fortran and C:

```
flang clang
```

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Software Availability: Jun-2025

Peak Compiler Invocation (Continued)

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -fsto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdaloc -ldl
```

```
538.imagick_r: basepeak = yes
```

```
544.nab_r: -m64 -fsto -Wl,-mllvm -Wl,-ldist-scalar-expand  
-fenable-aggressive-gather -Ofast -march=znver5  
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7  
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdaloc -ldl
```

C++ benchmarks:

```
508.namd_r: -m64 -std=c++14  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -fsto
```

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Peak Optimization Flags (Continued)

508.namd_r (continued):

```
-mllvm -unroll-threshold=100  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdalloc -ldl
```

510.parest_r: basepeak = yes

Fortran benchmarks:

503.bwaves_r: basepeak = yes

```
549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-Mrecursive -mllvm -reduce-array-computations=3  
-fepilog-vectorization-of-inductions -fvector-transform  
-fscalar-transform -lamdlibm -lamdalloc -ldl -lflang
```

554.roms_r: basepeak = yes

Benchmarks using both Fortran and C:

```
521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -Mrecursive  
-funroll-loops -mllvm -lsr-in-nested-loop  
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc  
-ldl -lflang
```

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

511.povray_r: basepeak = yes

```
526.blender_r: -m64 -std=c++14  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining
```

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Peak Optimization Flags (Continued)

526.blender_r (continued):

```
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt  
-mllvm -unroll-threshold=100 -lamdlibm -lamdaloc -ldl
```

Benchmarks using Fortran, C, and C++:

```
507.cactusBSSN_r: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument
```

Benchmarks using both C and C++:

```
-Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.xml>

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