



SPEC CPU®2017 Integer Rate Result

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Dell Inc.

SPECrate®2017_int_base = 625

PowerEdge XE9680 (Intel Xeon Platinum 8460Y+)

SPECrate®2017_int_peak = 648

CPU2017 License: 6573

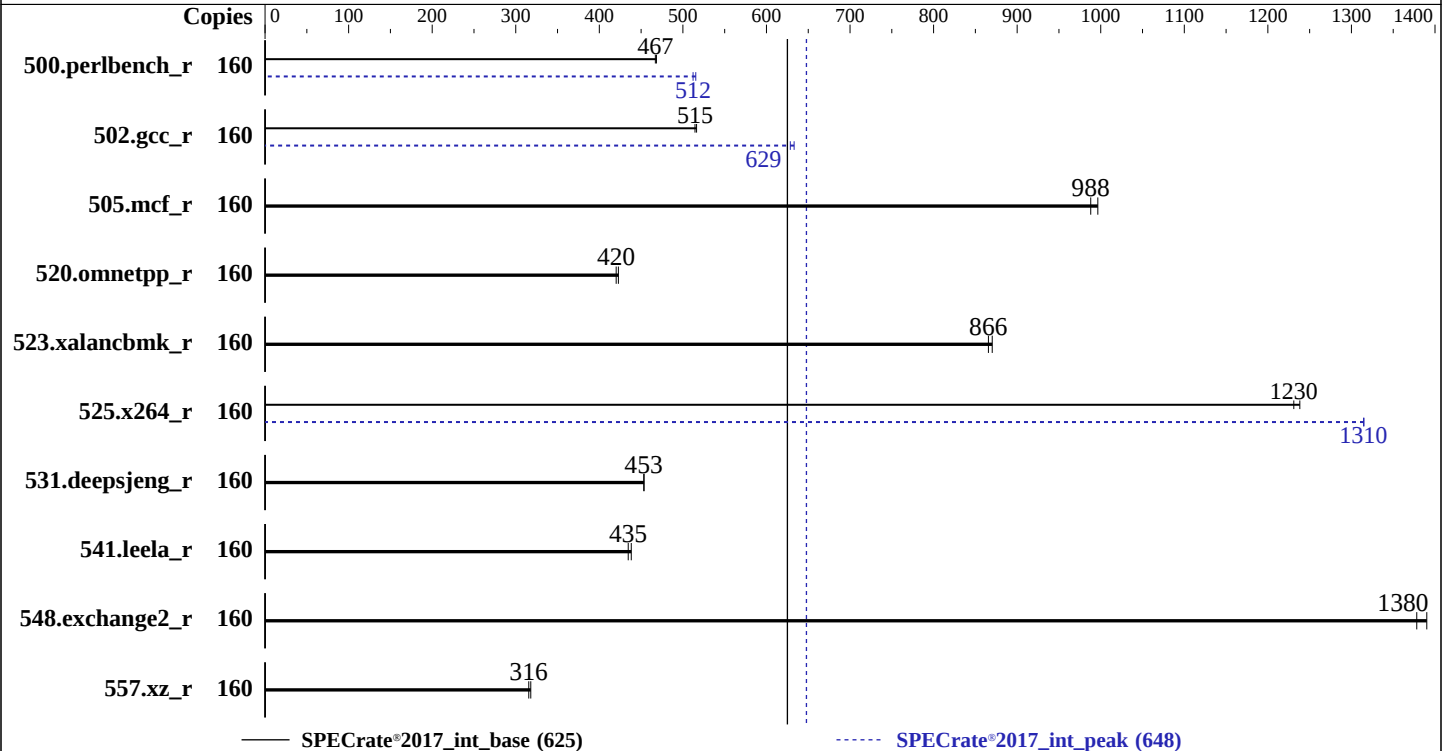
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Sep-2025

Hardware Availability: Jun-2025

Software Availability: Apr-2024



Hardware

CPU Name: Intel Xeon Platinum 8460Y+
Max MHz: 3700
Nominal: 2000
Enabled: 80 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 105 MB I+D on chip per chip
Other: None
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 4800)
Storage: 100 GB on tmpfs
Other: CPU Cooling: Air

Software

OS: Red Hat Enterprise Linux 9.4 (Plow)
5.14.0-427.13.1.el9_4.x86_64
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel: No
Firmware: Version 2.7.5 released Jul-2025
File System: tmpfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	160	544	468	<u>545</u>	<u>467</u>			160	494	515	<u>497</u>	<u>512</u>		
502.gcc_r	160	<u>440</u>	<u>515</u>	439	516			160	358	633	<u>360</u>	<u>629</u>		
505.mcf_r	160	<u>262</u>	<u>988</u>	259	997			160	<u>262</u>	<u>988</u>	259	997		
520.omnetpp_r	160	496	423	<u>500</u>	<u>420</u>			160	496	423	<u>500</u>	<u>420</u>		
523.xalancbmk_r	160	194	870	<u>195</u>	<u>866</u>			160	194	870	<u>195</u>	<u>866</u>		
525.x264_r	160	<u>228</u>	<u>1230</u>	226	1240			160	<u>213</u>	<u>1310</u>	213	1320		
531.deepsjeng_r	160	404	454	<u>405</u>	<u>453</u>			160	404	454	<u>405</u>	<u>453</u>		
541.leela_r	160	<u>610</u>	<u>435</u>	605	438			160	<u>610</u>	<u>435</u>	605	438		
548.exchange2_r	160	302	1390	<u>304</u>	<u>1380</u>			160	302	1390	<u>304</u>	<u>1380</u>		
557.xz_r	160	<u>548</u>	<u>316</u>	543	318			160	<u>548</u>	<u>316</u>	543	318		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH =

"/mnt/ramdisk/cpu2017-1.1.9-ic2024.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2024.1/lib/ia32:/mnt/ramdisk/cpu2017-1.1.9-ic2024.1/je5.0.1-32"

MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

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General Notes (Continued)

Benchmark run from a 100 GB ramdisk created with the cmd: "mount -t tmpfs -o size=100G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:

Virtualization Technology : Disabled
DCU Streamer Prefetcher : Disabled
Sub NUMA Cluster : 4-Way Clustering
UMA Based Clustering Status : Disable
Dead Line LLC Alloc : Disabled
x2APIC Mode : Disabled

System Profile : Custom
C-States : Autonomous
Memory Patrol Scrub : Disabled
Uncore Frequency : Dynamic
DIMM Self Healing -

on Uncorrectable Memory Error : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2024.1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on GN01104-XE9680 Sun Sep 14 13:07:19 2025

SUT (System Under Test) info as seen by some common utilities.

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1. uname -a
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3. Username
4. ulimit -a
5. sysinfo process ancestry
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7. lscpu
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9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-32.el9_4)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux GN01104-XE9680 5.14.0-427.13.1.el9_4.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 10 10:29:16 EDT 2024
x86_64 x86_64 x86_64 GNU/Linux

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Platform Notes (Continued)

2. w

13:07:19 up 1 day, 20:20, 1 user, load average: 0.20, 0.05, 0.21

USER	TTY	LOGIN@	IDLE	JCPU	PCPU	WHAT
------	-----	--------	------	------	------	------

root	tty1	11:01	22.00s	1.00s	0.00s	/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh
------	------	-------	--------	-------	-------	---

rate --define DL-VERS=6.3a --output_format html,pdf,txt

3. Username

From environment variable \$USER: root

4. ulimit -a

```
real-time non-blocking time (microseconds, -R) unlimited
core file size              (blocks, -c) 0
data seg size                (kbytes, -d) unlimited
scheduling priority         (-e) 0
file size                    (blocks, -f) unlimited
pending signals              (-i) 4126014
max locked memory            (kbytes, -l) 64
max memory size              (kbytes, -m) unlimited
open files                   (-n) 1024
pipe size                    (512 bytes, -p) 8
POSIX message queues         (bytes, -q) 819200
real-time priority           (-r) 0
stack size                   (kbytes, -s) unlimited
cpu time                     (seconds, -t) unlimited
max user processes           (-u) 4126014
virtual memory               (kbytes, -v) unlimited
file locks                   (-x) unlimited
```

5. sysinfo process ancestry

/usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31

login -- root

-bash

/bin/bash /home/DellFiles/bin/DELL_rate.sh

/bin/bash /home/DellFiles/bin/dell-run-main.sh rate

/bin/bash /home/DellFiles/bin/dell-run-main.sh rate

/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-VERS=6.3a --output_format html,pdf,txt

/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-VERS=6.3a --output_format html,pdf,txt

runcpu --nobuild --action validate --define default-platform-flags --define numcopies=160 -c ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=80 --define physicalfirst --define invoke_with_interleave --define drop_caches --tune base,peak -o all --iterations 2 --define DL-VERS=6.3a --output_format html,pdf,txt intrate

runcpu --nobuild --action validate --define default-platform-flags --define numcopies=160 --configfile ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=80 --define physicalfirst --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --iterations 2 --define DL-VERS=6.3a --output_format html,pdf,txt --nopower --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile \$SPEC/tmp/CPU2017.001/temlogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2

specperl \$SPEC/bin/sysinfo

\$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2024.1

6. /proc/cpuinfo

model name	: Intel(R) Xeon(R) Platinum 8460Y+
vendor_id	: GenuineIntel
cpu family	: 6

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Platform Notes (Continued)

```
model          : 143
stepping       : 8
microcode      : 0x2b000643
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores      : 40
siblings       : 80
2 physical ids (chips)
160 processors (hardware threads)
physical id 0: core ids 0-39
physical id 1: core ids 0-39
physical id 0: apicids 0-79
physical id 1: apicids 128-207
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.4:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                160
On-line CPU(s) list:   0-159
Vendor ID:             GenuineIntel
BIOS Vendor ID:       Intel
Model name:            Intel(R) Xeon(R) Platinum 8460Y+
BIOS Model name:       Intel(R) Xeon(R) Platinum 8460Y+
CPU family:            6
Model:                143
Thread(s) per core:    2
Core(s) per socket:    40
Socket(s):             2
Stepping:              8
CPU(s) scaling MHz:    100%
CPU max MHz:           3700.0000
CPU min MHz:           800.0000
BogoMIPS:              4000.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb
                        rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl
                        xtopology nonstop_tsc cpuid aperfperf tsc_known_freq pni pclmulqdq
                        dtes64 monitor ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid
                        dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx
                        f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2
                        cdp_l3 cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase
                        tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq
                        rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni
                        avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc
                        cqm_mbm_total cqm_mbm_local split_lock_detect avx_vnni avx512_bf16
                        wbnoinvd dtherm ida arat pln pts hfi avx512vbmi umip pku ospke waitpkg
                        avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
                        avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
                        enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16
                        avx512_fp16 amx_tile amx_int8 flush_l1d arch_capabilities
L1d cache:            3.8 MiB (80 instances)
L1i cache:            2.5 MiB (80 instances)
L2 cache:             160 MiB (80 instances)
L3 cache:             210 MiB (2 instances)
```

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Platform Notes (Continued)

NUMA node(s): 8
NUMA node0 CPU(s): 0, 8, 16, 24, 32, 40, 48, 56, 64, 72, 80, 88, 96, 104, 112, 120, 128, 136, 144, 152
NUMA node1 CPU(s): 4, 12, 20, 28, 36, 44, 52, 60, 68, 76, 84, 92, 100, 108, 116, 124, 132, 140, 148, 156
NUMA node2 CPU(s): 2, 10, 18, 26, 34, 42, 50, 58, 66, 74, 82, 90, 98, 106, 114, 122, 130, 138, 146, 154
NUMA node3 CPU(s): 6, 14, 22, 30, 38, 46, 54, 62, 70, 78, 86, 94, 102, 110, 118, 126, 134, 142, 150, 158
NUMA node4 CPU(s): 1, 9, 17, 25, 33, 41, 49, 57, 65, 73, 81, 89, 97, 105, 113, 121, 129, 137, 145, 153
NUMA node5 CPU(s): 5, 13, 21, 29, 37, 45, 53, 61, 69, 77, 85, 93, 101, 109, 117, 125, 133, 141, 149, 157
NUMA node6 CPU(s): 3, 11, 19, 27, 35, 43, 51, 59, 67, 75, 83, 91, 99, 107, 115, 123, 131, 139, 147, 155
NUMA node7 CPU(s): 7, 15, 23, 31, 39, 47, 55, 63, 71, 79, 87, 95, 103, 111, 119, 127, 135, 143, 151, 159
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS, IBPB conditional, RSB filling, PBRSE-IBRS SW sequence
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	3.8M	12	Data	1	64	1	64
L1i	32K	2.5M	8	Instruction	1	64	1	64
L2	2M	160M	16	Unified	2	2048	1	64
L3	105M	210M	15	Unified	3	114688	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)
node 0 cpus: 0, 8, 16, 24, 32, 40, 48, 56, 64, 72, 80, 88, 96, 104, 112, 120, 128, 136, 144, 152
node 0 size: 128471 MB
node 0 free: 128032 MB
node 1 cpus: 4, 12, 20, 28, 36, 44, 52, 60, 68, 76, 84, 92, 100, 108, 116, 124, 132, 140, 148, 156
node 1 size: 129018 MB
node 1 free: 128635 MB
node 2 cpus: 2, 10, 18, 26, 34, 42, 50, 58, 66, 74, 82, 90, 98, 106, 114, 122, 130, 138, 146, 154
node 2 size: 129018 MB
node 2 free: 128739 MB
node 3 cpus: 6, 14, 22, 30, 38, 46, 54, 62, 70, 78, 86, 94, 102, 110, 118, 126, 134, 142, 150, 158
node 3 size: 129018 MB
node 3 free: 128738 MB
node 4 cpus: 1, 9, 17, 25, 33, 41, 49, 57, 65, 73, 81, 89, 97, 105, 113, 121, 129, 137, 145, 153
node 4 size: 129018 MB
node 4 free: 128693 MB
node 5 cpus: 5, 13, 21, 29, 37, 45, 53, 61, 69, 77, 85, 93, 101, 109, 117, 125, 133, 141, 149, 157
node 5 size: 129018 MB
node 5 free: 118437 MB
node 6 cpus: 3, 11, 19, 27, 35, 43, 51, 59, 67, 75, 83, 91, 99, 107, 115, 123, 131, 139, 147, 155
node 6 size: 128977 MB
node 6 free: 128719 MB
node 7 cpus: 7, 15, 23, 31, 39, 47, 55, 63, 71, 79, 87, 95, 103, 111, 119, 127, 135, 143, 151, 159
node 7 size: 129003 MB
node 7 free: 128766 MB
node distances:

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Platform Notes (Continued)

node	0	1	2	3	4	5	6	7
0:	10	12	12	12	21	21	21	21
1:	12	10	12	12	21	21	21	21
2:	12	12	10	12	21	21	21	21
3:	12	12	12	10	21	21	21	21
4:	21	21	21	21	10	12	12	12
5:	21	21	21	21	12	10	12	12
6:	21	21	21	21	12	12	10	12
7:	21	21	21	21	12	12	12	10

9. /proc/meminfo

MemTotal: 1056303412 kB

10. who -r

run-level 3 Sep 12 16:47

11. Systemd service manager version: systemd 252 (252-32.el9_4)

Default Target Status
multi-user running

12. Services, from systemctl list-unit-files

STATE UNIT FILES

enabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online atd auditd bluetooth
chronyd crond dbus-broker firewallld getty@ insights-client-boot irqbalance iscsi-onboot
iscsi-starter kdump libstoragemgmt low-memory-monitor lvm2-monitor mcelog mdmonitor
microcode multipathd nis-domainname nvme-fc-boot-connections rhsmcertd rsyslog rtkit-daemon
selinux-autorelabel-mark smartd sshd sssd systemd-boot-update systemd-network-generator
udisks2 upower
enabled-runtime systemd-remount-fs
disabled arp-ethers blk-availability canberra-system-bootup canberra-system-shutdown
canberra-system-shutdown-reboot chrony-wait chronyd-restricted console-getty cpupower
debug-shell dnf-system-upgrade iprdump iprinit iprupdate ipsec iscsi-init iscsid iscsiuiio
kpatch kvm_stat ledmon man-db-restart-cache-update nftables nvme-fc-autoconnect psacct
rdisc rhcd rhsm rhsm-facts rpmbd-rebuild selinux-check-proper-disable serial-getty@
sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysext
indirect iscsi sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate
systemd-sysupdate-reboot

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-427.13.1.el9_4.x86_64
root=/dev/mapper/rhel-root
ro
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap
rhgb
quiet

14. cpupower frequency-info

analyzing CPU 71:

current policy: frequency should be within 800 MHz and 3.70 GHz.
The governor "performance" may decide which speed to use
within this range.

boost state support:

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Platform Notes (Continued)

Supported: yes
Active: yes

15. sysctl

```
kernel.numa_balancing      1
kernel.randomize_va_space  2
vm.compaction_proactiveness 20
vm.dirty_background_bytes   0
vm.dirty_background_ratio   10
vm.dirty_bytes              0
vm.dirty_expire_centisecs   3000
vm.dirty_ratio              20
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold        500
vm.min_unmapped_ratio       1
vm.nr_hugepages             0
vm.nr_hugepages_mempolicy   0
vm.nr_overcommit_hugepages  0
vm.swappiness               60
vm.watermark_boost_factor   15000
vm.watermark_scale_factor   10
vm.zone_reclaim_mode        0
```

16. /sys/kernel/mm/transparent_hugepage

```
defrag      always defer defer+madvice [madvice] never
enabled     [always] madvice never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force
```

17. /sys/kernel/mm/transparent_hugepage/khugepaged

```
alloc_sleep_millisecs 60000
defrag                1
max_ptes_none         511
max_ptes_shared        256
max_ptes_swap         64
pages_to_scan         4096
scan_sleep_millisecs  10000
```

18. OS release

```
From /etc/*-release /etc/*-version
os-release      Red Hat Enterprise Linux 9.4 (Plow)
redhat-release  Red Hat Enterprise Linux release 9.4 (Plow)
system-release  Red Hat Enterprise Linux release 9.4 (Plow)
```

19. Disk information

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2024.1

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
tmpfs	tmpfs	100G	5.0G	96G	5%	/mnt/ramdisk

20. /sys/devices/virtual/dmi/id

```
Vendor:      Dell Inc.
Product:     PowerEdge XE9680
Product Family: PowerEdge
```

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Platform Notes (Continued)

Serial: GN01104

21. dmidecode

Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x 002C0632002C MTC40F2046S1RC56BG1 64 GB 2 rank 5600, configured at 4800

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Dell Inc.

BIOS Version: 2.7.5

BIOS Date: 07/31/2025

BIOS Revision: 2.7

Compiler Version Notes

C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308
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C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
| 541.leela_r(base, peak)

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SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017_int_base = 625

PowerEdge XE9680 (Intel Xeon Platinum 8460Y+)

SPECrate®2017_int_peak = 648

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Sep-2025

Hardware Availability: Jun-2025

Software Availability: Apr-2024

Compiler Version Notes (Continued)

=====

Fortran | 548.exchange2_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64

502.gcc_r: -DSPEC_LP64

505.mcf_r: -DSPEC_LP64

520.omnetpp_r: -DSPEC_LP64

523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX

525.x264_r: -DSPEC_LP64

531.deepsjeng_r: -DSPEC_LP64

541.leela_r: -DSPEC_LP64

548.exchange2_r: -DSPEC_LP64

557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math

-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math

-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

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Base Optimization Flags (Continued)

Fortran benchmarks:

-w -m64 -wl, -z, muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -w -std=c11 -m64 -wl, -z, muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse

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Peak Optimization Flags (Continued)

500.perlbench_r (continued):

-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmallo

502.gcc_r: -m32 -L/opt/intel/oneapi/compiler/2024.1/lib32 -std=gnu89

-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast

-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmallo

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.17x.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.17x.xml>



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