



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR850 V4  
(3.60 GHz, Intel Xeon 6724P)

SPECspeed®2017\_int\_base = 15.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

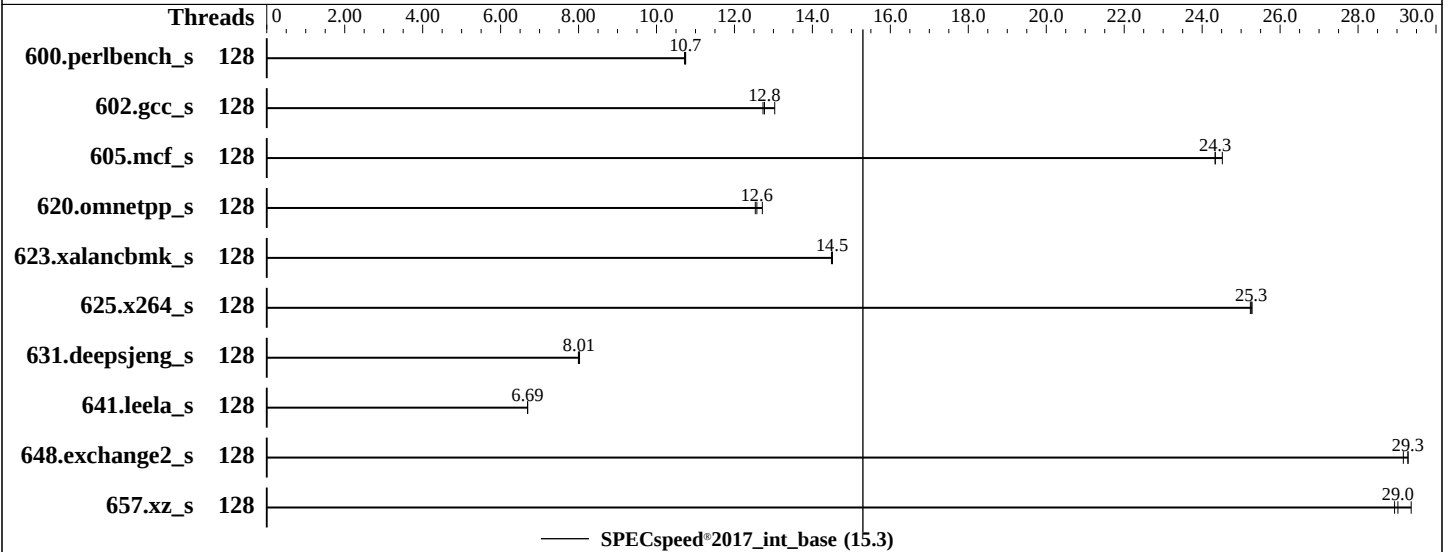
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Sep-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025



## Hardware

CPU Name: Intel Xeon 6724P  
Max MHz: 4300  
Nominal: 3600  
Enabled: 64 cores, 4 chips, 2 threads/core  
Orderable: 2,4 chips  
Cache L1: 64 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 72 MB I+D on chip per chip  
Other: None  
Memory: 2 TB (32 x 64 GB 2Rx4 PC5-6400B-R)  
Storage: 1 x 960 GB M.2 NVME SSD  
Other: CPU Cooling: Air

## Software

OS: SUSE Linux Enterprise Server 15 SP7  
Kernel 6.4.0-150700.51-default  
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;  
Parallel: Yes  
Firmware: Lenovo BIOS Version RVE103X 1.10 released Jul-2025  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: Not Applicable  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR850 V4  
(3.60 GHz, Intel Xeon 6724P)

SPECspeed®2017\_int\_base = 15.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Sep-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025

## Results Table

| Benchmark       | Base    |             |             |            |             |             |             | Peak    |         |       |         |       |         |       |
|-----------------|---------|-------------|-------------|------------|-------------|-------------|-------------|---------|---------|-------|---------|-------|---------|-------|
|                 | Threads | Seconds     | Ratio       | Seconds    | Ratio       | Seconds     | Ratio       | Threads | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 600.perlbench_s | 128     | 166         | 10.7        | <u>165</u> | <u>10.7</u> | 165         | 10.7        |         |         |       |         |       |         |       |
| 602.gcc_s       | 128     | 306         | 13.0        | <u>312</u> | <u>12.8</u> | 313         | 12.7        |         |         |       |         |       |         |       |
| 605.mcf_s       | 128     | 194         | 24.3        | <u>194</u> | <u>24.3</u> | 193         | 24.5        |         |         |       |         |       |         |       |
| 620.omnetpp_s   | 128     | <u>130</u>  | <u>12.6</u> | 130        | 12.5        | 128         | 12.7        |         |         |       |         |       |         |       |
| 623.xalancbmk_s | 128     | 97.6        | 14.5        | 97.8       | 14.5        | <u>97.7</u> | <u>14.5</u> |         |         |       |         |       |         |       |
| 625.x264_s      | 128     | <u>69.8</u> | <u>25.3</u> | 69.8       | 25.3        | 69.9        | 25.2        |         |         |       |         |       |         |       |
| 631.deepsjeng_s | 128     | <u>179</u>  | <u>8.01</u> | 179        | 8.03        | 179         | 8.00        |         |         |       |         |       |         |       |
| 641.leela_s     | 128     | 255         | 6.69        | 255        | 6.69        | <u>255</u>  | <u>6.69</u> |         |         |       |         |       |         |       |
| 648.exchange2_s | 128     | 101         | 29.2        | 100        | 29.3        | <u>100</u>  | <u>29.3</u> |         |         |       |         |       |         |       |
| 657.xz_s        | 128     | 211         | 29.4        | 214        | 28.9        | <u>213</u>  | <u>29.0</u> |         |         |       |         |       |         |       |

SPECspeed®2017\_int\_base = 15.3

SPECspeed®2017\_int\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/cpu2017-1.1.9-ic2024.1/lib/intel64:/home/cpu2017-1.1.9-ic2024.1/je5.0.1-64"

MALLOC\_CONF = "retain:true"

OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM

memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop\_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR850 V4  
(3.60 GHz, Intel Xeon 6724P)

SPECspeed®2017\_int\_base = 15.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Sep-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025

## Platform Notes

BIOS configuration:  
Workload Profile set to Custom  
Page Policy set to Adaptive  
Uncore Frequency Scaling set to Disabled

Sysinfo program /home/cpu2017-1.1.9-ic2024.1/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Sun Sep 14 10:15:51 2025

SUT (System Under Test) info as seen by some common utilities.

### Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a  
Linux localhost 6.4.0-150700.51-default #1 SMP PREEMPT\_DYNAMIC Wed Apr 30 21:35:43 UTC 2025 (6930611)  
x86\_64 x86\_64 x86\_64 GNU/Linux

2. w  
10:15:51 up 5 min, 1 user, load average: 0.22, 0.67, 0.44  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

3. Username  
From environment variable \$USER: root

4. ulimit -a  
core file size (blocks, -c) unlimited  
data seg size (kbytes, -d) unlimited  
scheduling priority (-e) 0  
file size (blocks, -f) unlimited  
pending signals (-i) 8255340  
max locked memory (kbytes, -l) 8192

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR850 V4  
(3.60 GHz, Intel Xeon 6724P)

SPECspeed®2017\_int\_base = 15.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Sep-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025

### Platform Notes (Continued)

max memory size (kbytes, -m) unlimited  
open files (-n) 1024  
pipe size (512 bytes, -p) 8  
POSIX message queues (bytes, -q) 819200  
real-time priority (-r) 0  
stack size (kbytes, -s) unlimited  
cpu time (seconds, -t) unlimited  
max user processes (-u) 8255340  
virtual memory (kbytes, -v) unlimited  
file locks (-x) unlimited

```
-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
/bin/bash ./02.remote_local_SPECCpu_1.02.sh
sh Run542-compliant-ic2024.1-lin-sapphirerapids-speedint-base-smt-on-20240308.sh
runcpu --nobuild --action validate --define default-platform-flags -c
ic2024.1-lin-sapphirerapids-speed-20240308.cfg --define cores=64 --tune base -o all --define
intspeedaffinity --define smt-on --define drop_caches intspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
ic2024.1-lin-sapphirerapids-speed-20240308.cfg --define cores=64 --tune base --output_format all --define
intspeedaffinity --define smt-on --define drop_caches --nopower --runmode speed --tune base --size
refspeed intspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.054/temlogs/preenv.intspeed.054.0.log --lognum 054.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-ic2024.1
```

```
-----
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) 6724P
vendor_id       : GenuineIntel
cpu family      : 6
model           : 173
stepping        : 1
microcode       : 0x10003d0
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores       : 16
siblings        : 32
4 physical ids (chips)
128 processors (hardware threads)
physical id 0:  core ids 0-15
physical id 1:  core ids 0-15
physical id 2:  core ids 0-15
physical id 3:  core ids 0-15
physical id 0:  apicids 0-31
physical id 1:  apicids 128-159
physical id 2:  apicids 256-287
physical id 3:  apicids 384-415
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.40.4:

Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR850 V4  
(3.60 GHz, Intel Xeon 6724P)

SPECspeed®2017\_int\_base = 15.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Sep-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025

## Platform Notes (Continued)

Address sizes: 52 bits physical, 57 bits virtual  
Byte Order: Little Endian  
CPU(s): 128  
On-line CPU(s) list: 0-127  
Vendor ID: GenuineIntel  
Model name: Intel(R) Xeon(R) 6724P  
CPU family: 6  
Model: 173  
Thread(s) per core: 2  
Core(s) per socket: 16  
Socket(s): 4  
Stepping: 1  
BogoMIPS: 7200.00  
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant\_tsc art arch\_perfmon pebs bts rep\_good nopl xtopology nonstop\_tsc cpuid aperfmperf tsc\_known\_freq pni pclmulqdq dtes64 monitor ds\_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4\_1 sse4\_2 x2apic movbe popcnt tsc\_deadline\_timer aes xsave avx f16c rdrand lahf\_lm abm 3dnowprefetch cpuid\_fault epb cat\_l3 cat\_l2 cdp\_l3 intel\_ppin cdp\_l2 ssbd mba ibrs ibpb stibp ibrs\_enhanced tpr\_shadow flexpriority ept vpid ept\_ad fsgsbase tsc\_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt\_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel\_pt avx512cd sha\_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm\_llc cqm\_occup\_llc cqm\_mbm\_total cqm\_mbm\_local split\_lock\_detect user\_shstk avx\_vnni avx512\_bf16 wbnoinvd dtherm ida arat pln pts vnmi avx512vbmi umip pku ospke waitpkg avx512\_vbmi2 gfni vaes vpclmulqdq avx512\_vnni avx512\_bitalg tme avx512\_vpopcntdq la57 rdpid bus\_lock\_detect cldemote movdiri movdir64b enqcmd fsrm md\_clear serialize tsxldtrk pconfig arch\_lbr ibt amx\_bf16 avx512\_fp16 amx\_tile amx\_int8 flush\_l1d arch\_capabilities  
Virtualization: VT-x  
L1d cache: 3 MiB (64 instances)  
L1i cache: 4 MiB (64 instances)  
L2 cache: 128 MiB (64 instances)  
L3 cache: 288 MiB (4 instances)  
NUMA node(s): 4  
NUMA node0 CPU(s): 0-15,64-79  
NUMA node1 CPU(s): 16-31,80-95  
NUMA node2 CPU(s): 32-47,96-111  
NUMA node3 CPU(s): 48-63,112-127  
Vulnerability Gather data sampling: Not affected  
Vulnerability Itlb multihit: Not affected  
Vulnerability L1tf: Not affected  
Vulnerability Mds: Not affected  
Vulnerability Meltdown: Not affected  
Vulnerability Mmio stale data: Not affected  
Vulnerability Reg file data sampling: Not affected  
Vulnerability Retbleed: Not affected  
Vulnerability Spec rstack overflow: Not affected  
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl  
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization  
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSE-IBRS Not affected; BHI BHI\_DIS\_S  
Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE | LEVEL | SETS | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|------|-------|------|----------|----------------|
|------|----------|----------|------|------|-------|------|----------|----------------|

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR850 V4  
(3.60 GHz, Intel Xeon 6724P)

SPECspeed®2017\_int\_base = 15.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Sep-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025

### Platform Notes (Continued)

|     |     |      |                |   |       |   |    |
|-----|-----|------|----------------|---|-------|---|----|
| L1d | 48K | 3M   | 12 Data        | 1 | 64    | 1 | 64 |
| L1i | 64K | 4M   | 16 Instruction | 1 | 64    | 1 | 64 |
| L2  | 2M  | 128M | 16 Unified     | 2 | 2048  | 1 | 64 |
| L3  | 72M | 288M | 16 Unified     | 3 | 73728 | 1 | 64 |

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)

node 0 cpus: 0-15,64-79

node 0 size: 515685 MB

node 0 free: 514535 MB

node 1 cpus: 16-31,80-95

node 1 size: 516087 MB

node 1 free: 515066 MB

node 2 cpus: 32-47,96-111

node 2 size: 516048 MB

node 2 free: 515215 MB

node 3 cpus: 48-63,112-127

node 3 size: 516041 MB

node 3 free: 515271 MB

node distances:

|      |    |    |    |    |
|------|----|----|----|----|
| node | 0  | 1  | 2  | 3  |
| 0:   | 10 | 21 | 21 | 21 |
| 1:   | 21 | 10 | 21 | 21 |
| 2:   | 21 | 21 | 10 | 21 |
| 3:   | 21 | 21 | 21 | 10 |

9. /proc/meminfo

MemTotal: 2113395040 kB

10. who -r

run-level 3 Sep 14 10:10

11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)

Default Target Status

multi-user running

12. Services, from systemctl list-unit-files

STATE UNIT FILES

enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance issue-generator  
kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections nvmmf-autoconnect postfix  
purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked wickedd-auto4  
wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny

enabled-runtime systemd-remount-fs

disabled autofs autovast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait  
chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info  
firewalld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievd issue-add-ssh-keys  
kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd  
serial-getty@ smartd\_generate\_opts snmpd snmptrapd systemd-boot-check-no-failures  
systemd-confext systemd-network-generator systemd-sysexit systemd-time-wait-sync  
systemd-timesyncd

generated ntp\_sync

indirect systemd-userdbd wickedd

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR850 V4  
(3.60 GHz, Intel Xeon 6724P)

SPECspeed®2017\_int\_base = 15.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Sep-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025

### Platform Notes (Continued)

#### 13. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150700.51-default  
root=UUID=548b98ec-c242-4959-8c74-ed91816d4845  
splash=silent  
mitigations=auto  
quiet  
security=apparmor
```

#### 14. cpupower frequency-info

```
analyzing CPU 24:  
Unable to determine current policy  
boost state support:  
Supported: yes  
Active: yes
```

#### 15. sysctl

```
kernel.numa_balancing          1  
kernel.randomize_va_space      2  
vm.compaction_proactiveness    20  
vm.dirty_background_bytes      0  
vm.dirty_background_ratio      10  
vm.dirty_bytes                 0  
vm.dirty_expire_centisecs      3000  
vm.dirty_ratio                 20  
vm.dirty_writeback_centisecs   500  
vm.dirtytime_expire_seconds    43200  
vm.extfrag_threshold           500  
vm.min_unmapped_ratio          1  
vm.nr_hugepages                0  
vm.nr_hugepages_mempolicy      0  
vm.nr_overcommit_hugepages     0  
vm.swappiness                   60  
vm.watermark_boost_factor      15000  
vm.watermark_scale_factor      10  
vm.zone_reclaim_mode           0
```

#### 16. /sys/kernel/mm/transparent\_hugepage

```
defrag          always defer defer+madvice [madvice] never  
enabled         [always] madvice never  
hpage_pmd_size  2097152  
shmem_enabled   always within_size advise [never] deny force
```

#### 17. /sys/kernel/mm/transparent\_hugepage/khugepaged

```
alloc_sleep_millisecs  60000  
defrag                 1  
max_ptes_none          511  
max_ptes_shared        256  
max_ptes_swap          64  
pages_to_scan          4096  
scan_sleep_millisecs   10000
```

#### 18. OS release

```
From /etc/*-release /etc/*-version  
os-release SUSE Linux Enterprise Server 15 SP7
```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR850 V4  
(3.60 GHz, Intel Xeon 6724P)

SPECspeed®2017\_int\_base = 15.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Sep-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025

## Platform Notes (Continued)

### 19. Disk information

SPEC is set to: /home/cpu2017-1.1.9-ic2024.1

| Filesystem     | Type | Size | Used | Avail | Use% | Mounted on |
|----------------|------|------|------|-------|------|------------|
| /dev/nvme0n1p3 | xfs  | 893G | 43G  | 851G  | 5%   | /          |

### 20. /sys/devices/virtual/dmi/id

Vendor: Lenovo  
Product: ThinkSystem SR850 V4  
Product Family: ThinkSystem  
Serial: 9876543210

### 21. dmidecode

Additional information from dmidecode 3.6 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

#### Memory:

18x Samsung M321R8GA0PB1-CCPPC 64 GB 2 rank 6400  
1x Samsung M321R8GA0PB1-CCPQC 64 GB 2 rank 6400  
11x Samsung M321R8GA0PB1-CCPYC 64 GB 2 rank 6400  
2x Samsung M321R8GA0PB2-CCPPC 64 GB 2 rank 6400

### 22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Lenovo  
BIOS Version: RVE103X-1.10  
BIOS Date: 07/17/2025  
BIOS Revision: 1.10  
Firmware Revision: 1.40

## Compiler Version Notes

C | 600.perlbench\_s(base) 602.gcc\_s(base) 605.mcf\_s(base) 625.x264\_s(base) 657.xz\_s(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C++ | 620.omnetpp\_s(base) 623.xalancbmk\_s(base) 631.deepsjeng\_s(base) 641.leela\_s(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Fortran | 648.exchange2\_s(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

ThinkSystem SR850 V4  
(3.60 GHz, Intel Xeon 6724P)

SPECspeed®2017\_int\_base = 15.3

SPECspeed®2017\_int\_peak = Not Run

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Sep-2025

**Hardware Availability:** Nov-2025

**Software Availability:** Jun-2025

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LP64 -DSPEC\_LINUX  
625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64  
648.exchange2\_s: -DSPEC\_LP64  
657.xz\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
-DSPEC\_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR850 V4  
(3.60 GHz, Intel Xeon 6724P)

SPECspeed®2017\_int\_base = 15.3

SPECspeed®2017\_int\_peak = Not Run

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Sep-2025

**Hardware Availability:** Nov-2025

**Software Availability:** Jun-2025

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Birchstream-F.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Birchstream-F.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2025-09-13 22:15:50-0400.

Report generated on 2025-10-07 16:38:20 by CPU2017 PDF formatter v6716.

Originally published on 2025-10-07.