



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero SDA200A2N-212)

(3.20 GHz, AMD EPYC 9255)

SPECspeed®2017_int_base = 18.3

SPECspeed®2017_int_peak = 18.5

CPU2017 License: 6042

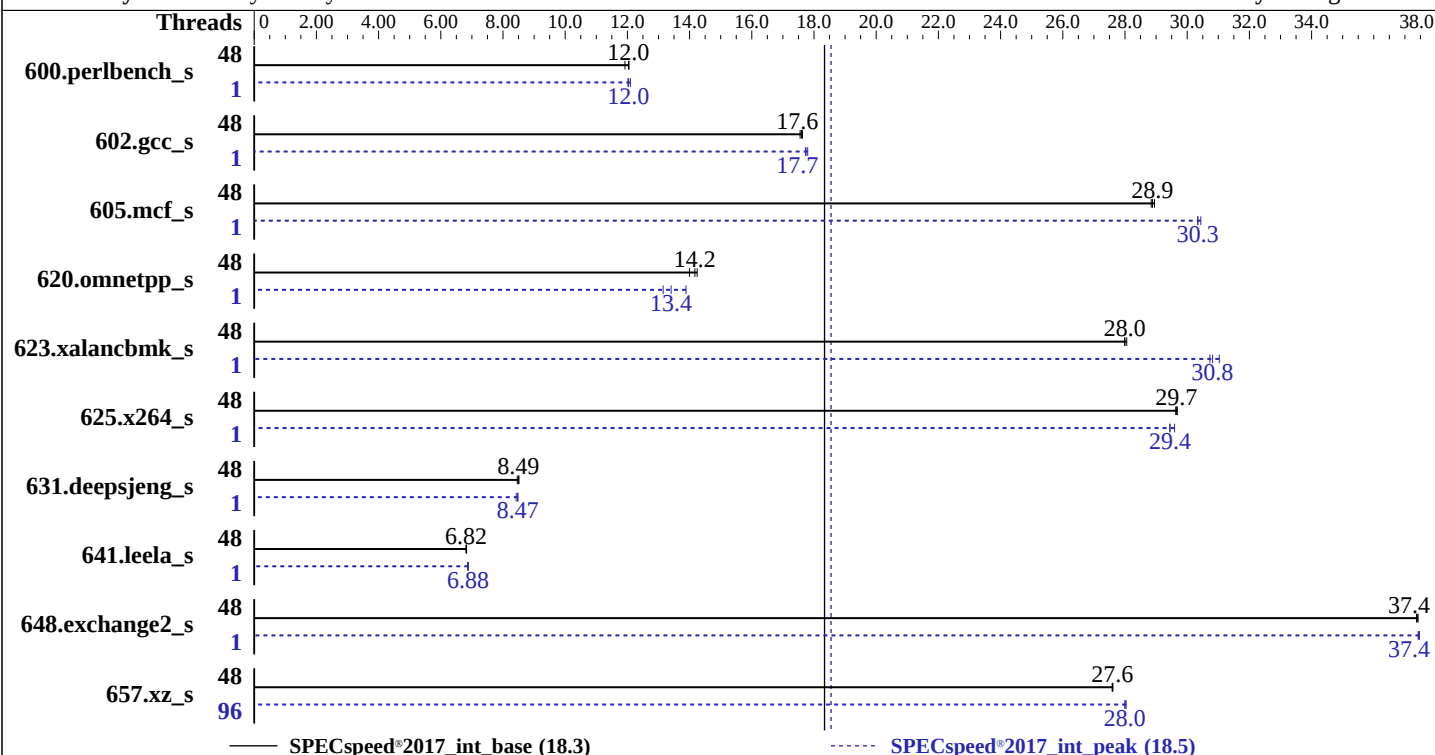
Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025



Hardware

CPU Name: AMD EPYC 9255
Max MHz: 4300
Nominal: 3200
Enabled: 48 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 128 MB I+D on chip per chip, 32 MB shared / 6 cores
Other: None
Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-5600B-R)
Storage: 1 x 1 TB NVMe
Other: CPU Cooling: Air

Software

OS: Ubuntu 24.04 LTS
kernel version 6.14.0-29-generic
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: Yes
Firmware: Version ES312AMS.205T5 released Sep-2025
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: OS and BIOS set to prefer performance at the cost of additional power usage.



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero SDA200A2N-212)

(3.20 GHz, AMD EPYC 9255)

SPECspeed®2017_int_base = 18.3

SPECspeed®2017_int_peak = 18.5

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	48	<u>147</u>	<u>12.0</u>	149	11.9	147	12.1	1	<u>147</u>	<u>12.0</u>	147	12.1	148	12.0
602.gcc_s	48	226	17.6	227	17.6	<u>226</u>	<u>17.6</u>	1	224	17.8	225	17.7	<u>224</u>	<u>17.7</u>
605.mcf_s	48	163	28.9	164	28.8	<u>163</u>	<u>28.9</u>	1	156	30.3	<u>156</u>	<u>30.3</u>	155	30.4
620.omnetpp_s	48	115	14.2	117	14.0	<u>115</u>	<u>14.2</u>	1	<u>122</u>	<u>13.4</u>	124	13.1	117	13.9
623.xalancbmk_s	48	50.5	28.1	50.6	28.0	<u>50.6</u>	<u>28.0</u>	1	45.7	31.0	46.1	30.7	<u>46.0</u>	<u>30.8</u>
625.x264_s	48	59.5	29.6	59.4	29.7	<u>59.5</u>	<u>29.7</u>	1	59.9	29.4	59.6	29.6	<u>59.9</u>	<u>29.4</u>
631.deepsjeng_s	48	<u>169</u>	<u>8.49</u>	169	8.46	168	8.51	1	169	8.48	170	8.42	<u>169</u>	<u>8.47</u>
641.leela_s	48	<u>250</u>	<u>6.82</u>	250	6.83	250	6.81	1	249	6.86	248	6.88	<u>248</u>	<u>6.88</u>
648.exchange2_s	48	78.6	37.4	<u>78.6</u>	<u>37.4</u>	78.7	37.4	1	78.6	37.4	<u>78.5</u>	<u>37.4</u>	78.5	37.5
657.xz_s	48	224	27.6	224	27.6	<u>224</u>	<u>27.6</u>	96	<u>221</u>	<u>28.0</u>	220	28.0	221	28.0

SPECspeed®2017_int_base = 18.3

SPECspeed®2017_int_peak = 18.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero SDA200A2N-212)

(3.20 GHz, AMD EPYC 9255)

SPECspeed®2017_int_base = 18.3

SPECspeed®2017_int_peak = 18.5

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP_CPU_AFFINITY = "0-95"

LD_LIBRARY_PATH =

"/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib32:"

LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"

MALLOC_CONF = "retain:true"

OMP_DYNAMIC = "false"

OMP_SCHEDULE = "static"

OMP_STACKSIZE = "128M"

OMP_THREAD_LIMIT = "96"

Environment variables set by runcpu during the 600.perlbench_s peak run:

GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 602.gcc_s peak run:

GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 605.mcf_s peak run:

GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 620.omnetpp_s peak run:

GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 623.xalancbmk_s peak run:

GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 625.x264_s peak run:

GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 631.deepsjeng_s peak run:

GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 641.leela_s peak run:

GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 648.exchange2_s peak run:

GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 657.xz_s peak run:

GOMP_CPU_AFFINITY = "0-95"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:

CTDP: 400

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero SDA200A2N-212)

(3.20 GHz, AMD EPYC 9255)

SPECspeed®2017_int_base = 18.3

SPECspeed®2017_int_peak = 18.5

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Platform Notes (Continued)

Determinism Slider set to Power

ACPI SRAT L3 Cache as NUMA Domain: enabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on amd-benchmark Thu Sep 25 11:31:21 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

```
-----
1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.8)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS
-----

1. uname -a
Linux amd-benchmark 6.14.0-29-generic #29-24.04.1-Ubuntu SMP PREEMPT_DYNAMIC Thu Aug 14 16:52:50 UTC 2
x86_64 x86_64 x86_64 GNU/Linux
-----

2. w
11:31:22 up 17:35, 3 users, load average: 0.08, 0.02, 0.00
USER      TTY      FROM          LOGIN@      IDLE        JCPU      PCPU      WHAT
amd        -        172.17.20.105 11:29       19:50m      0.00s     ?        sshd: amd [priv]
amd        -        172.17.20.105 11:29       19:50m      0.00s     ?        sshd: amd [priv]
amd        tty1      -             Wed15       9.00s       0.51s     0.50s     sudo su
-----

3. Username
From environment variable $USER: root
From the command 'logname':      amd
-----

4. ulimit -a
time(seconds)      unlimited
file(blocks)        unlimited
data(kbytes)        unlimited
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero SDA200A2N-212)

(3.20 GHz, AMD EPYC 9255)

SPECspeed®2017_int_base = 18.3

SPECspeed®2017_int_peak = 18.5

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Platform Notes (Continued)

```
stack(kbytes)          unlimited
coredump(blocks)       0
memory(kbytes)         unlimited
locked memory(kbytes)  2097152
process                6190174
nofiles                1024
vmemory(kbytes)        unlimited
locks                  unlimited
rtprio                 0
```

```
-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --system --deserialize=76 splash
/bin/login -p --
-bash
sudo su
sudo su
su
bash
python3 ./run_amd_speed_aocc500_znver5_A1.py
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 intspeak
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeak intspeak --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/temlogs/preenv.intspeak.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

```
-----
6. /proc/cpuinfo
model name      : AMD EPYC 9255 24-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 26
model          : 2
stepping       : 1
microcode      : 0xb00211a
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass srs0
TLB size       : 192 4K pages
cpu cores      : 24
siblings       : 48
2 physical ids (chips)
96 processors (hardware threads)
physical id 0: core ids 0-5,8-13,16-21,24-29
physical id 1: core ids 0-5,8-13,16-21,24-29
physical id 0: apicids 0-11,16-27,32-43,48-59
physical id 1: apicids 64-75,80-91,96-107,112-123
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                96
On-line CPU(s) list:   0-95
Vendor ID:             AuthenticAMD
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero SDA200A2N-212)

(3.20 GHz, AMD EPYC 9255)

SPECspeed®2017_int_base = 18.3

SPECspeed®2017_int_peak = 18.5

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Platform Notes (Continued)

BIOS Vendor ID:	Advanced Micro Devices, Inc.	
Model name:	AMD EPYC 9255 24-Core Processor	
BIOS Model name:	AMD EPYC 9255 24-Core Processor	Unknown CPU @
	3.2GHz	
BIOS CPU family:	107	
CPU family:	26	
Model:	2	
Thread(s) per core:	2	
Core(s) per socket:	24	
Socket(s):	2	
Stepping:	1	
Frequency boost:	enabled	
CPU(s) scaling MHz:	58%	
CPU max MHz:	4317.0000	
CPU min MHz:	600.0000	
BogoMIPS:	6390.36	
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl xtopology nonstop_tsc cpuid extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsaves avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512v1 xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect flush_l1d debug_swap amd_lbr_pmc_freeze	
Virtualization:	AMD-V	
L1d cache:	2.3 MiB (48 instances)	
L1i cache:	1.5 MiB (48 instances)	
L2 cache:	48 MiB (48 instances)	
L3 cache:	256 MiB (8 instances)	
NUMA node(s):	2	
NUMA node0 CPU(s):	0-23,48-71	
NUMA node1 CPU(s):	24-47,72-95	
Vulnerability Gather data sampling:	Not affected	
Vulnerability Ghostwrite:	Not affected	
Vulnerability Indirect target selection:	Not affected	
Vulnerability Itlb multihit:	Not affected	
Vulnerability L1tf:	Not affected	
Vulnerability Mds:	Not affected	
Vulnerability Meltdown:	Not affected	
Vulnerability Mmio stale data:	Not affected	
Vulnerability Reg file data sampling:	Not affected	
Vulnerability Retbleed:	Not affected	
Vulnerability Spec rstack overflow:	Mitigation; IBPB on VMEXIT only	
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl	
Vulnerability Spectre v1:	Mitigation; usercopy/swapgs barriers and __user pointer sanitization	
Vulnerability Spectre v2:	Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP	

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero SDA200A2N-212)

(3.20 GHz, AMD EPYC 9255)

SPECspeed®2017_int_base = 18.3

SPECspeed®2017_int_peak = 18.5

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Platform Notes (Continued)

Vulnerability Srbds:

Vulnerability Tsx async abort:

always-on; PBR SB-eIBRS Not affected; BHI Not affected

Not affected

Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	2.3M	12	Data	1	64	1	64
L1i	32K	1.5M	8	Instruction	1	64	1	64
L2	1M	48M	16	Unified	2	1024	1	64
L3	32M	256M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0-23,48-71

node 0 size: 773649 MB

node 0 free: 772076 MB

node 1 cpus: 24-47,72-95

node 1 size: 774068 MB

node 1 free: 772593 MB

node distances:

node 0 1

0: 10 32

1: 32 10

9. /proc/meminfo

MemTotal: 1584862956 kB

10. who -r

run-level 3 Sep 24 15:37

11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.8)

Default Target Status

multi-user degraded

12. Failed units, from systemctl list-units --state=failed

UNIT	LOAD	ACTIVE	SUB	DESCRIPTION
------	------	--------	-----	-------------

* NetworkManager-wait-online.service loaded failed failed Network Manager Wait Online

Legend: LOAD -> Reflects whether the unit definition was properly loaded.

ACTIVE -> The high-level unit activation state, i.e. generalization of SUB.

SUB -> The low-level unit activation state, values depend on unit type.

1 loaded units listed.

13. Services, from systemctl list-unit-files

STATE	UNIT FILES
-------	------------

enabled	ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon anacron apparmor apport avahi-daemon bluetooth cloud-config cloud-final cloud-init cloud-init-local collectd console-setup cron cups cups-browsed dmesg e2scrub_reap getty@ gnome-remote-desktop gpu-manager grub-common grub-initrd-fallback kerneloops keyboard-setup networkd-dispatcher openvpn power-profiles-daemon rsyslog secureboot-db setvtrgb snapd ssl-cert sssd switcheroo-control sysstat systemd-oomd systemd-pstore systemd-resolved systemd-timesyncd thermald ua-reboot-cmds ubuntu-advantage udisks2 ufw unattended-upgrades wpa_supplicant
enabled-runtime	netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero SDA200A2N-212)

(3.20 GHz, AMD EPYC 9255)

SPECspeed®2017_int_base = 18.3

SPECspeed®2017_int_peak = 18.5

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Platform Notes (Continued)

disabled brlty console-getty debug-shell ipmievdf nftables openvpn-client@ openvpn-server@ openvpn@
rsync rtkit-daemon serial-getty@ speech-dispatcherd ssh systemd-boot-check-no-failures
systemd-confext systemd-network-generator systemd-networkd systemd-networkd-wait-online
systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code
systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy
systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysex
systemd-time-wait-sync upower wpa_supplicant-nl80211@ wpa_supplicant-wired@
wpa_supplicant@
generated openipmi speech-dispatcher
indirect saned@ spice-vdagentd sssd-autofs sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo
systemd-sysupdate systemd-sysupdate-reboot uidd
masked alsa-utils cryptdisks cryptdisks-early hwclock saned sudo x11-common

14. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=/vmlinuz-6.14.0-29-generic
root=UUID=4c4111a1-2027-4689-8cc8-4d6d365932ca
ro
quiet
splash
vt.handoff=7

15. cpupower frequency-info

analyzing CPU 77:
current policy: frequency should be within 2.30 GHz and 4.32 GHz.
The governor "performance" may decide which speed to use
within this range.
boost state support:
Supported: yes
Active: yes

16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	1
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	1

17. /sys/kernel/mm/transparent_hugepage

defrag	[always] defer defer+madvise never
enabled	[always] madvise never
hpage_pmd_size	2097152
shmem_enabled	always within_size advise [never] deny force

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero SDA200A2N-212)

(3.20 GHz, AMD EPYC 9255)

SPECspeed®2017_int_base = 18.3

SPECspeed®2017_int_peak = 18.5

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Platform Notes (Continued)

18. /sys/kernel/mm/transparent_hugepage/khugepaged

```
alloc_sleep_millisecs    60000
defrag                    1
max_ptes_none             511
max_ptes_shared           256
max_ptes_swap             64
pages_to_scan             4096
scan_sleep_millisecs     10000
```

19. OS release

```
From /etc/*-release /etc/*-version
os-release Ubuntu 24.04 LTS
```

20. Disk information

SPEC is set to: /home/cpu2017

```
Filesystem  Type  Size  Used Avail Use% Mounted on
/dev/nvme0n1p5 ext4   701G   7.9G  657G   2% /home
```

21. /sys/devices/virtual/dmi/id

```
Vendor:      Tyrone Systems
Product:     Tyrone Camarero MDA200A3N-212
Product Family: Camarero
```

22. dmidecode

Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
24x Samsung M321R8GA0PB0-CWMCJ 64 GB 2 rank 5600

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

```
BIOS Vendor:      American Megatrends International, LLC.
BIOS Version:     ES312AMS.205T5
BIOS Date:        09/02/2025
BIOS Revision:    5.35
```

Compiler Version Notes

```
=====  
C      | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)  
      | 657.xz_s(base, peak)  
=====
```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero SDA200A2N-212)

(3.20 GHz, AMD EPYC 9255)

SPECspeed®2017_int_base = 18.3

SPECspeed®2017_int_peak = 18.5

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Compiler Version Notes (Continued)

C++ | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
| 641.leela_s(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Fortran | 648.exchange2_s(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Base Portability Flags

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64

602.gcc_s: -DSPEC_LP64

605.mcf_s: -DSPEC_LP64

620.omnetpp_s: -DSPEC_LP64

623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64

625.x264_s: -DSPEC_LP64

631.deepsjeng_s: -DSPEC_LP64

641.leela_s: -DSPEC_LP64

648.exchange2_s: -DSPEC_LP64

657.xz_s: -DSPEC_LP64



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero SDA200A2N-212)

(3.20 GHz, AMD EPYC 9255)

SPECspeed®2017_int_base = 18.3

SPECspeed®2017_int_peak = 18.5

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -Wl,-mllvm -Wl,-extra-inliner -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP
-flto -fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp -lamdlibm
-lflang -lamdalloc
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc-ext
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-iv-split -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -O3 -march=znver5 -fveclib=AMDLIBM
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc
```

Base Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero SDA200A2N-212)

(3.20 GHz, AMD EPYC 9255)

SPECspeed®2017_int_base = 18.3

SPECspeed®2017_int_peak = 18.5

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang
```

602.gcc_s: Same as 600.perlbench_s

```
605.mcf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang
```

625.x264_s: Same as 600.perlbench_s

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero SDA200A2N-212)

(3.20 GHz, AMD EPYC 9255)

SPECspeed®2017_int_base = 18.3

SPECspeed®2017_int_peak = 18.5

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Peak Optimization Flags (Continued)

657.xz_s: Same as 600.perlbench_s

C++ benchmarks:

620.omnetpp_s: -m64 -std=c++14

-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdalloc-ext -lflang

623.xalancbmk_s: -m64 -std=c++14

-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -fopenmp=libomp -lomp
-lamdlibm -lamdalloc-ext -lflang

631.deepsjeng_s: -m64 -std=c++14

-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

641.leela_s: Same as 631.deepsjeng_s

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-iv-split -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -O3 -march=znver5 -fveclib=AMDLIBM
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero SDA200A2N-212)

(3.20 GHz, AMD EPYC 9255)

SPECspeed®2017_int_base = 18.3

SPECspeed®2017_int_peak = 18.5

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-Turin-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-Turin-revD.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-09-25 02:01:21-0400.

Report generated on 2025-12-03 14:21:20 by CPU2017 PDF formatter v6716.

Originally published on 2025-12-02.