



SPEC CPU®2017 Floating Point Rate Result

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Fsas Technologies Inc.

(Test Sponsor: Fujitsu)

SPECrate®2017_fp_base = 2980

PRIMERGY RX4770 M8, Intel Xeon 6788P, 2.00GHz

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 19

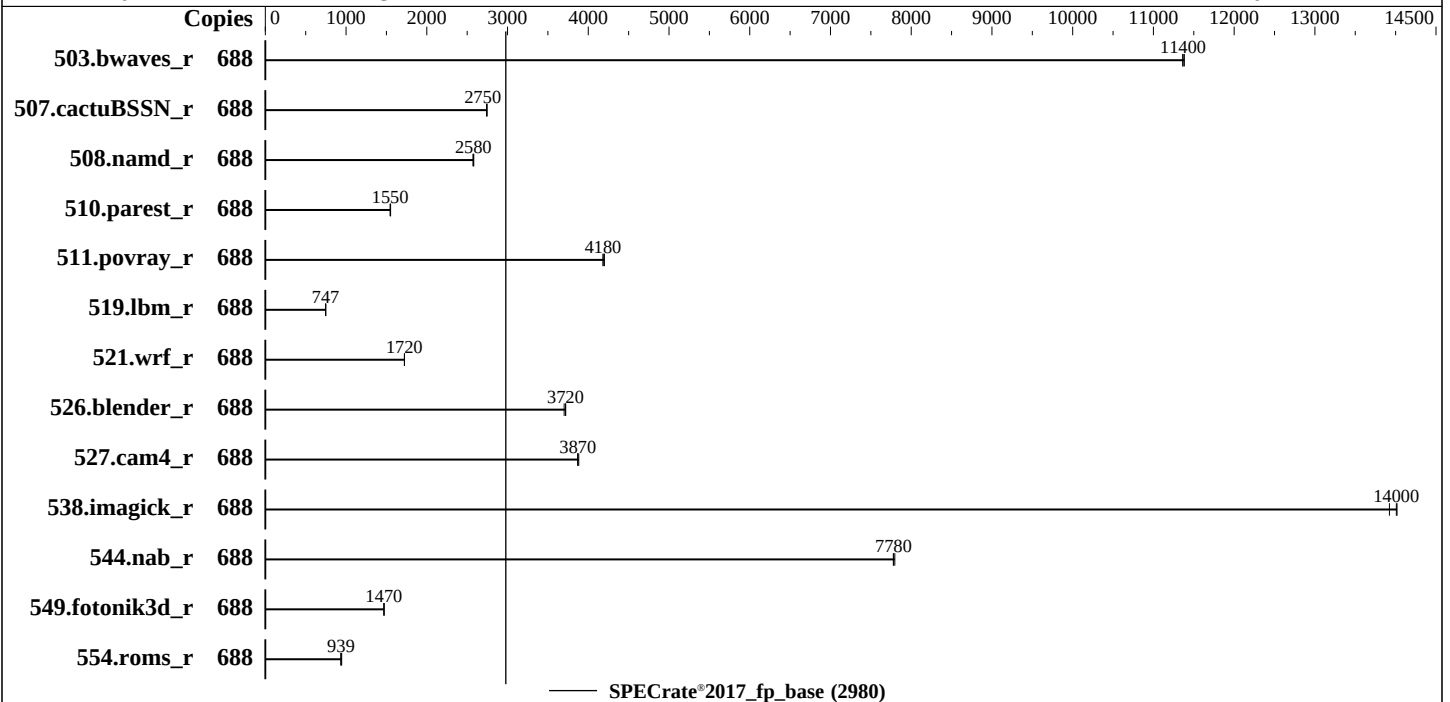
Test Sponsor: Fujitsu

Tested by: Fsas Technologies Inc.

Test Date: Nov-2025

Hardware Availability: Dec-2025

Software Availability: Jun-2025



Hardware

CPU Name: Intel Xeon 6788P
Max MHz: 3800
Nominal: 2000
Enabled: 344 cores, 4 chips, 2 threads/core
Orderable: 2,4 chips
Cache L1: 64 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 336 MB I+D on chip per chip
Other: None
Memory: 2 TB (32 x 64 GB 2Rx4 PC5-6400B-R)
Storage: 380 GB on tmpfs
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP7 6.4.0-150700.51-default
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel: No
Firmware: Fsas Technologies Inc. BIOS Version V1.0.0.0 R1.2.0 for D4136-A1x. Released Dec-2025 tested as V1.0.0.0 R1.0.0 for D4136-A1x. Oct-2025
File System: tmpfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	688	607	11400	607	11400	606	11400							
507.cactuBSSN_r	688	317	2750	318	2740	317	2750							
508.namd_r	688	254	2570	253	2580	254	2580							
510.parest_r	688	1163	1550	1160	1550	1164	1550							
511.povray_r	688	384	4180	384	4180	382	4200							
519.lbm_r	688	970	747	970	747	970	747							
521.wrf_r	688	894	1720	894	1720	895	1720							
526.blender_r	688	283	3700	282	3720	282	3720							
527.cam4_r	688	311	3870	311	3870	310	3880							
538.imagick_r	688	123	13900	122	14000	122	14000							
544.nab_r	688	149	7780	149	7800	149	7780							
549.fotonik3d_r	688	1824	1470	1824	1470	1824	1470							
554.roms_r	688	1156	946	1169	935	1165	939							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/mnt/ramdisk/speccpu-24.1/lib/intel64:/mnt/ramdisk/speccpu-24.1/je5.0.1-64"
MALLOCCONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

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General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 380 GB ramdisk created with the command of "mount -t tmpfs -o size=380G tmpfs /mnt/ramdisk" jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS configuration:
Fan Control = Full
SNC (Sub NUMA) = Enabled
CPU Performance Boost = Aggressive
DCU Streamer Prefetcher = Disabled
Homeless Prefetch = Enabled
XPT Prefetch = Disabled
Loctorem Thresholds Normal = Low
Loctorem Thresholds Empty = Low
LLC Dead Line Alloc = Disabled
APS rocketing = Enabled
ASPM Support = Auto

Sysinfo program /mnt/ramdisk/speccpu-24.1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Fri Nov 28 07:43:01 2025

SUT (System Under Test) info as seen by some common utilities.

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13. Linux kernel boot-time arguments, from /proc/cmdline
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Platform Notes (Continued)

1. uname -a

```
Linux localhost 6.4.0-150700.51-default #1 SMP PREEMPT_DYNAMIC Wed Apr 30 21:35:43 UTC 2025 (6930611)
x86_64 x86_64 x86_64 GNU/Linux
```

2. w

```
07:43:01 up 5 min,  2 users,  load average: 1.17, 6.03, 3.44
USER      TTY      FROM            LOGIN@   IDLE   JCPU   PCPU WHAT
root      tty1      -               07:42    13.00s 2.88s  1.32s -bash
root      pts/0    192.168.10.106  07:40    37.00s 0.10s  0.10s -bash
```

3. Username

From environment variable \$USER: root

4. ulimit -a

```
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 8253585
max locked memory       (kbytes, -l) 8192
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 8253585
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=688 -c
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=344 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base --o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=688 --configfile
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=344 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode
rate --tune base --size refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.fprate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/speccpu-24.1
```

6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) 6788P
vendor_id      : GenuineIntel
cpu family     : 6
model          : 173
stepping       : 1
microcode      : 0x10003f3
bugs           : spectre_v1 spectre_v2 spec_store_bypass swaps bhi
```

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Platform Notes (Continued)

```
cpu cores      : 86
siblings       : 172
4 physical ids (chips)
688 processors (hardware threads)
physical id 0: core ids 0-42,64-106
physical id 1: core ids 0-42,64-106
physical id 2: core ids 0-42,64-106
physical id 3: core ids 0-42,64-106
physical id 0: apicids 0-85,128-213
physical id 1: apicids 256-341,384-469
physical id 2: apicids 512-597,640-725
physical id 3: apicids 768-853,896-981
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.40.4:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                688
On-line CPU(s) list:   0-687
Vendor ID:             GenuineIntel
Model name:            Intel(R) Xeon(R) 6788P
CPU family:            6
Model:                 173
Thread(s) per core:    2
Core(s) per socket:    86
Socket(s):             4
Stepping:              1
CPU(s) scaling MHz:    21%
CPU max MHz:           3800.0000
CPU min MHz:           800.0000
BogoMIPS:              4000.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcem pcid dca sse4_1 sse4_2 x2apic movbe popcnt
tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2
ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm
rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb
intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1
xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req vnni avx512vbmi
umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk
pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile amx_int8 flush_l1d
arch_capabilities
Virtualization:        VT-x
L1d cache:             16.1 MiB (344 instances)
L1i cache:             21.5 MiB (344 instances)
```

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Platform Notes (Continued)

L2 cache: 688 MiB (344 instances)
L3 cache: 1.3 GiB (4 instances)
NUMA node(s): 8
NUMA node0 CPU(s): 0-42,344-386
NUMA node1 CPU(s): 43-85,387-429
NUMA node2 CPU(s): 86-128,430-472
NUMA node3 CPU(s): 129-171,473-515
NUMA node4 CPU(s): 172-214,516-558
NUMA node5 CPU(s): 215-257,559-601
NUMA node6 CPU(s): 258-300,602-644
NUMA node7 CPU(s): 301-343,645-687
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSE-eIBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	16.1M	12	Data	1	64	1	64
L1i	64K	21.5M	16	Instruction	1	64	1	64
L2	2M	688M	16	Unified	2	2048	1	64
L3	336M	1.3G	16	Unified	3	344064	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)
node 0 cpus: 0-42,344-386
node 0 size: 257439 MB
node 0 free: 256466 MB
node 1 cpus: 43-85,387-429
node 1 size: 258025 MB
node 1 free: 257302 MB
node 2 cpus: 86-128,430-472
node 2 size: 257986 MB
node 2 free: 257129 MB
node 3 cpus: 129-171,473-515
node 3 size: 258025 MB
node 3 free: 257350 MB
node 4 cpus: 172-214,516-558
node 4 size: 258025 MB
node 4 free: 257360 MB
node 5 cpus: 215-257,559-601
node 5 size: 258025 MB
node 5 free: 257306 MB
node 6 cpus: 258-300,602-644
node 6 size: 258025 MB
node 6 free: 257299 MB
node 7 cpus: 301-343,645-687

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Platform Notes (Continued)

node 7 size: 257871 MB
node 7 free: 249066 MB
node distances:
node 0 1 2 3 4 5 6 7
0: 10 12 21 21 21 21 21 21
1: 12 10 21 21 21 21 21 21
2: 21 21 10 12 21 21 21 21
3: 21 21 12 10 21 21 21 21
4: 21 21 21 21 10 12 21 21
5: 21 21 21 21 12 10 21 21
6: 21 21 21 21 21 21 10 12
7: 21 21 21 21 21 21 12 10

9. /proc/meminfo
MemTotal: 2112945440 kB

10. who -r
run-level 3 Nov 28 07:39

11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)
Default Target Status
multi-user running

12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance
issue-generator kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections
nvmf-autoconnect postfix purge-kernels rollback rsyslog sep5 smartd sshd systemd-pstore
wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs
disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
firewalld fsidd gpm grub2-once haveged ipmi ipmievdev issue-add-ssh-keys kexec-load lunmask
man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd serial-getty@
smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures systemd-confext
systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd
vncserver@
indirect systemd-userdbd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150700.51-default
root=UUID=6490486a-f020-45c4-b83e-03e98fc56a3b
splash=silent
mitigations=auto
quiet
security=apparmor

14. cpupower frequency-info
analyzing CPU 4:
current policy: frequency should be within 800 MHz and 3.80 GHz.
The governor "powersave" may decide which speed to use
within this range.
boost state support:
Supported: yes

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Platform Notes (Continued)

Active: yes

15. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	20
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	60
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	0

16. /sys/kernel/mm/transparent_hugepage

defrag	always	defer	defer+madvise	[madvise]	never
enabled	[always]	madvise	never		
hpage_pmd_size	2097152				
shmem_enabled	always	within_size	advise	[never]	deny force

17. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

18. OS release

From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP7

19. Disk information

SPEC is set to: /mnt/ramdisk/speccpu-24.1

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
tmpfs	tmpfs	380G	7.9G	373G	3%	/mnt/ramdisk

20. /sys/devices/virtual/dmi/id

Vendor:	Fsas Technologies
Product:	PRIMERGY RX4770 M8
Product Family:	SERVER
Serial:	xxxxxxxxxx

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Platform Notes (Continued)

21. dmidecode

Additional information from dmidecode 3.6 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

32x Micron MTC40F2046S1RC64BD2 MWFF 64 GB 2 rank 6400

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Fsas Technologies
BIOS Version: V1.0.0.0 R1.0.0 for D4136-A1x
BIOS Date: 10/28/2025
BIOS Revision: 1.0
Firmware Revision: 3.6

Compiler Version Notes

C | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C++ | 508.namd_r(base) 510.parest_r(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C++, C | 511.povray_r(base) 526.blender_r(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C++, C, Fortran | 507.cactuBSSN_r(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Fortran | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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SPECrate®2017_fp_base = 2980

PRIMERGY RX4770 M8, Intel Xeon 6788P, 2.00GHz

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fsas Technologies Inc.

Test Date: Nov-2025

Hardware Availability: Dec-2025

Software Availability: Jun-2025

Compiler Version Notes (Continued)

=====
Fortran, C | 521.wrf_r(base) 527.cam4_r(base)
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308

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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308

Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64

507.cactuBSSN_r: -DSPEC_LP64

508.namd_r: -DSPEC_LP64

510.parest_r: -DSPEC_LP64

511.povray_r: -DSPEC_LP64

519.lbm_r: -DSPEC_LP64

521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian

526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char

527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG

538.imagick_r: -DSPEC_LP64

544.nab_r: -DSPEC_LP64

549.fotonik3d_r: -DSPEC_LP64

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

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Base Portability Flags (Continued)

554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Fsas-Platform-Settings-V1.0-GNR-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Fsas-Platform-Settings-V1.0-GNR-RevB.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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