



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017_fp_base = 94.2

PowerEdge R360 (Intel Xeon 6337P)

SPECrate®2017_fp_peak = 95.0

CPU2017 License: 6573

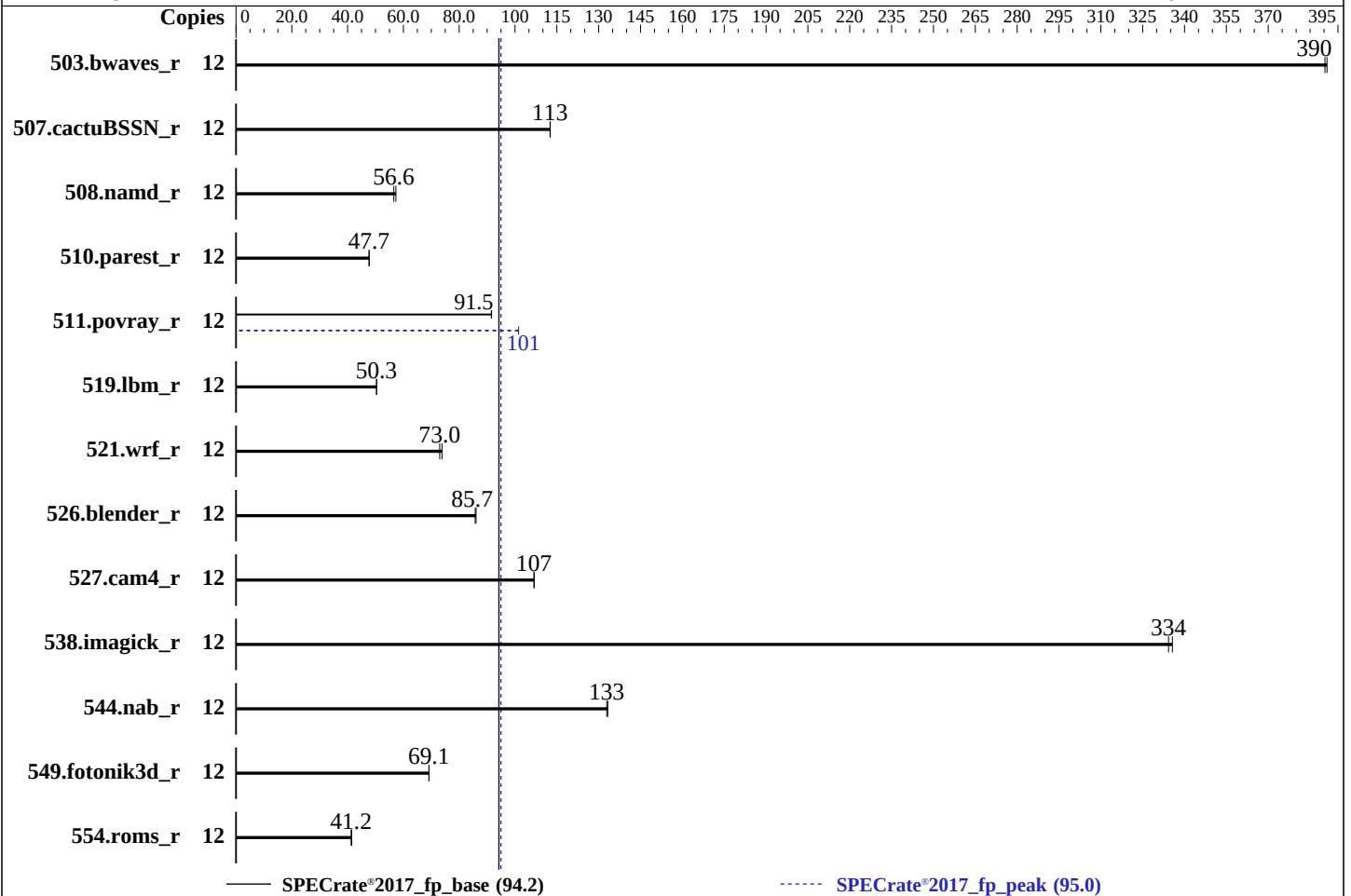
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Dec-2024

Hardware Availability: Mar-2025

Software Availability: Jun-2024



Hardware

CPU Name: Intel Xeon 6337P
Max MHz: 5300
Nominal: 3500
Enabled: 6 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 18 MB I+D on chip per chip
Other: None
Memory: 64 GB (2 x 32 GB 2Rx8 PC5-5600B-E, running at 4400)
Storage: 40 GB on tmpfs
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6
6.4.0-150600.21-default
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel: No
Firmware: Version 2.0.0 released Dec-2024
File System: tmpfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	12	308	390	308	391			12	308	390	308	391		
507.cactuBSSN_r	12	135	113	135	113			12	135	113	135	113		
508.namd_r	12	199	57.3	202	56.6			12	199	57.3	202	56.6		
510.parest_r	12	659	47.7	657	47.8			12	659	47.7	657	47.8		
511.povray_r	12	306	91.5	306	91.6			12	277	101	277	101		
519.lbm_r	12	251	50.4	251	50.3			12	251	50.4	251	50.3		
521.wrf_r	12	364	73.8	368	73.0			12	364	73.8	368	73.0		
526.blender_r	12	212	86.0	213	85.7			12	212	86.0	213	85.7		
527.cam4_r	12	196	107	197	107			12	196	107	197	107		
538.imagick_r	12	88.9	336	89.3	334			12	88.9	336	89.3	334		
544.nab_r	12	152	133	152	133			12	152	133	152	133		
549.fotonik3d_r	12	676	69.1	676	69.2			12	676	69.1	676	69.2		
554.roms_r	12	462	41.2	460	41.5			12	462	41.2	460	41.5		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH =

"/mnt/ramdisk/cpu2017-1.1.9-ic2024.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2024.1/je5.0.1-64"

MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

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General Notes (Continued)

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 40 GB ramdisk created with the cmd: "mount -t tmpfs -o size=40G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:
Defaults set

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2024.1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on 1234567-R360 Tue Dec 31 01:18:31 2024

SUT (System Under Test) info as seen by some common utilities.

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12. Services, from systemctl list-unit-files
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15. tuned-adm active
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17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
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21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

1. uname -a
Linux 1234567-R360 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09/lp)
x86_64 x86_64 x86_64 GNU/Linux

2. w
01:18:31 up 2:56, 1 user, load average: 9.23, 11.40, 11.79
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

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Platform Notes (Continued)

```
root      tty1      -                22:22    2:55m  0.77s  0.00s /bin/bash
/home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-VERS=6.1 --output_format html,pdf,txt
```

3. Username

From environment variable \$USER: root

4. ulimit -a

```
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size                (blocks, -f) unlimited
pending signals         (-i) 256405
max locked memory       (kbytes, -l) 8192
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 256405
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
login -- root
-bash
/bin/bash /home/DellFiles/bin/DELL_rate.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-VERS=6.1 --output_format
html,pdf,txt
/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-VERS=6.1 --output_format
html,pdf,txt
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=12 -c
ic2024.1-lin-core-avx2-rate-20240308.cfg --define smt-on --define cores=6 --define physicallogical
--define no-numa --tune base,peak -o all --define drop_caches --iterations 2 --define DL-VERS=6.1
--output_format html,pdf,txt fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=12 --configfile
ic2024.1-lin-core-avx2-rate-20240308.cfg --define smt-on --define cores=6 --define physicallogical
--define no-numa --tune base,peak --output_format all --define drop_caches --iterations 2 --define
DL-VERS=6.1 --output_format html,pdf,txt --nopower --runmode rate --tune base:peak --size refrate fprate
--nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.002/tempslogs/preenv.fprate.002.0.log --lognum 002.0
--from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2024.1
```

6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) 6337P
vendor_id      : GenuineIntel
cpu family     : 6
model          : 183
stepping       : 1
microcode      : 0x12c
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb bhi
```

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Platform Notes (Continued)

cpu cores : 6
siblings : 12
1 physical ids (chips)
12 processors (hardware threads)
physical id 0: core ids 0-5
physical id 0: apicids 0-11

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 42 bits physical, 48 bits virtual
Byte Order: Little Endian
CPU(s): 12
On-line CPU(s) list: 0-11
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel
Model name: Intel(R) Xeon(R) 6337P
BIOS Model name: Intel(R) Xeon(R) 6337P CPU @ 3.5GHz
BIOS CPU family: 179
CPU family: 6
Model: 183
Thread(s) per core: 2
Core(s) per socket: 6
Socket(s): 1
Stepping: 1
BogoMIPS: 6988.80
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
pclmulqdq dtes64 monitor ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr
pdcmm sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault ssbd ibrs ibpb
stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms
invpcid rdseed adx smap clflushopt clwb intel_pt sha_ni xsaveopt
xsavec xgetbv1 xsaves split_lock_detect user_shstk avx_vnni dtherm
ida arat pln pts hfi umip pku ospke waitpkg gfni vaes vpclmulqdq tme
rdrnd movdiri movdir64b fsrm md_clear serialize pconfig arch_lbr ibt
flush_l1d arch_capabilities
L1d cache: 288 KiB (6 instances)
L1i cache: 192 KiB (6 instances)
L2 cache: 12 MiB (6 instances)
L3 cache: 18 MiB (1 instance)
NUMA node(s): 1
NUMA node0 CPU(s): 0-11
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
```

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Platform Notes (Continued)

Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSE-eIBRS SW sequence; BHI BHI_DIS_S
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	288K	12	Data	1	64	1	64
L1i	32K	192K	8	Instruction	1	64	1	64
L2	2M	12M	16	Unified	2	2048	1	64
L3	18M	18M	9	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 1 nodes (0)
node 0 cpus: 0-11
node 0 size: 64132 MB
node 0 free: 52603 MB
node distances:
node 0
0: 10

9. /proc/meminfo

MemTotal: 65671240 kB

10. who -r

run-level 3 Dec 30 22:22

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

Default Target Status
multi-user running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron display-manager firewalld getty@ irqbalance issue-generator kbdsettings klog lvm2-monitor nsd nvme-fc-boot-connections nvme-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant
enabled-runtime	systemd-remount-fs
disabled	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon autofs autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell dmraid-activation dnsmasq ebttables exchange-bmc-os-info fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmiev issue-add-ssh-keys kexec-load ksm kvm_stat lunmask man-db-create multipathd nfs nfs-blkmap nmb openvpn@ ostree-remount rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd_generate_opts smb snmpd snmptrapd speech-dispatcherd svnserv systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd tuned udisks2 update-system-flatpaks upower vncserver@ wpa_supplicant@
indirect	pcsd saned@ systemd-userdbd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

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Platform Notes (Continued)

```
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=a32afac2-5c61-4e8f-b8a9-ea3e26e773e7
splash=silent
resume=/dev/disk/by-uuid/0edd05e3-85a6-4dcb-a034-c5d5547f2fba
mitigations=auto
quiet
security=apparmor
```

```
-----
14. cpupower frequency-info
   analyzing CPU 3:
     Unable to determine current policy
     boost state support:
       Supported: yes
       Active: yes
```

```
-----
15. tuned-adm active
   No current active profile.
```

```
-----
16. sysctl
   kernel.numa_balancing          0
   kernel.randomize_va_space      2
   vm.compaction_proactiveness     20
   vm.dirty_background_bytes       0
   vm.dirty_background_ratio       10
   vm.dirty_bytes                  0
   vm.dirty_expire_centisecs       3000
   vm.dirty_ratio                  20
   vm.dirty_writeback_centisecs    500
   vm.dirtytime_expire_seconds     43200
   vm.extfrag_threshold            500
   vm.min_unmapped_ratio           1
   vm.nr_hugepages                 0
   vm.nr_hugepages_mempolicy        0
   vm.nr_overcommit_hugepages      0
   vm.swappiness                   60
   vm.watermark_boost_factor       15000
   vm.watermark_scale_factor       10
   vm.zone_reclaim_mode            0
```

```
-----
17. /sys/kernel/mm/transparent_hugepage
   defrag          always defer defer+madvice [madvice] never
   enabled          [always] madvice never
   hpage_pmd_size   2097152
   shmem_enabled    always within_size advise [never] deny force
```

```
-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
   alloc_sleep_millisecs  60000
   defrag                  1
   max_ptes_none           511
   max_ptes_shared         256
   max_ptes_swap           64
   pages_to_scan           4096
   scan_sleep_millisecs    10000
```

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Platform Notes (Continued)

19. OS release

From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6

20. Disk information

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2024.1
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 40G 5.0G 36G 13% /mnt/ramdisk

21. /sys/devices/virtual/dmi/id

Vendor: Dell Inc.
Product: PowerEdge R360
Product Family: PowerEdge
Serial: 1234567

22. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

2x 00AD00001501 HMC688AGBEA084N 32 GB 2 rank 5600, configured at 4400

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Dell Inc.
BIOS Version: 2.0.0
BIOS Date: 12/20/2024
BIOS Revision: 2.0

Compiler Version Notes

C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Compiler Version Notes (Continued)

=====
C++, C, Fortran | 507.cactuBSSN_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====
Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====
Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx



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Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:

-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

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SPEC CPU®2017 Floating Point Rate Result

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Dell Inc.

SPECrate®2017_fp_base = 94.2

PowerEdge R360 (Intel Xeon 6337P)

SPECrate®2017_fp_peak = 95.0

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Dec-2024

Hardware Availability: Mar-2025

Software Availability: Jun-2024

Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: basepeak = yes

(Continued on next page)



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Peak Optimization Flags (Continued)

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: basepeak = yes

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: basepeak = yes

Benchmarks using both Fortran and C:

521.wrf_r: basepeak = yes

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -w -std=c++14 -m64 -std=c11 -wl, -z, muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2 -flto
-Ofast -ffast-math -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2025-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.18t.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2025-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.18t.xml>



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