



SPEC CPU®2017 Integer Rate Result

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xFusion

SPECrate®2017_int_base = 404

FusionServer 1288 V8 (Intel Xeon 6517P)

SPECrate®2017_int_peak = Not Run

CPU2017 License: 6488

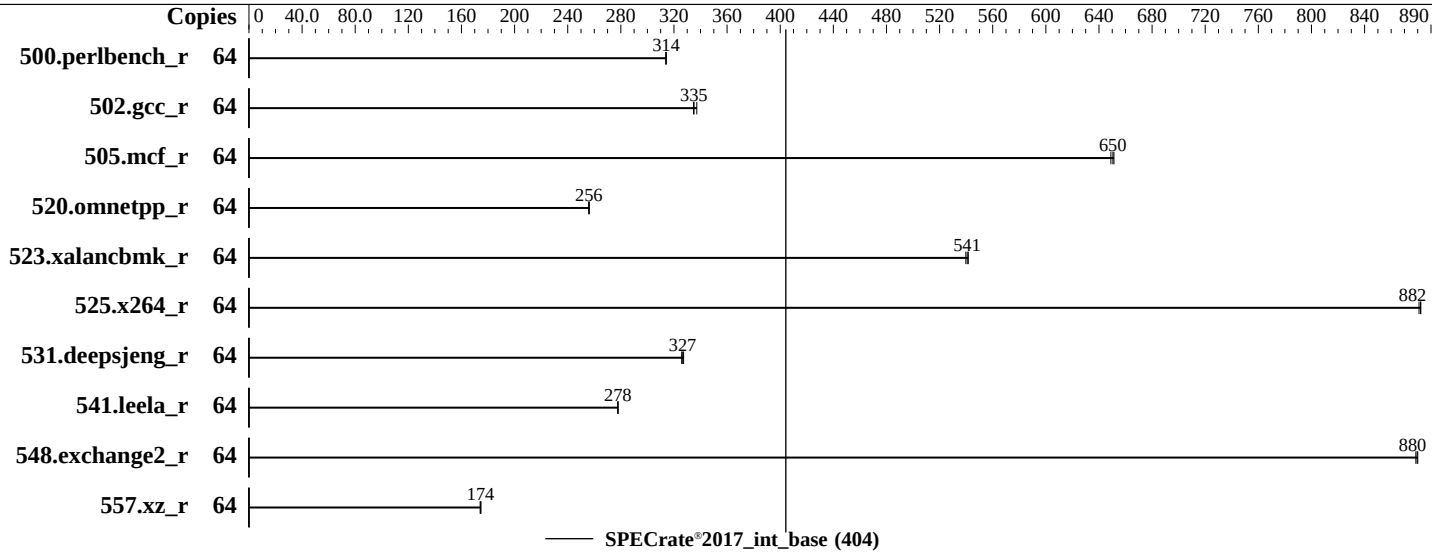
Test Sponsor: xFusion

Tested by: xFusion

Test Date: Dec-2025

Hardware Availability: May-2025

Software Availability: Apr-2024



Hardware

CPU Name: Intel Xeon 6517P
Max MHz: 4200
Nominal: 3200
Enabled: 32 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 64 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 72 MB I+D on chip per chip
Other: None
Memory: 512 GB (16 x 32 GB 2Rx8 PC5-6400B-R)
Storage: 1 x 960 GB SATA SSD
Other: CPU Cooling: Air

Software

OS: Red Hat Enterprise Linux 9.4 (Plow)
5.14.0-427.13.1.el9_4.x86_64
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++
Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran Compiler
for Linux;
Parallel: No
Firmware: Version 01.31.01.16 released Nov-2025
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: None
Power Management: BIOS and OS set to prefer performance at the cost
of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	64	324	314	325	314	<u>324</u>	<u>314</u>							
502.gcc_r	64	271	335	269	337	<u>270</u>	<u>335</u>							
505.mcf_r	64	159	651	159	649	<u>159</u>	<u>650</u>							
520.omnetpp_r	64	<u>328</u>	<u>256</u>	328	256	328	256							
523.xalancbmk_r	64	125	542	125	540	<u>125</u>	<u>541</u>							
525.x264_r	64	127	882	127	881	<u>127</u>	<u>882</u>							
531.deepsjeng_r	64	224	327	<u>225</u>	<u>327</u>	225	326							
541.leela_r	64	381	278	<u>381</u>	<u>278</u>	382	278							
548.exchange2_r	64	191	879	191	880	<u>191</u>	<u>880</u>							
557.xz_r	64	<u>396</u>	<u>174</u>	396	175	397	174							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/QYL/cpu2017/lib/intel64:/home/QYL/cpu2017/lib/ia32:/home/QYL/cpu2017/je5.0.1-32"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
tuned-adm profile was set to throughput-performance using "tuned-adm profile throughput-performance"
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

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General Notes (Continued)

is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:

Performance Profile Set to Performance

LLC Prefetch Set to Enabled

DCU Streamer Prefetcher Set to Disabled

Latency Optimized Mode Set to Enabled

Sysinfo program /home/QYL/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost.localdomain Sat Dec 13 11:14:37 2025

SUT (System Under Test) info as seen by some common utilities.

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1. uname -a
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13. Linux kernel boot-time arguments, from /proc/cmdline
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17. /sys/kernel/mm/transparent_hugepage/khugepaged
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21. dmidecode
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1. uname -a
Linux localhost.localdomain 5.14.0-427.13.1.el9_4.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 10 10:29:16 EDT 2024 x86_64 x86_64 x86_64 GNU/Linux

2. w
11:14:37 up 5:15, 1 user, load average: 0.28, 21.28, 44.28
USER TTY LOGIN@ IDLE JCPU PCPU WHAT
root tty1 05:59 5:12m 1.01s 0.07s sh run_rate.sh

3. Username
From environment variable \$USER: root

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Platform Notes (Continued)

```
4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 2060156
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 2060156
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

-----

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 31
login -- root
-bash
sh loops.sh
sh run_rate.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=64 -c
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=32 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=64 --configfile
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=32 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode
rate --tune base --size refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/QYL/cpu2017

-----

6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) 6517P
vendor_id       : GenuineIntel
cpu family      : 6
model           : 173
stepping        : 1
microcode       : 0x10003c2
bugs             : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores       : 16
siblings        : 32
2 physical ids (chips)
64 processors (hardware threads)
physical id 0: core ids 0-15
physical id 1: core ids 0-15
physical id 0: apicids 0-31
physical id 1: apicids 128-159
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

-----

7. lscpu
```

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Platform Notes (Continued)

From lscpu from util-linux 2.37.4:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                64
On-line CPU(s) list:   0-63
Vendor ID:             GenuineIntel
BIOS Vendor ID:        Intel(R) Corporation
Model name:            Intel(R) Xeon(R) 6517P
BIOS Model name:       Intel(R) Xeon(R) 6517P
CPU family:            6
Model:                 173
Thread(s) per core:    2
Core(s) per socket:    16
Socket(s):             2
Stepping:              1
BogoMIPS:              6400.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb
                        rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl
                        xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq
                        dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca
                        sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
                        rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                        cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority
                        ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid
                        cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb
                        intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1
                        xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                        split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
                        vnmi avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
                        vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid
                        bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear
                        serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile
                        amx_int8 flush_l1d arch_capabilities

Virtualization:        VT-x
L1d cache:             1.5 MiB (32 instances)
L1i cache:             2 MiB (32 instances)
L2 cache:              64 MiB (32 instances)
L3 cache:              144 MiB (2 instances)
NUMA node(s):          2
NUMA node0 CPU(s):     0-15,32-47
NUMA node1 CPU(s):     16-31,48-63
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit:      Not affected
Vulnerability L1tf:              Not affected
Vulnerability Mds:               Not affected
Vulnerability Meltdown:          Not affected
Vulnerability Mmio stale data:    Not affected
Vulnerability Retbleed:          Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass:  Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:         Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:         Mitigation; Enhanced / Automatic IBRS, IBPB conditional, RSB filling,
                                   PBRSE-eIBRS Not affected
Vulnerability Srbds:             Not affected
Vulnerability Tsx async abort:    Not affected
```

From lscpu --cache:

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Platform Notes (Continued)

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.5M	12	Data	1	64	1	64
L1i	64K	2M	16	Instruction	1	64	1	64
L2	2M	64M	16	Unified	2	2048	1	64
L3	72M	144M	16	Unified	3	73728	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 2 nodes (0-1)
node 0 cpus: 0-15,32-47
node 0 size: 257088 MB
node 0 free: 255610 MB
node 1 cpus: 16-31,48-63
node 1 size: 257992 MB
node 1 free: 256452 MB
node distances:
node  0  1
 0:  10  21
 1:  21  10
```

9. /proc/meminfo

MemTotal: 527443484 kB

10. who -r

run-level 3 Dec 13 05:58

11. Systemd service manager version: systemd 252 (252-32.el9_4)

Default Target	Status
multi-user	running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd crond dbus-broker firewalld getty@ insights-client-boot irqbalance kdump low-memory-monitor lvm2-monitor mdmonitor microcode nis-domainname rhsmcertd rsyslog rtkit-daemon selinux-autorelabel-mark sshd sssd systemd-boot-update systemd-network-generator udisks2 upower
enabled-runtime	systemd-remount-fs
disabled	blk-availability canberra-system-bootup canberra-system-shutdown canberra-system-shutdown-reboot console-getty cpupower debug-shell dnf-system-upgrade kvm_stat man-db-restart-cache-update nftables pesign rdisc rhcd rhsm rhsm-facts rpmdb-rebuild selinux-check-proper-disable sep5 serial-getty@ sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysex
indirect	sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate systemd-sysupdate-reboot

13. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-427.13.1.el9_4.x86_64
root=/dev/mapper/rhel-root
ro
crashkernel=1G-4G:192M,4G-64G:256M,64G-:512M
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap
```

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Platform Notes (Continued)

14. cpupower frequency-info

```
analyzing CPU 40:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes
```

15. sysctl

```
kernel.numa_balancing          1
kernel.randomize_va_space      2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds   43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0
```

16. /sys/kernel/mm/transparent_hugepage

```
defrag          always defer defer+madvise [madvise] never
enabled         [always] madvise never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force
```

17. /sys/kernel/mm/transparent_hugepage/khugepaged

```
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
```

18. OS release

```
From /etc/*-release /etc/*-version
os-release      Red Hat Enterprise Linux 9.4 (Plow)
redhat-release  Red Hat Enterprise Linux release 9.4 (Plow)
system-release  Red Hat Enterprise Linux release 9.4 (Plow)
```

19. Disk information

SPEC is set to: /home/YQL/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/rhel-home	xfs	819G	26G	793G	4%	/home

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Platform Notes (Continued)

20. /sys/devices/virtual/dmi/id
Vendor: XFUSION
Product: 1288 V8
Product Family: Birch Stream

21. dmidecode
Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
16x Samsung M321R4GA3EB2-CCPWF 32 GB 2 rank 6400

22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: XFUSION
BIOS Version: 01.31.01.16
BIOS Date: 11/13/2025
BIOS Revision: 1.16

Compiler Version Notes

=====

C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base) 557.xz_r(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

C++ | 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base) 541.leela_r(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

Fortran | 548.exchange2_r(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmallo

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmallo

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmallo

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-GNR-V1.7.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-GNR-V1.7.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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