



SPEC CPU®2017 Integer Speed Result

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Dell Inc.

SPECspeed®2017_int_base = 16.7

PowerEdge R5715 (AMD EPYC 9015 8-Core Processor)

SPECspeed®2017_int_peak = 17.0

CPU2017 License: 6573

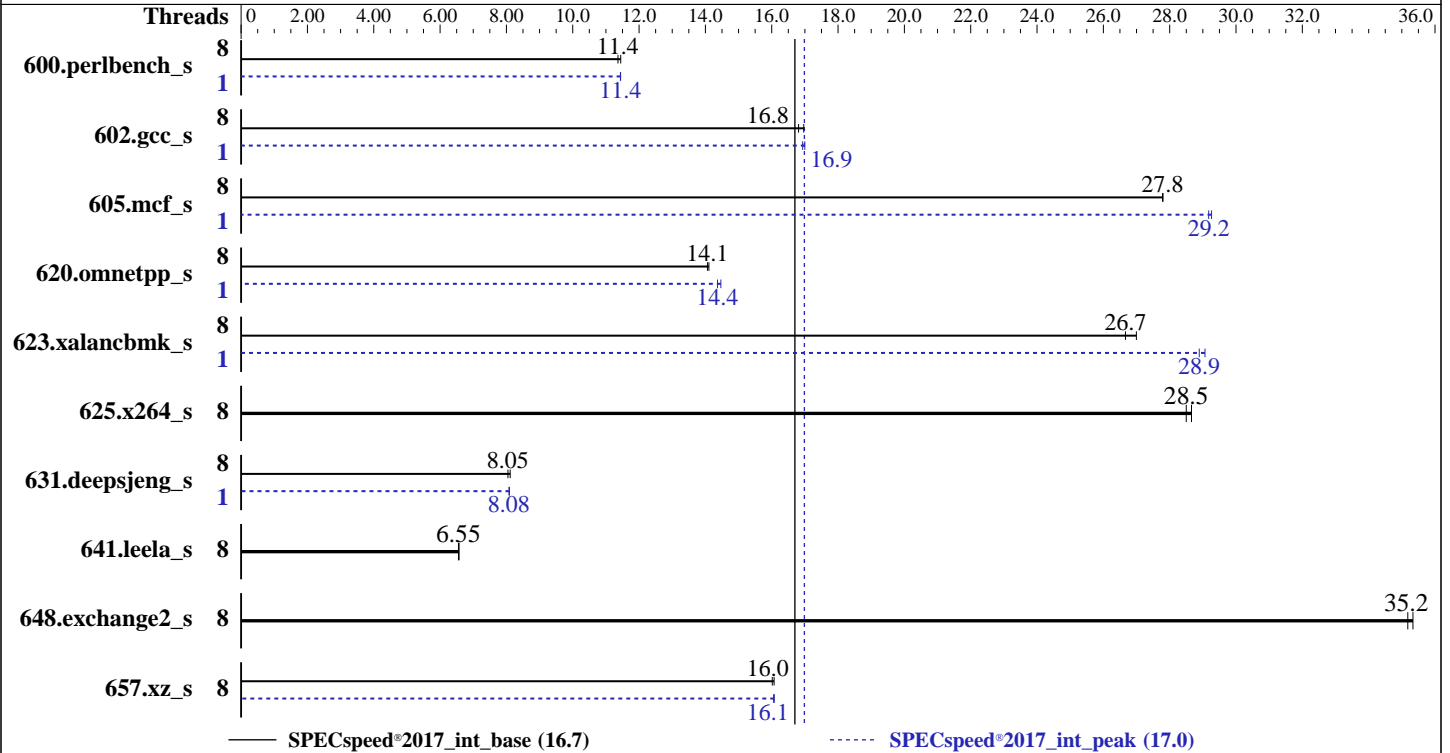
Test Date: Jan-2026

Test Sponsor: Dell Inc.

Hardware Availability: Mar-2026

Tested by: Dell Inc.

Software Availability: Dec-2025



Hardware

CPU Name: AMD EPYC 9015
 Max MHz: 4100
 Nominal: 3600
 Enabled: 8 cores, 1 chip
 Orderable: 1 chip
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 64 MB I+D on chip per chip, 32 MB shared / 4 cores
 Other: None
 Memory: 768 GB (12 x 64 GB 2Rx4 PC5-6400B-R, running at 5200)
 Storage: 30 GB on tmpfs
 Other: CPU Cooling: Air

Software

OS: Ubuntu 24.04.3 LTS
 6.8.0-90-generic
 Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
 Parallel: Yes
 Firmware: Version 1.6.3 released Jan-2026
 File System: tmpfs
 System State: Run level 5 (graphical multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: None
 Power Management: BIOS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base								Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
600.perlbench_s	8	155	11.4	<u>156</u>	<u>11.4</u>			1	<u>155</u>	<u>11.4</u>	155	11.4				
602.gcc_s	8	235	17.0	<u>237</u>	<u>16.8</u>			1	234	17.0	<u>235</u>	<u>16.9</u>				
605.mcf_s	8	<u>170</u>	<u>27.8</u>	170	27.8			1	161	29.3	<u>162</u>	<u>29.2</u>				
620.omnetpp_s	8	<u>116</u>	<u>14.1</u>	116	14.1			1	<u>114</u>	<u>14.4</u>	113	14.5				
623.xalancbmk_s	8	52.5	27.0	<u>53.1</u>	<u>26.7</u>			1	<u>49.0</u>	<u>28.9</u>	48.8	29.1				
625.x264_s	8	61.6	28.7	<u>61.9</u>	<u>28.5</u>			8	61.6	28.7	<u>61.9</u>	<u>28.5</u>				
631.deepsjeng_s	8	177	8.11	<u>178</u>	<u>8.05</u>			1	177	8.09	<u>177</u>	<u>8.08</u>				
641.leela_s	8	260	6.57	<u>260</u>	<u>6.55</u>			8	260	6.57	<u>260</u>	<u>6.55</u>				
648.exchange2_s	8	83.2	35.3	<u>83.6</u>	<u>35.2</u>			8	83.2	35.3	<u>83.6</u>	<u>35.2</u>				
657.xz_s	8	385	16.1	<u>386</u>	<u>16.0</u>			8	385	16.1	<u>385</u>	<u>16.1</u>				

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



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Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP_CPU_AFFINITY = "0-7"

LD_LIBRARY_PATH =

"/mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4/amd_speed_aocc500_znver5_A_lib/lib:/mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4/amd_speed_aocc500_znver5_A_lib/lib32:"

LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"

MALLOC_CONF = "retain:true"

OMP_DYNAMIC = "false"

OMP_SCHEDULE = "static"

OMP_STACKSIZE = "128M"

OMP_THREAD_LIMIT = "8"

Environment variables set by runcpu during the 600.perlbench_s peak run:

GOMP_CPU_AFFINITY = "0-7"

Environment variables set by runcpu during the 602.gcc_s peak run:

GOMP_CPU_AFFINITY = "0-7"

Environment variables set by runcpu during the 605.mcf_s peak run:

GOMP_CPU_AFFINITY = "0-7"

Environment variables set by runcpu during the 620.omnetpp_s peak run:

GOMP_CPU_AFFINITY = "0-7"

Environment variables set by runcpu during the 623.xalancbmk_s peak run:

GOMP_CPU_AFFINITY = "0-7"

Environment variables set by runcpu during the 631.deepsjeng_s peak run:

GOMP_CPU_AFFINITY = "0-7"

Environment variables set by runcpu during the 657.xz_s peak run:

GOMP_CPU_AFFINITY = "0-7"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB Memory using Ubuntu 22.04

Benchmark run from a 30 GB ramdisk created with the cmd: "mount -t tmpfs -o size=30G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:

Logical Processor : Disabled

Virtualization Technology : Disabled

NUMA Nodes Per Socket : 2

System Profile : Custom

C-States : Disabled

Memory Patrol Scrub : Disabled

PCI ASPM L1 Link Power Management : Disabled

Periodic Directory Rinse Tuning : Blended

Determinism Control : Manual

Determinism Slider : Power Determinism

Optimizer Mode : Enabled

Algorithm Performance Boost Disable : Enabled

ApbDis Fixed DF P-State : P0

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Platform Notes (Continued)

Adaptive Allocation : Enabled
Dram Refresh Delay : Performance
DIMM Self Healing -
on Uncorrectable Memory Error : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on SLR5751-R5715 Fri Jan 23 18:15:44 2026

SUT (System Under Test) info as seen by some common utilities.

----- Table of contents -----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 255 (255.4-lubuntu8.12)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux SLR5751-R5715 6.8.0-90-generic #91-Ubuntu SMP PREEMPT_DYNAMIC Tue Nov 18 14:14:30 UTC 2025 x86_64
x86_64 x86_64 GNU/Linux

2. w
18:15:44 up 8 min, 1 user, load average: 0.22, 0.06, 0.02
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root tty1 - 18:14 32.00s 1.20s 0.32s /bin/bash ./amd_speed_aocc500_znver5_A1.sh

3. Username
From environment variable \$USER: root

4. ulimit -a
time(seconds) unlimited
file(blocks) unlimited
data(kbytes) unlimited
stack(kbytes) unlimited
coredump(blocks) 0

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Platform Notes (Continued)

```
memory(kbytes)          unlimited
locked memory(kbytes)  2097152
process                 3093061
nofiles                 1024
vmemory(kbytes)        unlimited
locks                  unlimited
rtprio                  0
```

5. sysinfo process ancestry

```
/sbin/init
/bin/login -p --
-bash
/bin/bash /home/DellFiles/bin/DELL_speed.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh speed
/bin/bash /home/DellFiles/bin/dell-run-main.sh speed
/bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh speed --define DL-VERS=6.4_T17 --output_format
html,pdf,txt
python3 ./run_amd_speed_aocc500_znver5_A1.py
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
DL-VERS=6.4_T17 --output_format html,pdf,txt intspeed
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
DL-VERS=6.4_T17 --output_format html,pdf,txt --nopower --runmode speed --tune base:peak --size
test:train:refspeed intspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.intspeed.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4
```

6. /proc/cpuinfo

```
model name      : AMD EPYC 9015 8-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 26
model         : 2
stepping      : 1
microcode     : 0xb00215a
bugs          : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size     : 192 4K pages
cpu cores    : 8
siblings     : 8
1 physical ids (chips)
8 processors (hardware threads)
physical id 0: core ids 0-3,16-19
physical id 0: apicids 0-3,16-19
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                8
On-line CPU(s) list:   0-7
Vendor ID:              AuthenticAMD
BIOS Vendor ID:        AMD
```

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Platform Notes (Continued)

```

Model name: AMD EPYC 9015 8-Core Processor
BIOS Model name: AMD EPYC 9015 8-Core Processor CPU @ 3.6GHz
BIOS CPU family: 107
CPU family: 26
Model: 2
Thread(s) per core: 1
Core(s) per socket: 8
Socket(s): 1
Stepping: 1
Frequency boost: enabled
CPU(s) scaling MHz: 96%
CPU max MHz: 3600.0000
CPU min MHz: 1500.0000
BogoMIPS: 7189.51
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
smep bmi2 invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
xsaveerptr rdpru wbinvd amd_ppin cppc amd_ibpb_ret arat npt lbrv
svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl
vnni avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
movdiri movdir64b overflow_recov succor smca avx512_vp2intersect
flush_lld debug_swap

L1d cache: 384 KiB (8 instances)
L1i cache: 256 KiB (8 instances)
L2 cache: 8 MiB (8 instances)
L3 cache: 64 MiB (2 instances)
NUMA node(s): 2
NUMA node0 CPU(s): 0-3
NUMA node1 CPU(s): 4-7
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability Lltf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
disabled; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected

Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected
Vulnerability Vmscape: Not affected

```

From lscpu --cache:
NAME ONE-SIZE ALL-SIZE WAYS TYPE LEVEL SETS PHY-LINE COHERENCY-SIZE

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Platform Notes (Continued)

L1d	48K	384K	12 Data	1	64	1	64
L1i	32K	256K	8 Instruction	1	64	1	64
L2	1M	8M	16 Unified	2	1024	1	64
L3	32M	64M	16 Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0-3
node 0 size: 386362 MB
node 0 free: 380477 MB
node 1 cpus: 4-7
node 1 size: 386981 MB
node 1 free: 386548 MB
node distances:
node  0  1
  0: 10 12
  1: 12 10

```

9. /proc/meminfo

MemTotal: 791904944 kB

10. who -r

run-level 5 Jan 23 18:07

11. Systemd service manager version: systemd 255 (255.4-lubuntu8.12)

```

Default Target Status
graphical      running

```

12. Services, from systemctl list-unit-files

```

STATE UNIT FILES
enabled apparmor appport blk-availability cloud-config cloud-final cloud-init cloud-init-local
console-setup e2scrub_reap finalrd getty@ gpu-manager grub-common grub-initrd-fallback
keyboard-setup lm-sensors lvm2-monitor multipathd networkd-dispatcher open-iscsi pollinate
secureboot-db setvtrgb ssh systemd-networkd systemd-networkd-wait-online systemd-pstore
systemd-resolved systemd-timesyncd thermald
enabled-runtime netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
disabled console-getty debug-shell firewallld iscsid nftables serial-getty@ sysstat
systemd-boot-check-no-failures systemd-confext systemd-network-generator
systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code
systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy
systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext
systemd-time-wait-sync upower
indirect systemd-sysupdate systemd-sysupdate-reboot
masked cryptdisks cryptdisks-early hwclock multipath-tools-boot sudo x11-common

```

13. Linux kernel boot-time arguments, from /proc/cmdline

```

BOOT_IMAGE=/boot/vmlinuz-6.8.0-90-generic
root=UUID=33505bb0-411d-431d-ab81-0138a91fa2d4
ro

```

14. cpupower frequency-info

analyzing CPU 0:

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Platform Notes (Continued)

current policy: frequency should be within 1.50 GHz and 3.60 GHz.
The governor "schedutil" may decide which speed to use within this range.

boost state support:
Supported: yes
Active: yes
Boost States: 0
Total States: 3
Pstate-P0: 3600MHz

```

-----
15. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space      0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                  8
vm.dirty_writeback_centisecs    500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                    1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1
-----

```

```

-----
16. /sys/kernel/mm/transparent_hugepage
defrag          [always] defer defer+madvise madvise never
enabled        [always] madvise never
hpage_pmd_size 2097152
shmem_enabled  always within_size advise [never] deny force
-----

```

```

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs  10000
-----

```

```

-----
18. OS release
From /etc/*-release /etc/*-version
os-release Ubuntu 24.04.3 LTS
-----

```

```

-----
19. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4
Filesystem      Type  Size  Used Avail Use% Mounted on
tmpfs            tmpfs 30G   3.3G   27G  11% /mnt/ramdisk
-----

```

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Platform Notes (Continued)

```

20. /sys/devices/virtual/dmi/id
   Vendor:      Dell Inc.
   Product:     PowerEdge R5715
   Product Family: PowerEdge
   Serial:      SLR5751

```

```

21. dmidecode
   Additional information from dmidecode 3.5 follows.  WARNING: Use caution when you interpret this section.
   The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
   "DMTF SMBIOS" standard.
   Memory:
     12x 802C0000802C MTC40F2046S1RC64BD1 64 GB 2 rank 6400, configured at 5200

```

```

22. BIOS
   (This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:      Dell Inc.
   BIOS Version:     1.6.3
   BIOS Date:        001/06/2026
   BIOS Revision:    1.6

```

Compiler Version Notes

```

=====
C      | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
      | 657.xz_s(base, peak)
=====

```

```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin
=====

```

```

=====
C++   | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
      | 641.leela_s(base, peak)
=====

```

```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin
=====

```

```

=====
Fortran | 648.exchange2_s(base, peak)
=====

```

```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin
=====

```



SPEC CPU®2017 Integer Speed Result

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SPECspeed®2017_int_base = 16.7

PowerEdge R5715 (AMD EPYC 9015 8-Core Processor)

SPECspeed®2017_int_peak = 17.0

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2026

Hardware Availability: Mar-2026

Software Availability: Dec-2025

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Base Portability Flags

```

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

```

Base Optimization Flags

C benchmarks:

```

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -Wl,-mllvm -Wl,-extra-inliner -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP
-flto -fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp -lamdlibm
-lflang -lamdalloc

```

C++ benchmarks:

```

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc-ext

```

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Base Optimization Flags (Continued)

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-iv-split -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -O3 -march=znver5 -fveclib=AMDLIBM
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc
```

Base Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Peak Compiler Invocation

C benchmarks:

```
clang
```

C++ benchmarks:

```
clang++
```

Fortran benchmarks:

```
flang
```

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang
```

602.gcc_s: Same as 600.perlbench_s

```
605.mcf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang
```

625.x264_s: basepeak = yes

657.xz_s: Same as 600.perlbench_s

C++ benchmarks:

```
620.omnetpp_s: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdalloc-ext -lflang
```

```
623.xalancbmk_s: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -mllvm -reduce-array-computations=3
```

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Peak Optimization Flags (Continued)

623.xalancbmk_s (continued):

```
-mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -fopenmp=libomp -lomp
-lamdlibm -lamdalloc-ext -lflang
```

631.deepsjeng_s: -m64 -std=c++14

```
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

Peak Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.xml>



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