



# SPEC CPU®2017 Integer Rate Result

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## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### ProLiant Compute DL320 Gen12

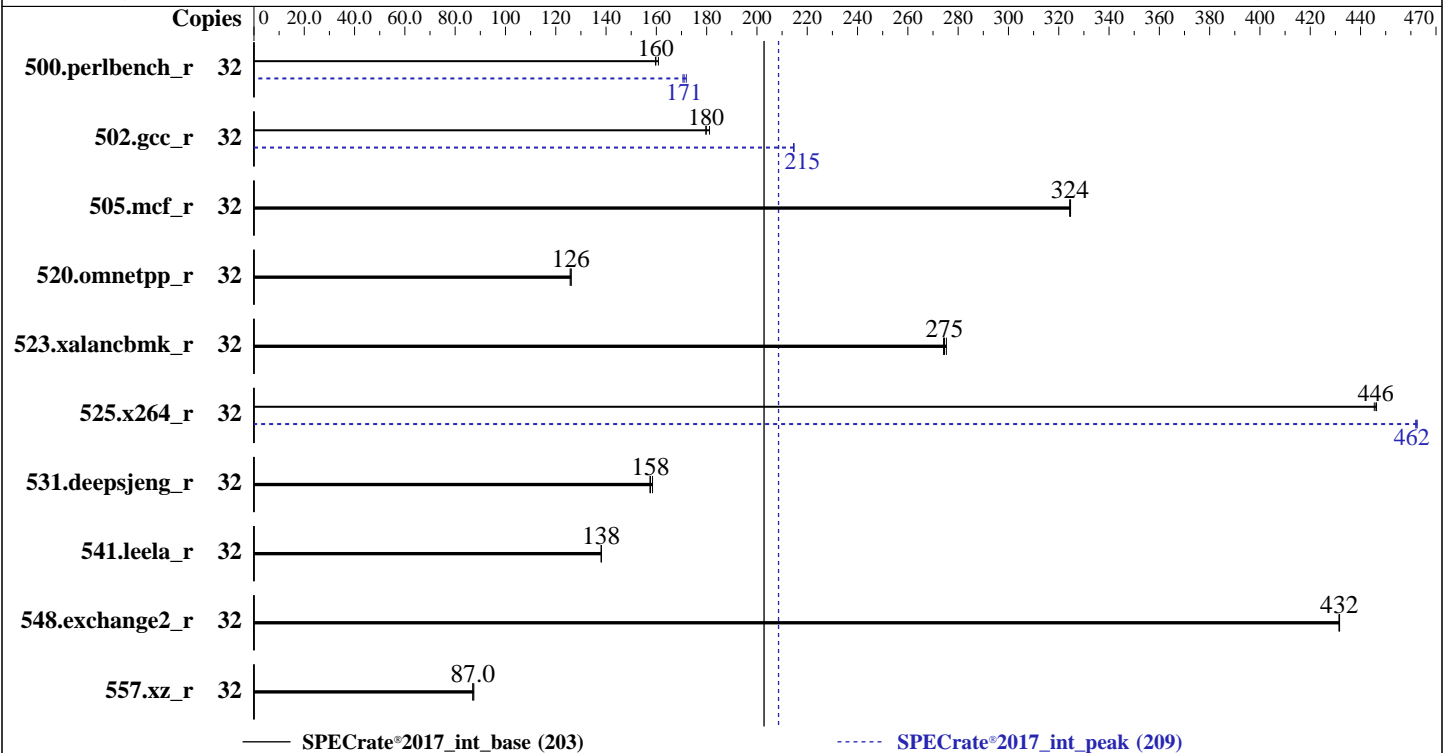
(3.70 GHz, Intel Xeon 6725P)

SPECrate®2017\_int\_base = 203

SPECrate®2017\_int\_peak = 209

CPU2017 License: 3  
Test Sponsor: HPE  
Tested by: HPE

Test Date: Apr-2026  
Hardware Availability: May-2026  
Software Availability: Dec-2025



### Hardware

CPU Name: Intel Xeon 6725P  
 Max MHz: 4800  
 Nominal: 3700  
 Enabled: 16 cores, 1 chip, 2 threads/core  
 Orderable: 1 Chip  
 Cache L1: 64 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 192 MB I+D on chip per chip  
 Other: None  
 Memory: 256 GB (8 x 32 GB 2Rx8 PC5-6400B-R)  
 Storage: 1 x 1.6 TB NVMe SSD  
 Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP7  
 Kernel 6.4.0-150700.53.6-default  
 Compiler: C/C++: Version 2025.2 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2025.2 of Intel Fortran Compiler for Linux;  
 C/C++: Version 2024.2 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Parallel: No  
 Firmware: HPE BIOS Version v1.62 02/06/2026 released Feb-2026  
 File System: btrfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS is set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	32	317	161	319	160	<b>319</b>	<b>160</b>	32	<b>298</b>	<b>171</b>	299	171	296	172
502.gcc_r	32	<b>252</b>	<b>180</b>	250	181	252	180	32	<b>211</b>	<b>215</b>	211	215	211	215
505.mcf_r	32	159	324	<b>159</b>	<b>324</b>	159	325	32	159	324	<b>159</b>	<b>324</b>	159	325
520.omnetpp_r	32	<b>333</b>	<b>126</b>	333	126	334	126	32	<b>333</b>	<b>126</b>	333	126	334	126
523.xalancbmk_r	32	<b>123</b>	<b>275</b>	123	274	123	275	32	<b>123</b>	<b>275</b>	123	274	123	275
525.x264_r	32	126	445	<b>126</b>	<b>446</b>	126	446	32	121	463	<b>121</b>	<b>462</b>	121	462
531.deepsjeng_r	32	231	158	233	157	<b>233</b>	<b>158</b>	32	231	158	233	157	<b>233</b>	<b>158</b>
541.leela_r	32	<b>384</b>	<b>138</b>	384	138	384	138	32	<b>384</b>	<b>138</b>	384	138	384	138
548.exchange2_r	32	194	431	194	432	<b>194</b>	<b>432</b>	32	194	431	194	432	<b>194</b>	<b>432</b>
557.xz_r	32	397	87.0	395	87.4	<b>397</b>	<b>87.0</b>	32	397	87.0	395	87.4	<b>397</b>	<b>87.0</b>

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
tuned-adm profile was set to throughput-performance using 'tuned-adm profile throughput-performance'

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

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## General Notes (Continued)

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS Configuration : Parameters are selected in the order shown below  
Workload Profile set to General Throughput Compute  
Thermal Configuration set to Maximum Cooling  
Enhanced Processor Performance Profile set to Aggressive  
Memory Patrol Scrubbing set to Disabled  
Minimum Processor Idle Power Core C-State set to C6 as ACPI C3 State  
Workload Profile set to Custom  
Minimum Processor Idle Power Package C-State set to No Package State

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on hqmorphpub1 Fri Apr 10 07:07:35 2026

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.27+suse.167.g130293e510)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

-----  
1. uname -a  
Linux hqmorphpub1 6.4.0-150700.53.6-default #1 SMP PREEMPT\_DYNAMIC Tue Jul 1 14:54:47 UTC 2025 (8ab7501)  
x86\_64 x86\_64 x86\_64 GNU/Linux  
-----

2. w  
07:07:35 up 0 min, 3 users, load average: 0.33, 0.10, 0.04  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
-----

3. Username  
-----

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## Platform Notes (Continued)

From environment variable \$USER: root

```

-----
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size          (kbytes, -d) unlimited
scheduling priority    (-e) 0
file size              (blocks, -f) unlimited
pending signals        (-i) 1029905
max locked memory      (kbytes, -l) 8192
max memory size        (kbytes, -m) unlimited
open files             (-n) 1024
pipe size              (512 bytes, -p) 8
POSIX message queues   (bytes, -q) 819200
real-time priority     (-r) 0
stack size             (kbytes, -s) unlimited
cpu time               (seconds, -t) unlimited
max user processes     (-u) 1029905
virtual memory         (kbytes, -v) unlimited
file locks             (-x) unlimited

```

```

-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
bash -c cd $SPEC/ && $SPEC/reportable-ic2025.2-lin-graniterapids-rate-smt-on-20250605.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 -c
ic2025.2-lin-graniterapids-rate-20250605.cfg --define smt-on --define cores=16 --define physicalfirst
--define no-numa --define intrate-peak --tune base,peak -o all --define drop_caches intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 --configfile
ic2025.2-lin-graniterapids-rate-20250605.cfg --define smt-on --define cores=16 --define physicalfirst
--define no-numa --define intrate-peak --tune base,peak --output_format all --define drop_caches --nopower
--runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

```

```

-----
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) 6725P
vendor_id      : GenuineIntel
cpu family     : 6
model          : 173
stepping      : 1
microcode     : 0x1000411
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores     : 16
siblings      : 32
1 physical ids (chips)
32 processors (hardware threads)
physical id 0: core ids 0-15
physical id 0: apicids 0-31
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

```

```

-----
7. lscpu

```

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## Platform Notes (Continued)

From lscpu from util-linux 2.40.4:

```

Architecture:                x86_64
CPU op-mode(s):              32-bit, 64-bit
Address sizes:                46 bits physical, 57 bits virtual
Byte Order:                  Little Endian
CPU(s):                       32
On-line CPU(s) list:         0-31
Vendor ID:                   GenuineIntel
Model name:                  Intel(R) Xeon(R) 6725P
CPU family:                  6
Model:                       173
Thread(s) per core:         2
Core(s) per socket:         16
Socket(s):                   1
Stepping:                    1
BogoMIPS:                    7400.00
Flags:                       fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                             pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
                             pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
                             nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
                             pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma
                             cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
                             tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                             3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin
                             cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                             flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep
                             bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                             avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw
                             avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc
                             cqm_mbm_total cqm_mbm_local split_lock_detect user_shstk avx_vnni
                             avx512_bf16 wbnoinvd dtherm ida arat pln pts hfi vnni avx512vbmi
                             umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                             avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid
                             bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear
                             serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16
                             amx_tile amx_int8 flush_lld arch_capabilities

Virtualization:              VT-x
L1d cache:                   768 KiB (16 instances)
L1i cache:                   1 MiB (16 instances)
L2 cache:                    32 MiB (16 instances)
L3 cache:                    192 MiB (1 instance)
NUMA node(s):                1
NUMA node0 CPU(s):          0-31
Vulnerability Gather data sampling: Not affected
Vulnerability Indirect target selection: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:         Not affected
Vulnerability Mds:          Not affected
Vulnerability Meltdown:    Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed:    Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:  Mitigation; usercopy/swapgs barriers and __user pointer
                             sanitization
Vulnerability Spectre v2:  Mitigation; Enhanced / Automatic IBRS; IBPB conditional;
                             PBRSE-eIBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds:       Not affected
Vulnerability Tsx async abort: Not affected

```

(Continued on next page)



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## Platform Notes (Continued)

From `lscpu --cache:`

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	768K	12	Data	1	64	1	64
L1i	64K	1M	16	Instruction	1	64	1	64
L2	2M	32M	16	Unified	2	2048	1	64
L3	192M	192M	16	Unified	3	196608	1	64

8. `numactl --hardware`

NOTE: a `numactl 'node'` might or might not correspond to a physical chip.

```

available: 1 nodes (0)
node 0 cpus: 0-31
node 0 size: 257509 MB
node 0 free: 256580 MB
node distances:
node 0
0: 10

```

9. `/proc/meminfo`

MemTotal: 263689632 kB

10. `who -r`

run-level 3 Apr 10 07:07

11. Systemd service manager version: `systemd 254 (254.27+suse.167.g130293e510)`

```

Default Target Status
multi-user      running

```

12. Services, from `systemctl list-unit-files`

```

STATE UNIT FILES
enabled ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd
bluetooth cron display-manager getty@ irqbalance issue-generator kbdsettings klog
lvm2-monitor nsd nvme-fc-boot-connections nvme-autoconnect postfix purge-kernels rollback
rsyslog smartd sshd systemd-pstore tuned wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6
wickedd-nanny wpa_supplicant
enabled-runtime systemd-remount-fs
disabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon autofsd
autoyast-initcripts blk-availability bluetooth-mesh boot-sysctl ca-certificates
chrony-wait chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables
exchange-bmc-os-info firewalld fsidd gnome-remote-desktop gpm grub2-once haveged
hwloc-dump-hwdata ipmi ipmievd issue-add-ssh-keys kexec-load lunmask man-db-create
multipathd nfs nfs-blkmap nmb openvpn@ ostree-remount ostree-state-overlay@ rpcbind
rpmconfigcheck rsyncd rtkit-daemon samba-bgqd serial-getty@ smartd_generate_opts smb snmpd
snmptrapd speech-dispatcherd systemd-boot-check-no-failures systemd-confext
systemd-network-generator systemd-sysextd systemd-time-wait-sync systemd-timesyncd udisks2
update-system-flatpaks upower vncserver@ wpa_supplicant@
indirect pcsd saned@ systemd-userdbd wickedd

```

13. Linux kernel boot-time arguments, from `/proc/cmdline`

```

BOOT_IMAGE=/boot/vmlinuz-6.4.0-150700.53.6-default
root=UUID=9c4fd0fb-bb42-489c-bd93-1c9f83bb28af
splash=silent
mitigations=auto
quiet

```

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## Platform Notes (Continued)

security=apparmor

-----  
14. cpupower frequency-info

analyzing CPU 9:

Unable to determine current policy

boost state support:

Supported: yes

Active: yes

-----  
15. tuned-adm active

Current active profile: throughput-performance

-----  
16. sysctl

kernel.numa_balancing	0
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	40
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	10
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	0

-----  
17. /sys/kernel/mm/transparent\_hugepage

defrag	always	defer	defer+madvise	[madvise]	never
enabled	[always]	madvise	never		
hpage_pmd_size	2097152				
shmem_enabled	always	within_size	advise	[never]	deny force

-----  
18. /sys/kernel/mm/transparent\_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

-----  
19. OS release

From /etc/\*-release /etc/\*-version  
os-release SUSE Linux Enterprise Server 15 SP7

-----  
20. Disk information

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## Platform Notes (Continued)

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/nvme0n1p2	btrfs	1.5T	93G	1.4T	7%	/home

21. /sys/devices/virtual/dmi/id

```
Vendor:      HPE
Product:    HPE ProLiant Compute DL320 Gen12
Product Family: ProLiant
Serial:     S84PQDRU0T
```

22. dmidecode

Additional information from dmidecode 3.6 follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
8x Hynix HMC88AHBRA471N 32 GB 2 rank 6400

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

```
BIOS Vendor:      HPE
BIOS Version:     1.62
BIOS Date:        02/06/2026
BIOS Revision:    1.62
Firmware Revision: 1.19
```

## Compiler Version Notes

C | 502.gcc\_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.2.1 Build 20240711  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2025.2.0 Build 20250605  
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.

C | 502.gcc\_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.2.1 Build 20240711  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2025.2.0 Build 20250605

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Copyright (C) 1985-2025 Intel Corporation. All rights reserved.

=====  
C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base, peak) 531.deepsjeng\_r(base, peak)  
| 541.leela\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2025.2.0 Build 20250605  
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.

=====  
Fortran | 548.exchange2\_r(base, peak)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2025.2.0 Build 20250605  
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant Compute DL320 Gen12**

(3.70 GHz, Intel Xeon 6725P)

**SPECrate®2017\_int\_base = 203**

**SPECrate®2017\_int\_peak = 209**

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Apr-2026

**Hardware Availability:** May-2026

**Software Availability:** Dec-2025

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xgraniterapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/intel-compilers/compiler/2025.2/lib -lqkmallo
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xgraniterapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-fdelayed-template-parsing
-L/home/specdev/intel-compilers/compiler/2025.2/lib -lqkmallo
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xgraniterapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/home/specdev/intel-compilers/compiler/2025.2/lib -lqkmallo
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icx

502.gcc\_r: icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64

502.gcc\_r: -D\_FILE\_OFFSET\_BITS=64

505.mcf\_r: -DSPEC\_LP64

520.omnetpp\_r: -DSPEC\_LP64

523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX

525.x264\_r: -DSPEC\_LP64

531.deepsjeng\_r: -DSPEC\_LP64

541.leela\_r: -DSPEC\_LP64

548.exchange2\_r: -DSPEC\_LP64

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## Peak Portability Flags (Continued)

557.xz\_r: -DSPEC\_LP64

## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow -fno-strict-aliasing
-L/home/specdev/intel-compilers/compiler/2025.2/lib
-lqkmalloc
```

```
502.gcc_r: -m32 -L/home/specdev/intel-compilers/compiler/2024.2/lib32
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc
```

505.mcf\_r: basepeak = yes

```
525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xgraniterapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/home/specdev/intel-compilers/compiler/2025.2/lib
-lqkmalloc
```

557.xz\_r: basepeak = yes

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

(Continued on next page)



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**Test Date:** Apr-2026

**Hardware Availability:** May-2026

**Software Availability:** Dec-2025

## Peak Optimization Flags (Continued)

548.exchange2\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-GNR-rev1.6.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2025-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-GNR-rev1.6.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2025-official-linux64.xml>

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