



SPEC CPU®2017 Floating Point Rate Result

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New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 2530

H3C UniServer R4950 G7 (AMD EPYC 9965)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9066

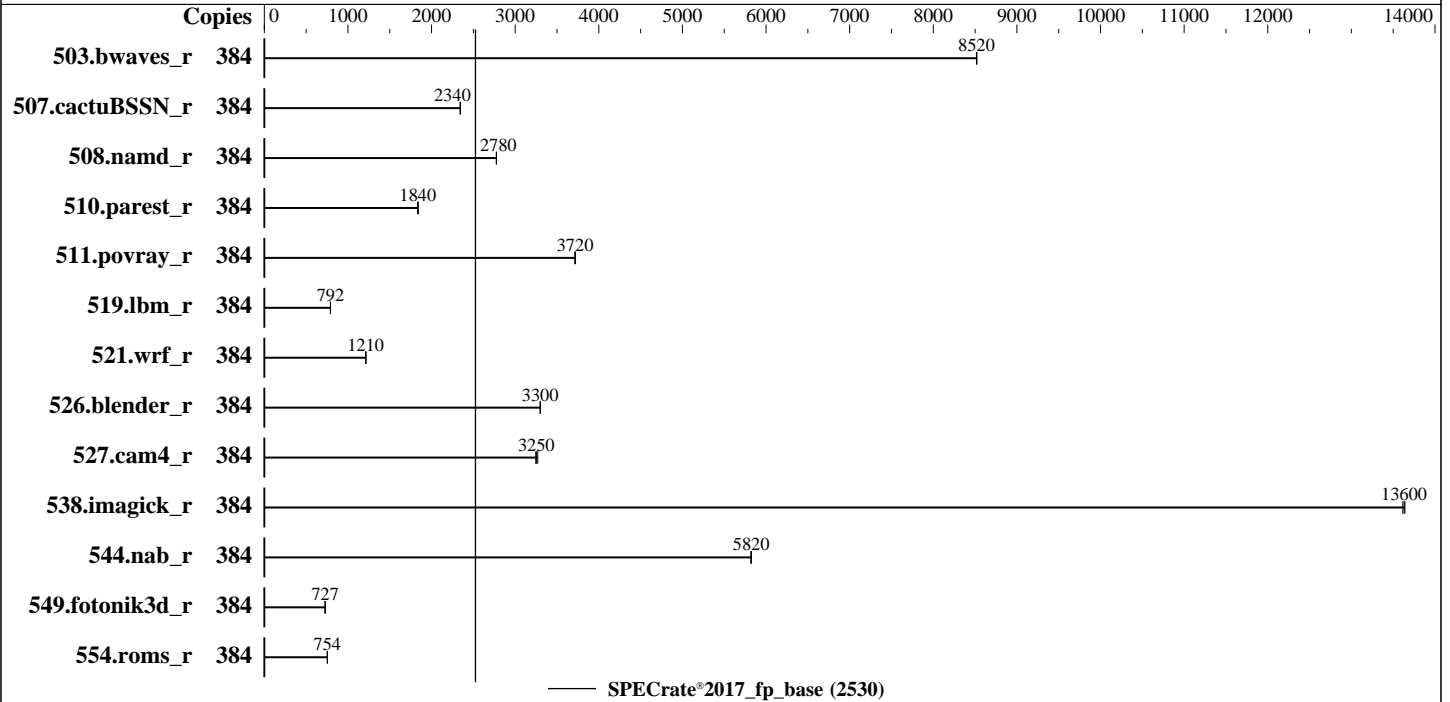
Test Date: Apr-2026

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Oct-2024

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Apr-2025



Hardware

CPU Name: AMD EPYC 9965
 Max MHz: 3700
 Nominal: 2250
 Enabled: 384 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 384 MB I+D on chip per chip, 32 MB shared / 16 cores
 Other: None
 Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R)
 Storage: 1 x 7.68 TB NVME SSD
 Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP7 kernel version 6.4.0-150700.51-default
 Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
 Parallel: No
 Firmware: Version 7.30.19 released Feb-2026
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other: None
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
503.bwaves_r	384	452	8520	<u>452</u>	<u>8520</u>	452	8520									
507.cactuBSSN_r	384	208	2340	<u>207</u>	<u>2340</u>	207	2340									
508.namd_r	384	131	2780	<u>131</u>	<u>2780</u>	131	2780									
510.parest_r	384	547	1840	<u>547</u>	<u>1840</u>	546	1840									
511.povray_r	384	241	3720	<u>241</u>	<u>3720</u>	242	3710									
519.lbm_r	384	512	790	511	792	<u>511</u>	<u>792</u>									
521.wrf_r	384	<u>709</u>	<u>1210</u>	708	1210	709	1210									
526.blender_r	384	177	3300	<u>177</u>	<u>3300</u>	177	3300									
527.cam4_r	384	205	3270	207	3250	<u>206</u>	<u>3250</u>									
538.imagick_r	384	<u>70.1</u>	<u>13600</u>	70.0	13600	70.1	13600									
544.nab_r	384	111	5820	<u>111</u>	<u>5820</u>	111	5820									
549.fotonik3d_r	384	2058	727	<u>2057</u>	<u>727</u>	2057	727									
554.roms_r	384	810	753	808	755	<u>810</u>	<u>754</u>									

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and

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Operating System Notes (Continued)

'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH =

"/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib32:"

MALLOC_CONF = "retain:true"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS settings:

SVM Mode = Disabled

Power Profile Selection = High Performance Mode

Determinism Slider = Power

cTDP = 500

PPT = 500

NUMA nodes per socket = NPS 4

APBDIS = 1

Global C-states Control = Disabled

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost Fri Apr 10 00:42:15 2026

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info

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Platform Notes (Continued)

- 15. sysctl
- 16. /sys/kernel/mm/transparent_hugepage
- 17. /sys/kernel/mm/transparent_hugepage/khugepaged
- 18. OS release
- 19. Disk information
- 20. /sys/devices/virtual/dmi/id
- 21. dmidecode
- 22. BIOS

```
1. uname -a
Linux localhost 6.4.0-150700.51-default #1 SMP PREEMPT_DYNAMIC Wed Apr 30 21:35:43 UTC 2025 (6930611)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
00:42:15 up 9:39, 2 users, load average: 0.32, 0.70, 76.29
USER      TTY      FROM          LOGIN@      IDLE        JCPU        PCPU WHAT
root      tty1     -             15:03       31.00s     1.21s      0.14s /bin/bash ./amd_rate_aocc500_znver5_A1.sh
root      pts/0   172.17.74.10  16:10       8:24m     0.04s      0.04s -bash
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 6186166
max locked memory       (kbytes, -l) 2097152
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 6186166
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=31
login -- root
-bash
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune base --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune base --reportable --iterations 3 --nopower
--runmode rate --tune base --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.020/temlogs/preenv.fprate.020.0.log --lognum 020.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

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Platform Notes (Continued)

```

6. /proc/cpuinfo
model name      : AMD EPYC 9965 192-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 26
model          : 17
stepping       : 0
microcode      : 0xb101054
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass srso
TLB size      : 192 4K pages
cpu cores     : 192
siblings      : 384
2 physical ids (chips)
768 processors (hardware threads)
physical id 0: core ids 0-191
physical id 1: core ids 0-191
physical id 0: apicids 0-383
physical id 1: apicids 512-895

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.40.4:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                768
On-line CPU(s) list:  0-767
Vendor ID:             AuthenticAMD
Model name:            AMD EPYC 9965 192-Core Processor
CPU family:            26
Model:                 17
Thread(s) per core:   2
Core(s) per socket:   192
Socket(s):              2
Stepping:              0
Frequency boost:       enabled
CPU(s) scaling MHz:    101%
CPU max MHz:           2250.0000
CPU min MHz:           1500.0000
BogoMIPS:              4489.65
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
rdtsmp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx fl6c rdrand lahf_lm
cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
xsaveerptr rdpru wbnoinvd amd_ppin cppc amd_ibpb_ret arat npt lbrv
svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl
vnm1 avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq

```

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Platform Notes (Continued)

```

avx512_vnni avx512_bitalg avx512_vpoptdq la57 rdpid bus_lock_detect
movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect
flush_llid debug_swap hv_inuse_wr_allowed srso_user_kernel_no
amd_lbr_pmc_freeze
L1d cache: 18 MiB (384 instances)
L1i cache: 12 MiB (384 instances)
L2 cache: 384 MiB (384 instances)
L3 cache: 768 MiB (24 instances)
NUMA node(s): 8
NUMA node0 CPU(s): 0-47,384-431
NUMA node1 CPU(s): 48-95,432-479
NUMA node2 CPU(s): 96-143,480-527
NUMA node3 CPU(s): 144-191,528-575
NUMA node4 CPU(s): 192-239,576-623
NUMA node5 CPU(s): 240-287,624-671
NUMA node6 CPU(s): 288-335,672-719
NUMA node7 CPU(s): 336-383,720-767
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Mitigation; IBPB on VMEXIT only
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
always-on; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	18M	12	Data	1	64	1	64
L1i	32K	12M	8	Instruction	1	64	1	64
L2	1M	384M	16	Unified	2	1024	1	64
L3	32M	768M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0-47,384-431
node 0 size: 192658 MB
node 0 free: 191484 MB
node 1 cpus: 48-95,432-479
node 1 size: 193472 MB
node 1 free: 192482 MB
node 2 cpus: 96-143,480-527
node 2 size: 193510 MB
node 2 free: 192428 MB
node 3 cpus: 144-191,528-575
node 3 size: 193510 MB
node 3 free: 192303 MB
node 4 cpus: 192-239,576-623
node 4 size: 193510 MB
node 4 free: 192426 MB
node 5 cpus: 240-287,624-671

```

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Platform Notes (Continued)

```

node 5 size: 193510 MB
node 5 free: 192462 MB
node 6 cpus: 288-335,672-719
node 6 size: 193510 MB
node 6 free: 191321 MB
node 7 cpus: 336-383,720-767
node 7 size: 192884 MB
node 7 free: 191907 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10  12  12  12  32  32  32  32
1:  12  10  12  12  32  32  32  32
2:  12  12  10  12  32  32  32  32
3:  12  12  12  10  32  32  32  32
4:  32  32  32  32  10  12  12  12
5:  32  32  32  32  12  10  12  12
6:  32  32  32  32  12  12  10  12
7:  32  32  32  32  12  12  12  10

```

```

-----
9. /proc/meminfo
MemTotal:      1583685048 kB

```

```

-----
10. who -r
run-level 3 Apr 9 15:03

```

```

-----
11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)
Default Target  Status
multi-user      running

```

```

-----
12. Services, from systemctl list-unit-files
STATE          UNIT FILES
enabled        apparmor auditd cron getty@ irqbalance issue-generator kbdsettings kdump kdump-early
                kdump-notify nvme-fc-boot-connections nvmmf-autoconnect postfix purge-kernels rollback sshd
                systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs
disabled       boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed
                debug-shell ebtables firewalld fsidd grub2-once haveged hwloc-dump-hwdata
                issue-add-ssh-keys kexec-load lunmask nfs nfs-blkmap rpcbind rpmconfigcheck serial-getty@
                systemd-boot-check-no-failures systemd-conftext systemd-network-generator systemd-sysext
                systemd-time-wait-sync systemd-timesyncd
indirect       pcsd systemd-userdbd wickedd

```

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150700.51-default
root=UUID=4953839d-11e1-46da-9a81-d08855946185
splash=silent
resume=/dev/disk/by-uuid/dda201e9-fc6d-4e40-8516-a7f4746f0e49
mitigations=auto
quiet
security=apparmor
crashkernel=379M,high
crashkernel=72M,low

```

```

-----
14. cpupower frequency-info

```

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Platform Notes (Continued)

analyzing CPU 152:

current policy: frequency should be within 1.50 GHz and 2.25 GHz.
The governor "performance" may decide which speed to use within this range.

boost state support:

Supported: yes

Active: yes

```
-----
15. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space      0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                  8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1
-----
```

```
-----
16. /sys/kernel/mm/transparent_hugepage
defrag          [always] defer+madvise madvise never
enabled         [always] madvise never
hpage_pmd_size 2097152
shmem_enabled   always within_size advise [never] deny force
-----
```

```
-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
-----
```

```
-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP7
-----
```

```
-----
19. Disk information
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p4 xfs 482G 33G 450G 7% /home
-----
```

```
-----
20. /sys/devices/virtual/dmi/id
-----
```

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Platform Notes (Continued)

Vendor: New H3C Technologies Co., Ltd.
Product: H3C UniServer R4950 G7
Product Family: AMD Turin Server
Serial: 210235A55CH251000004

21. dmidecode

Additional information from dmidecode 3.6 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

13x SK Hynix HMC94AHBRA480N 64 GB 2 rank 6400
1x SK Hynix HMC94AHBRA481N 64 GB 2 rank 6400
1x SK Hynix HMC94AHBRA485N 64 GB 2 rank 6400
9x SK Hynix HMC94AHBRA487N 64 GB 2 rank 6400

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 7.30.19
BIOS Date: 02/10/2026
BIOS Revision: 5.35
Firmware Revision: 2.13

Compiler Version Notes

C | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C++ | 508.namd_r(base) 510.parest_r(base)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C++, C | 511.povray_r(base) 526.blender_r(base)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

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SPECrate®2017_fp_base = 2530

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SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Apr-2026
Hardware Availability: Oct-2024
Software Availability: Apr-2025

Compiler Version Notes (Continued)

=====
C++, C, Fortran | 507.cactuBSSN_r(base)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
Fortran | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
Fortran, C | 521.wrf_r(base) 527.cam4_r(base)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

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Base Compiler Invocation (Continued)

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
 507.cactuBSSN_r: -DSPEC_LP64
 508.namd_r: -DSPEC_LP64
 510.parest_r: -DSPEC_LP64
 511.povray_r: -DSPEC_LP64
 519.lbm_r: -DSPEC_LP64
 521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
 526.blender_r: -funsigned-char -DSPEC_LP64
 527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
 538.imagick_r: -DSPEC_LP64
 544.nab_r: -DSPEC_LP64
 549.fotonik3d_r: -DSPEC_LP64
 554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
 -Wl,-mllvm -Wl,-reduce-array-computations=3
 -Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
 -fstruct-layout=7 -mllvm -unroll-threshold=50
 -mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
 -mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
 -lflang -ldl

C++ benchmarks:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
 -Wl,-mllvm -Wl,-reduce-array-computations=3
 -Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
 -O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
 -mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
 -mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
 -lflang -ldl

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Base Optimization Flags (Continued)

Fortran benchmarks:

```

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang -ldl

```

Benchmarks using both Fortran and C:

```

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl

```

Benchmarks using both C and C++:

```

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
-ldl

```

Benchmarks using Fortran, C, and C++:

```

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops

```

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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl

Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2025-11-05.html>

http://www.spec.org/cpu2017/flags/New_H3C-Platform-AMD-Settings-V2.0-Turin.html

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2025-11-05.xml>

http://www.spec.org/cpu2017/flags/New_H3C-Platform-AMD-Settings-V2.0-Turin.xml

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