



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

NEC Corporation

SPECspeed®2017_int_base = 15.3

Express5800/T110m (Intel Xeon 6325P)

SPECspeed®2017_int_peak = 15.7

CPU2017 License: 9006

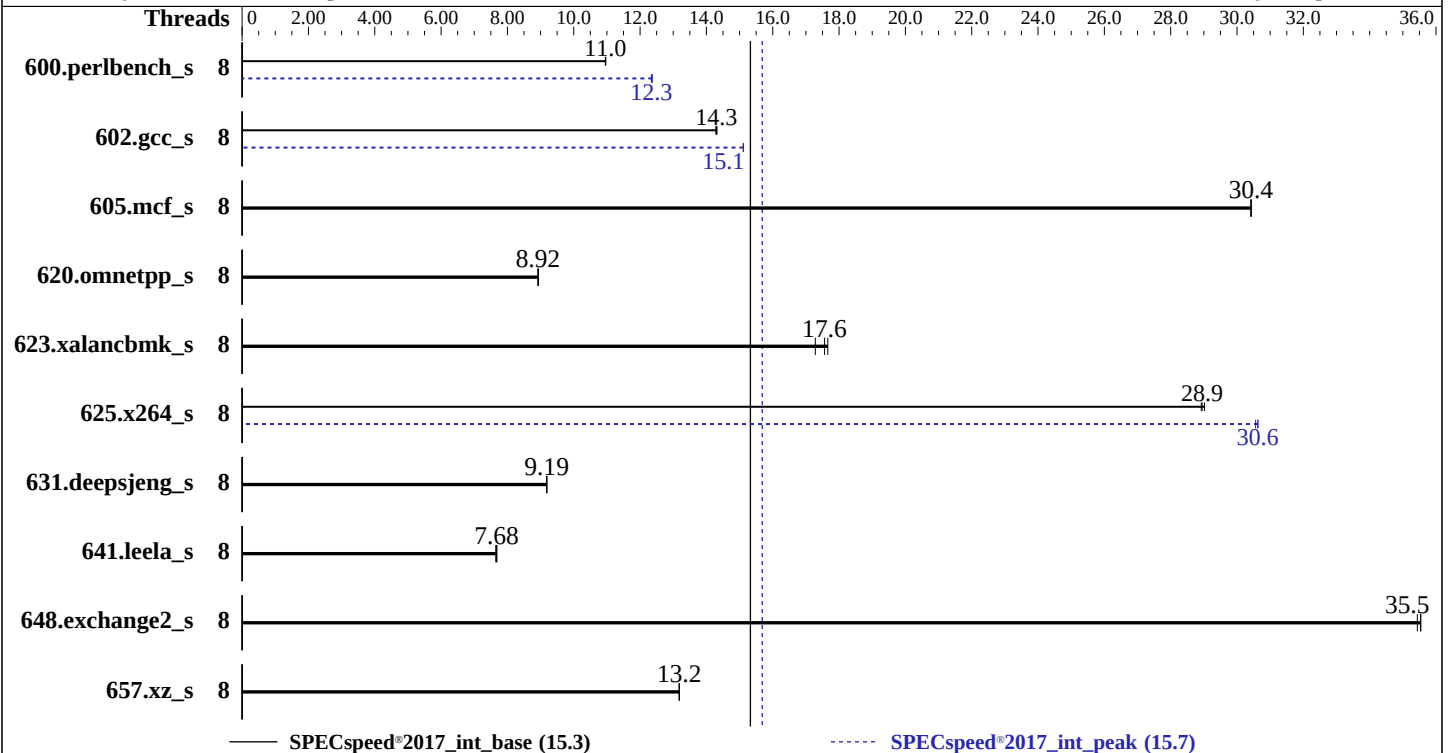
Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Dec-2025

Hardware Availability: Jun-2025

Software Availability: Apr-2024



Hardware

CPU Name: Intel Xeon 6325P
Max MHz: 5200
Nominal: 3500
Enabled: 4 cores, 1 chip, 2 threads/core
Orderable: 1 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 12 MB I+D on chip per chip
Other: None
Memory: 64 GB (2 x 32 GB 2Rx8 PC5-4800B-E, running at 4400)
Storage: 1 x 1 TB SATA, 7200 RPM
Other: CPU Cooling: Air

Software

OS: Red Hat Enterprise Linux 9.4 (Plow)
5.14.0-427.13.1.el9_4.x86_64
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel: Yes
Firmware: NEC BIOS Version F52 15.52 03/04/2025 released Mar-2025
File System: ext4
System State: Run level 5 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of additional power usage
intel_pstate is powersave



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

NEC Corporation

SPECspeed®2017_int_base = 15.3

Express5800/T110m (Intel Xeon 6325P)

SPECspeed®2017_int_peak = 15.7

CPU2017 License: 9006

Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Dec-2025

Hardware Availability: Jun-2025

Software Availability: Apr-2024

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	8	162	11.0	<u>162</u>	<u>11.0</u>	162	11.0	8	144	12.4	<u>144</u>	<u>12.3</u>	144	12.3
602.gcc_s	8	279	14.3	<u>278</u>	<u>14.3</u>	278	14.3	8	263	15.1	263	15.1	<u>263</u>	<u>15.1</u>
605.mcf_s	8	155	30.4	<u>155</u>	<u>30.4</u>	155	30.4	8	155	30.4	<u>155</u>	<u>30.4</u>	155	30.4
620.omnetpp_s	8	183	8.92	183	8.93	<u>183</u>	<u>8.92</u>	8	183	8.92	183	8.93	<u>183</u>	<u>8.92</u>
623.xalanchmk_s	8	80.2	17.7	<u>80.7</u>	<u>17.6</u>	82.0	17.3	8	80.2	17.7	<u>80.7</u>	<u>17.6</u>	82.0	17.3
625.x264_s	8	60.8	29.0	<u>60.9</u>	<u>28.9</u>	61.0	28.9	8	57.7	30.6	57.6	30.6	<u>57.6</u>	<u>30.6</u>
631.deepsjeng_s	8	<u>156</u>	<u>9.19</u>	156	9.19	156	9.18	8	<u>156</u>	<u>9.19</u>	156	9.19	156	9.18
641.leela_s	8	<u>222</u>	<u>7.68</u>	222	7.68	223	7.65	8	<u>222</u>	<u>7.68</u>	222	7.68	223	7.65
648.exchange2_s	8	82.7	35.5	83.0	35.4	<u>82.7</u>	<u>35.5</u>	8	82.7	35.5	83.0	35.4	<u>82.7</u>	<u>35.5</u>
657.xz_s	8	<u>469</u>	<u>13.2</u>	469	13.2	469	13.2	8	<u>469</u>	<u>13.2</u>	469	13.2	469	13.2

SPECspeed®2017_int_base = 15.3

SPECspeed®2017_int_peak = 15.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
'systemctl stop tuned' was used to disable the tuned daemon.

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon 6369P CPU + 64GB RAM
memory using Redhat Enterprise Linux 9.4
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

NEC Corporation

SPECspeed®2017_int_base = 15.3

Express5800/T110m (Intel Xeon 6325P)

SPECspeed®2017_int_peak = 15.7

CPU2017 License: 9006

Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Dec-2025

Hardware Availability: Jun-2025

Software Availability: Apr-2024

Platform Notes

BIOS Settings:

Intel(R) SpeedStep(tm): Disabled
Energy Efficient Turbo: Disabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on T110m-22.com Thu Dec 4 07:32:27 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-32.el9_4)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. tuned-adm active
17. sysctl
18. /sys/kernel/mm/transparent_hugepage
19. /sys/kernel/mm/transparent_hugepage/khugepaged
20. OS release
21. Disk information
22. /sys/devices/virtual/dmi/id
23. dmidecode
24. BIOS

1. uname -a
Linux T110m-22.com 5.14.0-427.13.1.el9_4.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 10 10:29:16 EDT 2024 x86_64
x86_64 x86_64 GNU/Linux

2. w
07:32:27 up 1 day, 6:28, 2 users, load average: 0.07, 0.02, 0.00
USER TTY LOGIN@ IDLE JCPU PCPU WHAT
root seat0 Wed01 0.00s 0.00s 0.00s /usr/libexec/gdm-wayland-session --register-session
gnome-session
root tty2 Wed01 30:28m 0.00s 0.00s /usr/libexec/gnome-session-binary

3. Username
From environment variable \$USER: root

4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

NEC Corporation

SPECspeed®2017_int_base = 15.3

Express5800/T110m (Intel Xeon 6325P)

SPECspeed®2017_int_peak = 15.7

CPU2017 License: 9006

Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Dec-2025

Hardware Availability: Jun-2025

Software Availability: Apr-2024

Platform Notes (Continued)

data seg size	(kbytes, -d) unlimited
scheduling priority	(-e) 0
file size	(blocks, -f) unlimited
pending signals	(-i) 256448
max locked memory	(kbytes, -l) 8192
max memory size	(kbytes, -m) unlimited
open files	(-n) 1024
pipe size	(512 bytes, -p) 8
POSIX message queues	(bytes, -q) 819200
real-time priority	(-r) 0
stack size	(kbytes, -s) unlimited
cpu time	(seconds, -t) unlimited
max user processes	(-u) 256448
virtual memory	(kbytes, -v) unlimited
file locks	(-x) unlimited

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31
/usr/lib/systemd/systemd --user
/usr/libexec/gnome-terminal-server
bash
bash
runcpu --nobuild --action validate --flagsurl=Intel-ic2024-official-linux64.xml,Default-Platform-Flags.xml
-c ic2024.1-lin-core-avx2-speed-20240308n.cfg --define cores=4 --tune base,peak -o all --define
intspeedaffinity --define smt-on --define drop_caches --nopower --runmode speed --tune base:peak --size
refspeed intspeer
runcpu --nobuild --action validate --flagsurl Intel-ic2024-official-linux64.xml,Default-Platform-Flags.xml
--configfile ic2024.1-lin-core-avx2-speed-20240308n.cfg --define cores=4 --tune base,peak --output_format
all --define intspeeraffinity --define smt-on --define drop_caches --nopower --runmode speed --tune
base:peak --size refspeer --nopower --runmode speed --tune base:peak --size refspeer intspeer --nopreenv
--note-preenv --logfile $SPEC/tmp/CPU2017.030/templogs/preenv.intspeer.030.0.log --lognum 030.0
--from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) 6325P
vendor_id       : GenuineIntel
cpu family      : 6
model           : 183
stepping        : 1
microcode       : 0x12e
bugs             : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores       : 4
siblings        : 8
1 physical ids (chips)
8 processors (hardware threads)
physical id 0: core ids 0-3
physical id 0: apicids 0-7
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

7. lscpu

```
From lscpu from util-linux 2.37.4:
Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

NEC Corporation

SPECspeed®2017_int_base = 15.3

Express5800/T110m (Intel Xeon 6325P)

SPECspeed®2017_int_peak = 15.7

CPU2017 License: 9006

Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Dec-2025

Hardware Availability: Jun-2025

Software Availability: Apr-2024

Platform Notes (Continued)

Address sizes: 42 bits physical, 48 bits virtual
Byte Order: Little Endian
CPU(s): 8
On-line CPU(s) list: 0-7
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
Model name: Intel(R) Xeon(R) 6325P
BIOS Model name: Intel(R) Xeon(R) 6325P
CPU family: 6
Model: 183
Thread(s) per core: 2
Core(s) per socket: 4
Socket(s): 1
Stepping: 1
Frequency boost: enabled
CPU(s) scaling MHz: 100%
CPU max MHz: 3501.0000
CPU min MHz: 800.0000
BogoMIPS: 6988.80
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb ssbd ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed adx smap clflushopt clwb intel_pt sha_ni xsaveopt xsavec xgetbv1 xsaves split_lock_detect avx_vnni dtherm ida arat pln pts hfi vnmi umip pku ospke waitpkg gfni vaes vpclmulqdq tme rdpid movdiri movdir64b fsrm md_clear serialize pconfig arch_lbr ibt flush_lld arch_capabilities
Virtualization: VT-x
L1d cache: 192 KiB (4 instances)
L1i cache: 128 KiB (4 instances)
L2 cache: 8 MiB (4 instances)
L3 cache: 12 MiB (1 instance)
NUMA node(s): 1
NUMA node0 CPU(s): 0-7
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS SW sequence
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	192K	12	Data	1	64	1	64
L1i	32K	128K	8	Instruction	1	64	1	64
L2	2M	8M	16	Unified	2	2048	1	64
L3	12M	12M	6	Unified	3	32768	1	64

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

NEC Corporation

SPECspeed®2017_int_base = 15.3

Express5800/T110m (Intel Xeon 6325P)

SPECspeed®2017_int_peak = 15.7

CPU2017 License: 9006

Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Dec-2025

Hardware Availability: Jun-2025

Software Availability: Apr-2024

Platform Notes (Continued)

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 1 nodes (0)
node 0 cpus: 0-7
node 0 size: 64170 MB
node 0 free: 62662 MB
node distances:
node   0
 0:   10
```

9. /proc/meminfo

MemTotal: 65710972 kB

10. who -r

run-level 5 Dec 3 01:05

11. Systemd service manager version: systemd 252 (252-32.el9_4)

Default Target Status
graphical degraded

12. Failed units, from systemctl list-units --state=failed

UNIT	LOAD	ACTIVE	SUB	DESCRIPTION
* NetworkManager-wait-online.service	loaded	failed	failed	Network Manager Wait Online
* sep5.service	loaded	failed	failed	systemd script to load sep5 driver at boot time

13. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon atd auditd avahi-daemon bluetooth chronyd crond cups dbus-broker firewalld gdm getty@ insights-client-boot irqbalance iscsi-onboot iscsi-starter kdump libstoragemgmt low-memory-monitor lvm2-monitor mcelog mdmonitor microcode multipathd nis-domainname nvme-fc-boot-connections ostree-remount power-profiles-daemon qemu-guest-agent rhsmcertd rsyslog rtkit-daemon selinux-autorelabel-mark sep5 smartd sshd sssd switcheroo-control systemd-boot-update systemd-network-generator tuned udisks2 upower vgauthd vmtoolsd
enabled-runtime	systemd-fsck-root systemd-remount-fs
disabled	arp-ethers blk-availability brltty canberra-system-bootup canberra-system-shutdown canberra-system-shutdown-reboot chrony-wait chronyd-restricted cni-dhcp console-getty cpupower cups-browsed dbus-daemon debug-shell dnf-system-upgrade dnsmasq iprddump iprinit iprupdate iscsi-init iscsid iscsiui kpatch kvm_stat ledmon man-db-restart-cache-update netavark-dhcp-proxy netavark-firewalld-reload nftables nvmmf-autoconnect ostree-readonly-sysroot-migration ostree-state-overlay@ pesign podman podman-auto-update podman-clean-transient podman-kube@ podman-restart psacct ras-mc-ctl rasdaemon rdisc rhcd rhsm rhsm-facts rpmdm-rebuild selinux-check-proper-disable serial-getty@ speech-dispatcherd sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysextd wpa_supplicant
indirect	iscsi spice-vdagentd sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate systemd-sysupdate-reboot

14. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=(hd2,gpt2)/vmlinuz-5.14.0-427.13.1.el9_4.x86_64
root=UUID=89e817bc-bc83-4d5b-abc7-e52deb26e589
ro

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

NEC Corporation

SPECspeed®2017_int_base = 15.3

Express5800/T110m (Intel Xeon 6325P)

SPECspeed®2017_int_peak = 15.7

CPU2017 License: 9006

Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Dec-2025

Hardware Availability: Jun-2025

Software Availability: Apr-2024

Platform Notes (Continued)

resume=UUID=74db9ce4-7e0e-4ccd-ae41-c56b65df5a2e
rhgb
quiet

15. cpupower frequency-info

analyzing CPU 6:

current policy: frequency should be within 800 MHz and 3.50 GHz.
The governor "performance" may decide which speed to use
within this range.

boost state support:

Supported: yes

Active: yes

16. tuned-adm active

It seems that tuned daemon is not running, preset profile is not activated.
Preset profile: throughput-performance

17. sysctl

kernel.numa_balancing	0
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	20
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	60
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	0

18. /sys/kernel/mm/transparent_hugepage

defrag	always defer defer+madvise [madvise] never
enabled	[always] madvise never
hpage_pmd_size	2097152
shmem_enabled	always within_size advise [never] deny force

19. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

20. OS release

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

NEC Corporation

SPECspeed®2017_int_base = 15.3

Express5800/T110m (Intel Xeon 6325P)

SPECspeed®2017_int_peak = 15.7

CPU2017 License: 9006

Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Dec-2025

Hardware Availability: Jun-2025

Software Availability: Apr-2024

Platform Notes (Continued)

```
From /etc/*-release /etc/*-version
os-release      Red Hat Enterprise Linux 9.4 (Plow)
redhat-release  Red Hat Enterprise Linux release 9.4 (Plow)
system-release  Red Hat Enterprise Linux release 9.4 (Plow)
```

21. Disk information

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda3	ext4	824G	65G	718G	9%	/

22. /sys/devices/virtual/dmi/id

Vendor: NEC
Product: Express5800/T110m [N8100-3026Y]
Serial: 0123456

23. dmidecode

Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

2x Samsung M324R4GA3BB0-CQK0D 32 GB 2 rank 4800, configured at 4400

24. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.
BIOS Version: F52
BIOS Date: 03/04/2025
BIOS Revision: 15.52

Compiler Version Notes

```
=====  
C      | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)  
      | 657.xz_s(base, peak)  
=====
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

```
=====  
C++    | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)  
      | 641.leela_s(base, peak)  
=====
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

```
=====  
Fortran | 648.exchange2_s(base, peak)  
=====
```

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

NEC Corporation

SPECspeed®2017_int_base = 15.3

Express5800/T110m (Intel Xeon 6325P)

SPECspeed®2017_int_peak = 15.7

CPU2017 License: 9006

Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Dec-2025

Hardware Availability: Jun-2025

Software Availability: Apr-2024

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

NEC Corporation

SPECspeed®2017_int_base = 15.3

Express5800/T110m (Intel Xeon 6325P)

SPECspeed®2017_int_peak = 15.7

CPU2017 License: 9006

Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Dec-2025

Hardware Availability: Jun-2025

Software Availability: Apr-2024

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -w -m64 -std=c11 -Wl, -z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2 -flto
-Ofast(pass 1) -O3 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
602.gcc_s: -w -m64 -std=c11 -Wl, -z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2 -flto
-Ofast(pass 1) -O3 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

605.mcf_s: basepeak = yes

```
625.x264_s: -w -std=c11 -m64 -Wl, -z,muldefs -xCORE-AVX2 -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

657.xz_s: basepeak = yes

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

NEC Corporation

SPECspeed®2017_int_base = 15.3

Express5800/T110m (Intel Xeon 6325P)

SPECspeed®2017_int_peak = 15.7

CPU2017 License: 9006

Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Dec-2025

Hardware Availability: Jun-2025

Software Availability: Apr-2024

Peak Optimization Flags (Continued)

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.0-T110m-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.0-T110m-RevA.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-12-03 17:32:27-0500.

Report generated on 2026-08-06 16:26:04 by CPU2017 PDF formatter v6716.

Originally published on 2026-08-06.