



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL145 Gen11

(2.95 GHz, AMD EPYC 8225P)

SPECspeed®2017_int_base = 19.2

SPECspeed®2017_int_peak = 19.5

CPU2017 License: 3

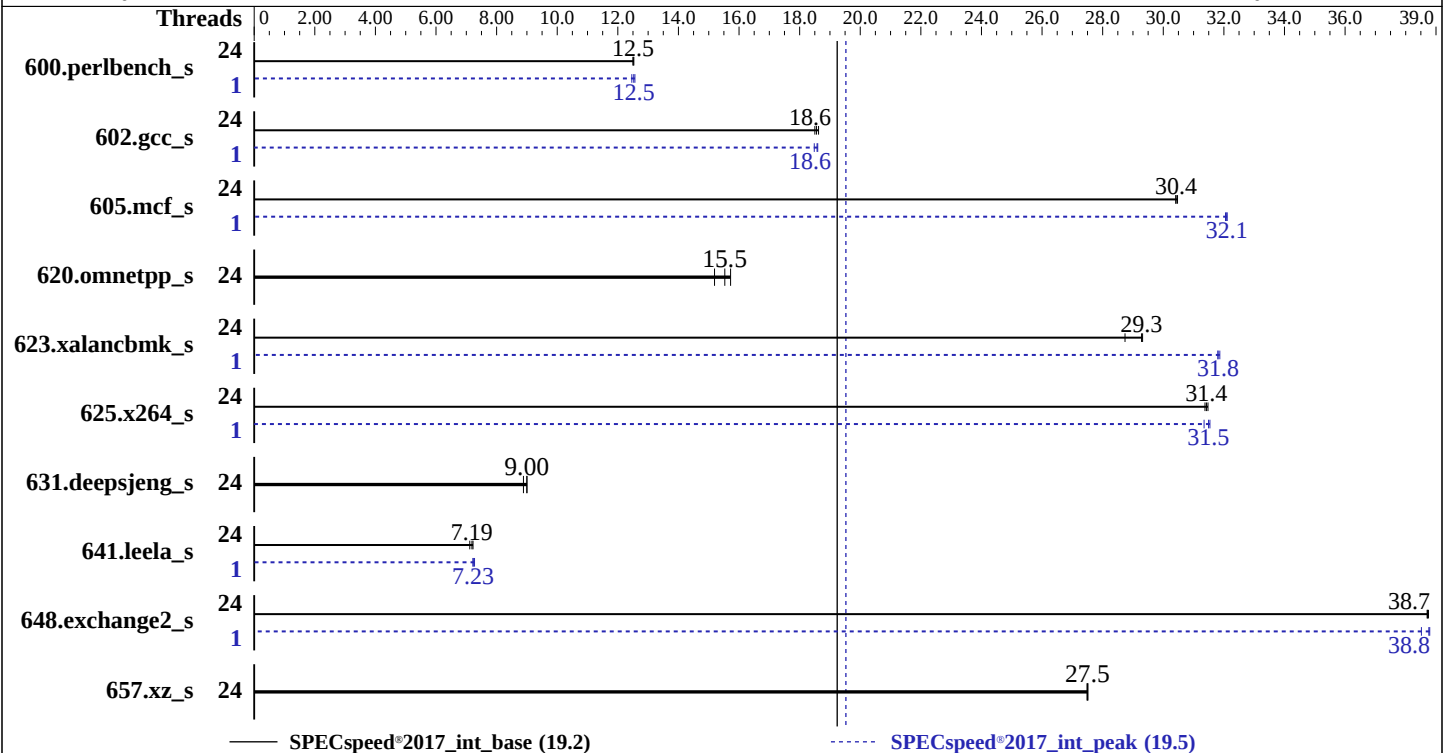
Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: Jun-2026

Software Availability: Jul-2025



Hardware

CPU Name: AMD EPYC 8225P
Max MHz: 4500
Nominal: 2950
Enabled: 24 cores, 1 chip
Orderable: 1 Chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 128 MB I+D on chip per chip,
32 MB shared / 6 cores
Other: None
Memory: 192 GB (6 x 32 GB 2Rx8 PC5-6400B-R)
Storage: 1 x 480 GB NVMe SSD
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP7
Kernel 6.4.0-150700.53.6-default
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: Yes
Firmware: HPE BIOS Version v2.10 04/02/2026
released Apr-2026
File System: btrfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: BIOS is set to prefer performance at the cost of additional power usage



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL145 Gen11

(2.95 GHz, AMD EPYC 8225P)

SPECspeed®2017_int_base = 19.2

SPECspeed®2017_int_peak = 19.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: Jun-2026

Software Availability: Jul-2025

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	24	142	12.5	<u>142</u>	<u>12.5</u>	142	12.5	<u>1</u>	<u>142</u>	<u>12.5</u>	141	12.6	<u>142</u>	<u>12.5</u>
602.gcc_s	24	214	18.6	<u>215</u>	<u>18.6</u>	215	18.5	<u>1</u>	<u>214</u>	<u>18.6</u>	215	18.5	214	18.6
605.mcf_s	24	155	30.5	155	30.4	<u>155</u>	<u>30.4</u>	<u>1</u>	147	32.1	147	32.1	<u>147</u>	<u>32.1</u>
620.omnetpp_s	24	<u>105</u>	<u>15.5</u>	107	15.2	104	15.7	<u>24</u>	<u>105</u>	<u>15.5</u>	107	15.2	104	15.7
623.xalancbmk_s	24	<u>48.4</u>	<u>29.3</u>	49.3	28.7	48.3	29.3	<u>1</u>	44.5	31.9	44.6	31.8	<u>44.6</u>	<u>31.8</u>
625.x264_s	24	56.0	31.5	<u>56.1</u>	<u>31.4</u>	56.2	31.4	<u>1</u>	55.9	31.5	56.3	31.3	<u>56.0</u>	<u>31.5</u>
631.deepsjeng_s	24	161	8.89	<u>159</u>	<u>9.00</u>	159	9.00	<u>24</u>	161	8.89	<u>159</u>	<u>9.00</u>	159	9.00
641.leela_s	24	236	7.22	<u>237</u>	<u>7.19</u>	240	7.11	<u>1</u>	<u>236</u>	<u>7.23</u>	236	7.22	235	7.27
648.exchange2_s	24	76.0	38.7	75.9	38.8	<u>75.9</u>	<u>38.7</u>	<u>1</u>	76.3	38.5	<u>75.8</u>	<u>38.8</u>	75.8	38.8
657.xz_s	24	<u>225</u>	<u>27.5</u>	225	27.5	225	27.5	<u>24</u>	<u>225</u>	<u>27.5</u>	225	27.5	225	27.5

SPECspeed®2017_int_base = 19.2

SPECspeed®2017_int_peak = 19.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.

tuned-adm profile was turned off using 'tuned-adm off'



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL145 Gen11

(2.95 GHz, AMD EPYC 8225P)

SPECspeed®2017_int_base = 19.2

SPECspeed®2017_int_peak = 19.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: Jun-2026

Software Availability: Jul-2025

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP_CPU_AFFINITY = "0-23"

LD_LIBRARY_PATH =

"/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib32:"

LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"

MALLOC_CONF = "retain:true"

OMP_DYNAMIC = "false"

OMP_SCHEDULE = "static"

OMP_STACKSIZE = "128M"

OMP_THREAD_LIMIT = "24"

Environment variables set by runcpu during the 600.perlbench_s peak run:

GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 602.gcc_s peak run:

GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 605.mcf_s peak run:

GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 623.xalancbmk_s peak run:

GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 625.x264_s peak run:

GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 641.leela_s peak run:

GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 648.exchange2_s peak run:

GOMP_CPU_AFFINITY = "15"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB

Memory using Ubuntu 22.04

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configurations : Parameters are selected in the order shown below

Workload Profile set to General Peak Frequency Compute

AMD SMT Option set to Disabled

Determinism Control set to Manual

Performance Determinism set to Power Deterministic

Memory Patrol Scrubbing set to Disabled

NUMA memory domains per socket set to Two memory domains per socket

AMD Periodic Directory Rinse Tuning set to Blended

ACPI CST C2 Latency set to 18 microseconds

Thermal Configuration set to Maximum Cooling

Package Power Limit Control Mode set to Manual

Package Power Limit Value set to 225

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL145 Gen11

(2.95 GHz, AMD EPYC 8225P)

SPECspeed®2017_int_base = 19.2

SPECspeed®2017_int_peak = 19.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: Jun-2026

Software Availability: Jul-2025

Platform Notes (Continued)

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Fri Jun 19 11:56:08 2026

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.27+suse.167.g130293e510)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

1. uname -a
Linux localhost 6.4.0-150700.53.6-default #1 SMP PREEMPT_DYNAMIC Tue Jul 1 14:54:47 UTC 2025 (8ab7501)
x86_64 x86_64 x86_64 GNU/Linux

2. w
11:56:08 up 1 min, 3 users, load average: 0.68, 0.29, 0.10
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 772366
max locked memory (kbytes, -l) 2097152
max memory size (kbytes, -m) unlimited
open files (-n) 999999
pipe size (512 bytes, -p) 8

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL145 Gen11

(2.95 GHz, AMD EPYC 8225P)

SPECspeed®2017_int_base = 19.2

SPECspeed®2017_int_peak = 19.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: Jun-2026

Software Availability: Jul-2025

Platform Notes (Continued)

POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) unlimited
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

```
-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
/bin/bash $SPEC/run_intspeed_sh.sh
sudo ./run_intspeed.py
python3 ./run_intspeed.py
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 intspeer
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runtime speed --tune base:peak --size test:train:refspeed intspeer --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.004/templogs/preenv.intspeer.004.0.log --lognum 004.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

```
-----
6. /proc/cpuinfo
model name      : AMD EPYC 8225P 24-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 26
model          : 2
stepping       : 1
microcode      : 0xb002162
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass srso
TLB size      : 192 4K pages
cpu cores      : 24
siblings       : 24
1 physical ids (chips)
24 processors (hardware threads)
physical id 0: core ids 0-5,8-13,16-21,24-29
physical id 0: apicids 0-5,8-13,16-21,24-29
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

7. lscpu

From lscpu from util-linux 2.40.4:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	52 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	24
On-line CPU(s) list:	0-23
Vendor ID:	AuthenticAMD
Model name:	AMD EPYC 8225P 24-Core Processor
CPU family:	26
Model:	2
Thread(s) per core:	1
Core(s) per socket:	24

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL145 Gen11

(2.95 GHz, AMD EPYC 8225P)

SPECspeed®2017_int_base = 19.2

SPECspeed®2017_int_peak = 19.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: Jun-2026

Software Availability: Jul-2025

Platform Notes (Continued)

Socket(s): 1
Stepping: 1
BogoMIPS: 5890.78
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin cppc amd_ibpb_ret arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect flush_l1d debug_swap hv_inuse_wr_allowed srso_user_kernel_no amd_lbr_pmc_freeze

Virtualization: AMD-V
L1d cache: 1.1 MiB (24 instances)
L1i cache: 768 KiB (24 instances)
L2 cache: 24 MiB (24 instances)
L3 cache: 128 MiB (4 instances)
NUMA node(s): 2
NUMA node0 CPU(s): 0-11
NUMA node1 CPU(s): 12-23
Vulnerability Gather data sampling: Not affected
Vulnerability Indirect target selection: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Mitigation; IBPB on VMEXIT only
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP disabled; PBRSE-eIBRS Not affected; BHI Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.1M	12	Data	1	64	1	64
L1i	32K	768K	8	Instruction	1	64	1	64
L2	1M	24M	16	Unified	2	1024	1	64
L3	32M	128M	16	Unified	3	32768	1	64

8. numactl --hardware

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL145 Gen11

(2.95 GHz, AMD EPYC 8225P)

SPECspeed®2017_int_base = 19.2

SPECspeed®2017_int_peak = 19.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: Jun-2026

Software Availability: Jul-2025

Platform Notes (Continued)

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0-11

node 0 size: 96448 MB

node 0 free: 95840 MB

node 1 cpus: 12-23

node 1 size: 96672 MB

node 1 free: 95841 MB

node distances:

node 0 1

0: 10 12

1: 12 10

9. /proc/meminfo

MemTotal: 197755876 kB

10. who -r

run-level 3 Jun 19 11:55

11. Systemd service manager version: systemd 254 (254.27+suse.167.g130293e510)

Default Target Status

multi-user running

12. Services, from systemctl list-unit-files

STATE UNIT FILES

enabled ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd
bluetooth cron display-manager getty@ irqbalance issue-generator kbdsettings klog
lvm2-monitor nsd postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore tuned
wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant

enabled-runtime systemd-remount-fs

disabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon autofs
autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl ca-certificates
chrony-wait chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables
exchange-bmc-os-info firewalld fsidd gnome-remote-desktop gpm grub2-once haveged
hwloc-dump-hwdata ipmi ipmievd issue-add-ssh-keys kexec-load lunmask man-db-create
multipathd nfs nfs-blkmap nmb openvpn@ ostree-remount ostree-state-overlay@ rpcbind
rpmconfigcheck rsyncd rtkit-daemon samba-bggd serial-getty@ smartd_generate_opts smb snmpd
snmptrapd speech-dispatcherd systemd-boot-check-no-failures systemd-confext
systemd-network-generator systemd-sysextd systemd-time-wait-sync systemd-timesyncd udisks2
update-system-flatpaks upower vncserver@ wpa_supplicant@

indirect pcsd saned@ systemd-userdbd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=/boot/vmlinuz-6.4.0-150700.53.6-default

root=UUID=3b2a6c4c-85d4-4fc4-a0f9-42d5898806b6

splash=silent

mitigations=auto

quiet

security=apparmor

14. cpupower frequency-info

analyzing CPU 5:

Unable to determine current policy

boost state support:

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL145 Gen11

(2.95 GHz, AMD EPYC 8225P)

SPECspeed®2017_int_base = 19.2

SPECspeed®2017_int_peak = 19.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: Jun-2026

Software Availability: Jul-2025

Platform Notes (Continued)

Supported: yes

Active: yes

15. tuned-adm active
No current active profile.

16. sysctl
kernel.numa_balancing 1
kernel.randomize_va_space 0
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 8
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 1
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 1

17. /sys/kernel/mm/transparent_hugepage
defrag [always] defer+madvise madvise never
enabled [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000

19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP7

20. Disk information
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 btrfs 447G 21G 423G 5% /home

21. /sys/devices/virtual/dmi/id
Vendor: HPE

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL145 Gen11

(2.95 GHz, AMD EPYC 8225P)

SPECspeed®2017_int_base = 19.2

SPECspeed®2017_int_peak = 19.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: Jun-2026

Software Availability: Jul-2025

Platform Notes (Continued)

Product: ProLiant DL145 Gen11

Product Family: ProLiant

Serial: 41600016J0A1

22. dmidecode

Additional information from dmidecode 3.6 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

4x Micron MTC20F2085S1RC64BD2 MWFF 32 GB 2 rank 6400

2x Micron MTC20F2085S1RC64BD2 QSFF 32 GB 2 rank 6400

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE

BIOS Version: 2.10

BIOS Date: 04/02/2026

BIOS Revision: 2.10

Firmware Revision: 1.76

Compiler Version Notes

```
C | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
  | 657.xz_s(base, peak)
```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin

```
C++ | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
    | 641.leela_s(base, peak)
```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin

```
Fortran | 648.exchange2_s(base, peak)
```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL145 Gen11

(2.95 GHz, AMD EPYC 8225P)

SPECspeed®2017_int_base = 19.2

SPECspeed®2017_int_peak = 19.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: Jun-2026

Software Availability: Jul-2025

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Base Portability Flags

```
600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -Wl,-mllvm -Wl,-extra-inliner -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP
-flto -fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp -lamdlibm
-lflang -lamdalloc
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc-ext
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL145 Gen11

(2.95 GHz, AMD EPYC 8225P)

SPECspeed®2017_int_base = 19.2

SPECspeed®2017_int_peak = 19.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: Jun-2026

Software Availability: Jul-2025

Base Optimization Flags (Continued)

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-iv-split -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -O3 -march=znver5 -fveclib=AMDLIBM
-ffast-math -fopenmp -flt0 -mllvm -optimize-strided-mem-cost
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc

Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Peak Portability Flags

Same as Base Portability Flags



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL145 Gen11

(2.95 GHz, AMD EPYC 8225P)

SPECspeed®2017_int_base = 19.2

SPECspeed®2017_int_peak = 19.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: Jun-2026

Software Availability: Jul-2025

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang

602.gcc_s: Same as 600.perlbench_s

605.mcf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang

625.x264_s: Same as 600.perlbench_s

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -fopenmp=libomp -lomp
-lamdlibm -lamdalloc-ext -lflang

631.deepsjeng_s: basepeak = yes

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL145 Gen11

(2.95 GHz, AMD EPYC 8225P)

SPECspeed®2017_int_base = 19.2

SPECspeed®2017_int_peak = 19.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: Jun-2026

Software Availability: Jul-2025

Peak Optimization Flags (Continued)

```
641.leela_s: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-iv-split -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -O3 -march=znver5 -fveclib=AMDLIBM
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang
```

Peak Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Sorano-rev1.3.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Sorano-rev1.3.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2026-06-19 02:26:08-0400.

Report generated on 2026-07-14 17:26:15 by CPU2017 PDF formatter v6716.

Originally published on 2026-07-14.