



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant MicroServer Gen11
(3.10 GHz, Intel Xeon 6377P)

SPECspeed®2017_fp_base = 98.5

SPECspeed®2017_fp_peak = 98.5

CPU2017 License: 3

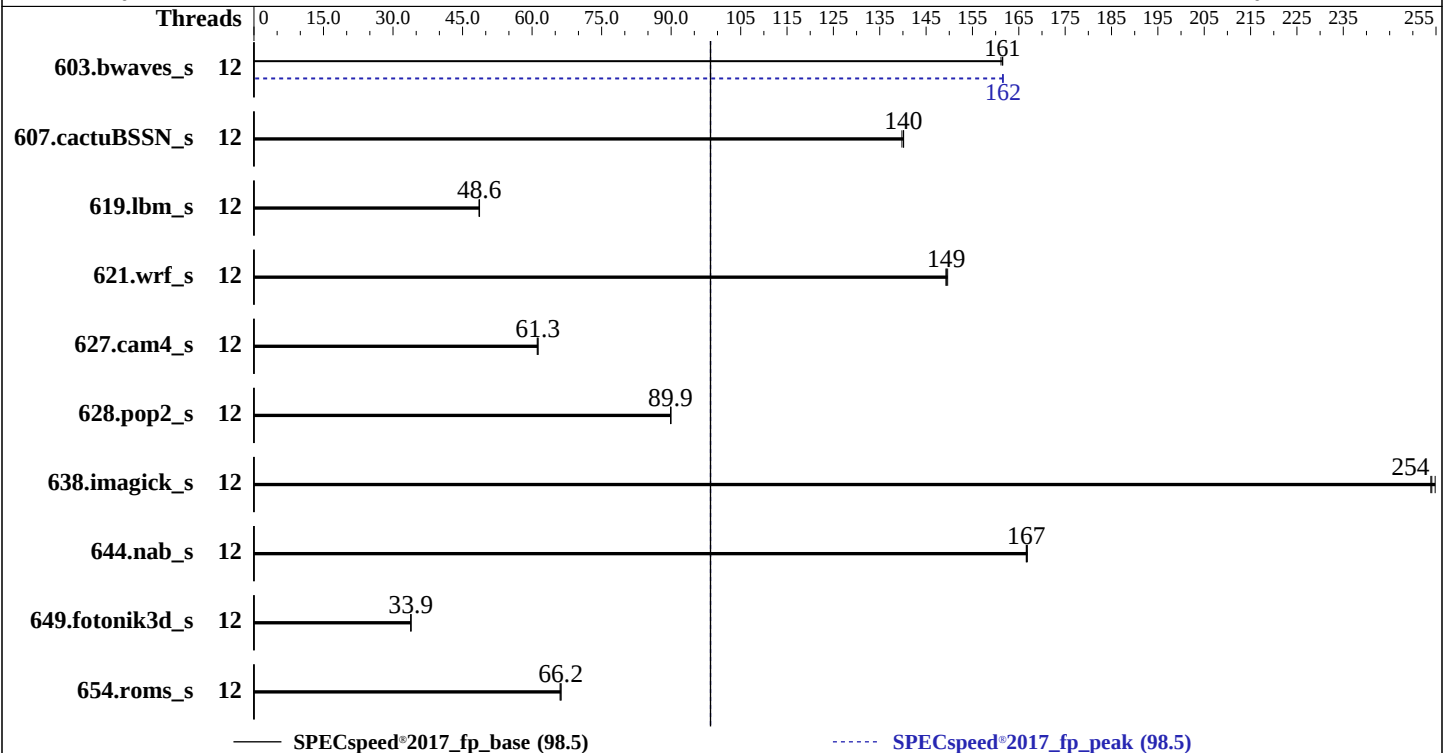
Test Sponsor: HPE

Tested by: HPE

Test Date: Jul-2026

Hardware Availability: Sep-2026

Software Availability: Jul-2025



Hardware

CPU Name: Intel Xeon 6377P
Max MHz: 5700
Nominal: 3100
Enabled: 12 cores, 1 chip
Orderable: 1 Chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 36 MB I+D on chip per chip
Other: None
Memory: 64 GB (2 x 32 GB 2Rx8 PC5-5600B-E, running at 4400)
Storage: 1 x 960 GB NVMe SSD
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP7
Kernel 6.4.0-150700.53.6-default
Compiler: C/C++: Version 2025.2 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2025.2 of Intel Fortran Compiler for Linux;
Parallel: Yes
Firmware: HPE BIOS Version v2.50 06/25/2026 released Jun-2026
File System: btrfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS is set to prefer performance at the cost of additional power usage



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant MicroServer Gen11
(3.10 GHz, Intel Xeon 6377P)

SPECspeed®2017_fp_base = 98.5

SPECspeed®2017_fp_peak = 98.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jul-2026

Hardware Availability: Sep-2026

Software Availability: Jul-2025

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	12	366	161	365	162	<u>365</u>	<u>161</u>	12	365	162	<u>365</u>	<u>162</u>	365	161
607.cactuBSSN_s	12	119	140	119	140	<u>119</u>	<u>140</u>	12	119	140	119	140	<u>119</u>	<u>140</u>
619.lbm_s	12	<u>108</u>	<u>48.6</u>	108	48.6	108	48.7	12	<u>108</u>	<u>48.6</u>	108	48.6	108	48.7
621.wrf_s	12	88.4	150	<u>88.5</u>	<u>149</u>	88.6	149	12	88.4	150	<u>88.5</u>	<u>149</u>	88.6	149
627.cam4_s	12	145	61.3	<u>145</u>	<u>61.3</u>	145	61.1	12	145	61.3	<u>145</u>	<u>61.3</u>	145	61.1
628.pop2_s	12	<u>132</u>	<u>89.9</u>	132	89.9	132	90.0	12	<u>132</u>	<u>89.9</u>	132	89.9	132	90.0
638.imagick_s	12	56.6	255	56.8	254	<u>56.8</u>	<u>254</u>	12	56.6	255	56.8	254	<u>56.8</u>	<u>254</u>
644.nab_s	12	105	167	<u>105</u>	<u>167</u>	105	167	12	105	167	<u>105</u>	<u>167</u>	105	167
649.fotonik3d_s	12	269	33.9	<u>269</u>	<u>33.9</u>	269	33.8	12	269	33.9	<u>269</u>	<u>33.9</u>	269	33.8
654.roms_s	12	<u>238</u>	<u>66.2</u>	238	66.0	238	66.3	12	<u>238</u>	<u>66.2</u>	238	66.0	238	66.3

SPECspeed®2017_fp_base = 98.5

SPECspeed®2017_fp_peak = 98.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
tuned-adm profile was set to balanced using 'tuned-adm profile balanced'

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Redhat Enterprise Linux 8.0
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant MicroServer Gen11
(3.10 GHz, Intel Xeon 6377P)

SPECspeed®2017_fp_base = 98.5

SPECspeed®2017_fp_peak = 98.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jul-2026

Hardware Availability: Sep-2026

Software Availability: Jul-2025

Platform Notes

BIOS Configurations : Parameters are selected in the order shown below
Workload Profile set to General Peak Frequency Compute
Thermal Configuration set to Maximum Cooling
Intel Hyper-Threading set to Disabled
Workload Profile set to Custom
Power Regulator set to Dynamic Power Savings Mode

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Mon Jul 6 12:55:39 2026

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.27+suse.167.g130293e510)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

1. uname -a
Linux localhost 6.4.0-150700.53.6-default #1 SMP PREEMPT_DYNAMIC Tue Jul 1 14:54:47 UTC 2025 (8ab7501)
x86_64 x86_64 x86_64 GNU/Linux

2. w
12:55:39 up 1 min, 4 users, load average: 1.18, 0.65, 0.25
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant MicroServer Gen11
(3.10 GHz, Intel Xeon 6377P)

SPECspeed®2017_fp_base = 98.5

SPECspeed®2017_fp_peak = 98.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jul-2026

Hardware Availability: Sep-2026

Software Availability: Jul-2025

Platform Notes (Continued)

```
file size          (blocks, -f) unlimited
pending signals    (-i) 256695
max locked memory  (kbytes, -l) 8192
max memory size    (kbytes, -m) unlimited
open files         (-n) 1024
pipe size          (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size         (kbytes, -s) unlimited
cpu time           (seconds, -t) unlimited
max user processes (-u) 256695
virtual memory     (kbytes, -v) unlimited
file locks         (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
bash -c cd $SPEC/ && $SPEC/fpspeed.sh
runcpu --nobuild --action validate --define default-platform-flags -c
ic2025.2-lin-core-avx2-speed-20250605.cfg --iterations=3 --reportable --define cores=12 --tune base,peak
-o all --define drop_caches fpspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
ic2025.2-lin-core-avx2-speed-20250605.cfg --iterations 3 --reportable --define cores=12 --tune base,peak
--output_format all --define drop_caches --nopower --runmode speed --tune base:peak --size refspeed
fpspeed --nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.001/temlogs/preenv.fpspeed.001.0.log
--lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) 6377P
vendor_id       : GenuineIntel
cpu family      : 6
model           : 183
stepping        : 1
microcode       : 0x137
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb bhi
cpu cores       : 12
siblings        : 12
1 physical ids (chips)
12 processors (hardware threads)
physical id 0: core ids 0-11
physical id 0: apicids 0,2,4,6,8,10,12,14,16,18,20,22
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

7. lscpu

From lscpu from util-linux 2.40.4:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 48 bits virtual
Byte Order:            Little Endian
CPU(s):                12
On-line CPU(s) list:   0-11
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant MicroServer Gen11
(3.10 GHz, Intel Xeon 6377P)

SPECspeed®2017_fp_base = 98.5

SPECspeed®2017_fp_peak = 98.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jul-2026

Hardware Availability: Sep-2026

Software Availability: Jul-2025

Platform Notes (Continued)

Vendor ID: GenuineIntel
Model name: Intel(R) Xeon(R) 6377P
CPU family: 6
Model: 183
Thread(s) per core: 1
Core(s) per socket: 12
Socket(s): 1
Stepping: 1
CPU(s) scaling MHz: 14%
CPU max MHz: 5700.0000
CPU min MHz: 800.0000
BogoMIPS: 6220.80
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb ssbd ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed adx smap clflushopt clwb intel_pt sha_ni xsaveopt xsavec xgetbv1 xsaves split_lock_detect user_shstk avx_vnni dtherm ida arat pln pts hwp hwp_notify hwp_act_window hwp_epp hwp_pkg_req hfi vnmi umip pku ospke waitpkg gfni vaes vpclmulqdq tme rdpid movdiri movdir64b fsrm md_clear serialize pconfig arch_lbr ibt flush_l1d arch_capabilities
Virtualization: VT-x
L1d cache: 576 KiB (12 instances)
L1i cache: 384 KiB (12 instances)
L2 cache: 24 MiB (12 instances)
L3 cache: 36 MiB (1 instance)
NUMA node(s): 1
NUMA node0 CPU(s): 0-11
Vulnerability Gather data sampling: Not affected
Vulnerability Indirect target selection: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; PBRSE-eIBRS SW sequence; BHI BHI_DIS_S
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	576K	12	Data	1	64	1	64
L1i	32K	384K	8	Instruction	1	64	1	64
L2	2M	24M	16	Unified	2	2048	1	64
L3	36M	36M	12	Unified	3	49152	1	64

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant MicroServer Gen11
(3.10 GHz, Intel Xeon 6377P)

SPECspeed®2017_fp_base = 98.5

SPECspeed®2017_fp_peak = 98.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jul-2026

Hardware Availability: Sep-2026

Software Availability: Jul-2025

Platform Notes (Continued)

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 1 nodes (0)
node 0 cpus: 0-11
node 0 size: 64203 MB
node 0 free: 63514 MB
node distances:
node 0
0: 10
```

9. /proc/meminfo

```
MemTotal: 65744212 kB
```

10. who -r

```
run-level 3 Jul 6 12:54
```

11. Systemd service manager version: systemd 254 (254.27+suse.167.g130293e510)

```
Default Target Status
multi-user running
```

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron display-manager getty@ irqbalance issue-generator kbdsettings klog lvm2-monitor nsd postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore tuned wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant
enabled-runtime	systemd-remount-fs
disabled	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon autofs autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables exchange-bmc-os-info firewalld fsidd gnome-remote-desktop gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievd issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nmb openvpn@ ostree-remount ostree-state-overlay@ rpcbind rpmconfigcheck rsyncd rtkit-daemon samba-bgqd serial-getty@ smartd_generate_opts smb snmpd snmptrapd speech-dispatcherd systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2 update-system-flatpaks upower vncserver@ wpa_supplicant@
indirect	pcscd saned@ systemd-userdbd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150700.53.6-default
root=UUID=16b66c39-3265-46a8-be7f-7591e5d8731a
splash=silent
mitigations=auto
quiet
security=apparmor
```

14. cpupower frequency-info

```
analyzing CPU 9:
current policy: frequency should be within 800 MHz and 5.70 GHz.
                  The governor "powersave" may decide which speed to use
                  within this range.

boost state support:
Supported: yes
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant MicroServer Gen11
(3.10 GHz, Intel Xeon 6377P)

SPECspeed®2017_fp_base = 98.5

SPECspeed®2017_fp_peak = 98.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jul-2026

Hardware Availability: Sep-2026

Software Availability: Jul-2025

Platform Notes (Continued)

Active: yes

15. tuned-adm active
Current active profile: balanced

16. sysctl
kernel.numa_balancing 0
kernel.randomize_va_space 2
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 20
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 60
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 0

17. /sys/kernel/mm/transparent_hugepage
defrag always defer defer+madvice [madvice] never
enabled [always] madvice never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000

19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP7

20. Disk information
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 btrfs 931G 203G 726G 22% /home

21. /sys/devices/virtual/dmi/id
Vendor: HPE
Product: ProLiant MicroServer Gen11

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant MicroServer Gen11
(3.10 GHz, Intel Xeon 6377P)

SPECspeed®2017_fp_base = 98.5

SPECspeed®2017_fp_peak = 98.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jul-2026

Hardware Availability: Sep-2026

Software Availability: Jul-2025

Platform Notes (Continued)

Product Family: ProLiant
Serial: JRT31JQXTD

22. dmidecode

Additional information from dmidecode 3.6 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

2x Hynix HMC688AGBEA084N 32 GB 2 rank 5600, configured at 4400

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE
BIOS Version: 2.50
BIOS Date: 06/25/2026
BIOS Revision: 2.50
Firmware Revision: 1.62

Compiler Version Notes

=====
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2025.2.0 Build 20250605
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.

=====
C++, C, Fortran | 607.cactuBSSN_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2025.2.0 Build 20250605
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2025.2.0 Build 20250605
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2025.2.0 Build 20250605
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.

=====
Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2025.2.0 Build 20250605
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.

=====
Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2025.2.0 Build 20250605
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2025.2.0 Build 20250605
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant MicroServer Gen11
(3.10 GHz, Intel Xeon 6377P)

SPECspeed®2017_fp_base = 98.5

SPECspeed®2017_fp_peak = 98.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jul-2026

Hardware Availability: Sep-2026

Software Availability: Jul-2025

Base Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -Wno-implicit-int -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant MicroServer Gen11
(3.10 GHz, Intel Xeon 6377P)

SPECspeed®2017_fp_base = 98.5

SPECspeed®2017_fp_peak = 98.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jul-2026

Hardware Availability: Sep-2026

Software Availability: Jul-2025

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:

-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant MicroServer Gen11
(3.10 GHz, Intel Xeon 6377P)

SPECspeed®2017_fp_base = 98.5

SPECspeed®2017_fp_peak = 98.5

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jul-2026

Hardware Availability: Sep-2026

Software Availability: Jul-2025

Peak Optimization Flags (Continued)

Fortran benchmarks:

```
603.bwaves_s: -w -m64 -wl, -z, muldefs -DSPEC_OPENMP -xCORE-AVX2 -Ofast
-ffast-math -flt0 -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc
```

649.fotonik3d_s: basepeak = yes

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2025-official-linux64.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-CatlowRefresh-rev1.1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2025-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-CatlowRefresh-rev1.1.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2026-07-06 03:25:39-0400.

Report generated on 2026-07-29 10:29:04 by CPU2017 PDF formatter v6716.

Originally published on 2026-07-28.