



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL145 Gen11

(2.90 GHz, AMD EPYC 8025P)

SPECspeed®2017_fp_base = 110

SPECspeed®2017_fp_peak = 110

CPU2017 License: 3

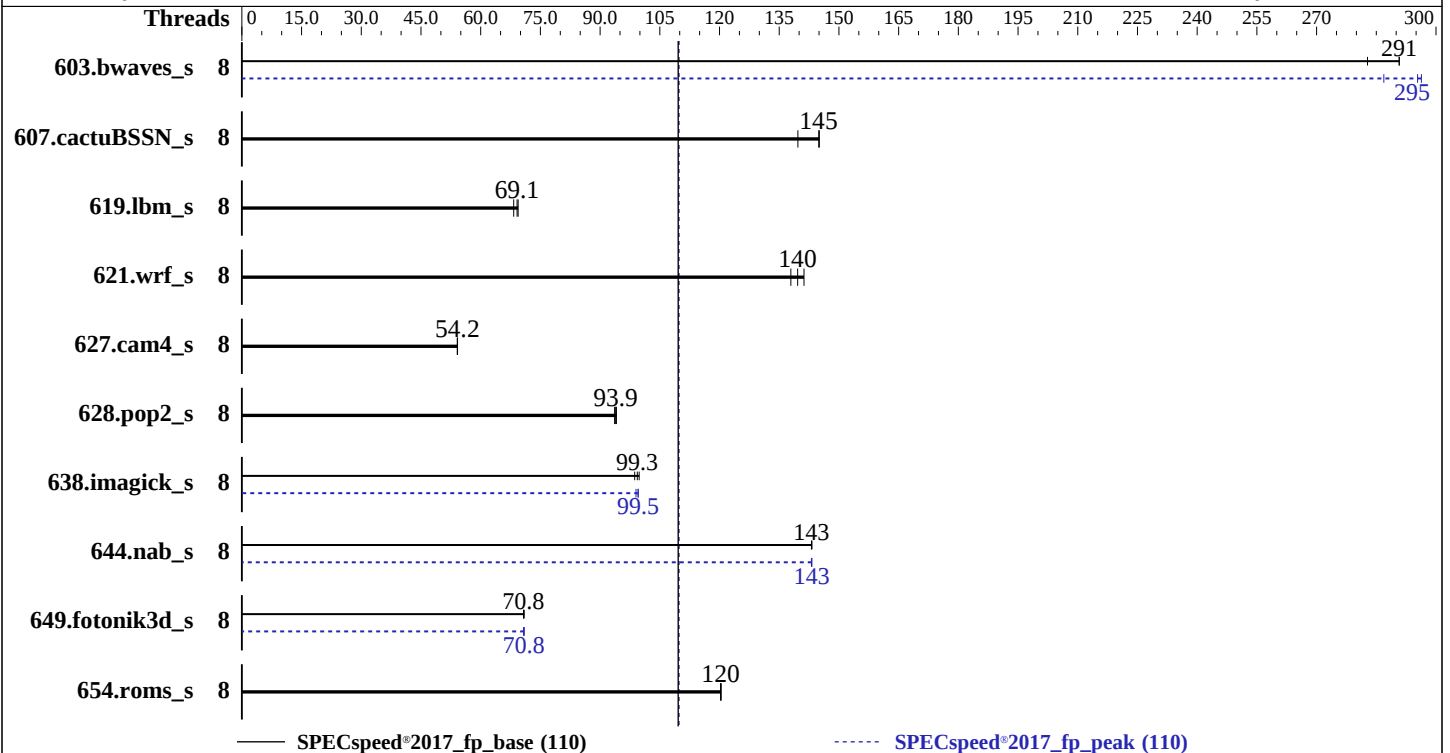
Test Sponsor: HPE

Tested by: HPE

Test Date: Jul-2026

Hardware Availability: Jun-2026

Software Availability: Jul-2025



Hardware

CPU Name: AMD EPYC 8025P
Max MHz: 4500
Nominal: 2900
Enabled: 8 cores, 1 chip
Orderable: 1 Chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 64 MB I+D on chip per chip,
32 MB shared / 4 cores
Other: None
Memory: 192 GB (6 x 32 GB 2Rx8 PC5-6400B-R)
Storage: 1 x 480 GB NVMe SSD
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP7
Kernel 6.4.0-150700.53.6-default
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: Yes
Firmware: HPE BIOS Version v2.10 04/02/2026
released Apr-2026
File System: btrfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: BIOS and OS is set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	8	203	291	203	291	209	283	8	199	296	206	287	200	295
607.cactuBSSN_s	8	115	145	119	140	115	145	8	115	145	119	140	115	145
619.lbm_s	8	76.7	68.3	75.5	69.4	75.8	69.1	8	76.7	68.3	75.5	69.4	75.8	69.1
621.wrf_s	8	94.7	140	93.7	141	95.9	138	8	94.7	140	93.7	141	95.9	138
627.cam4_s	8	164	54.2	164	54.1	164	54.2	8	164	54.2	164	54.1	164	54.2
628.pop2_s	8	126	93.9	127	93.6	126	94.1	8	126	93.9	127	93.6	126	94.1
638.imagick_s	8	145	99.3	146	98.7	145	99.8	8	145	99.6	146	99.0	145	99.5
644.nab_s	8	122	143	122	143	122	143	8	122	143	122	143	122	143
649.fotonik3d_s	8	129	70.9	129	70.8	129	70.8	8	129	70.8	129	70.8	129	70.9
654.roms_s	8	131	120	131	120	131	120	8	131	120	131	120	131	120

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
 'numactl' was used to bind copies to the cores.
 See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
 To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
 To free node-local memory and avoid remote memory usage,
 'sysctl -w vm.zone_reclaim_mode=1' run as root.
 To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
 To disable address space layout randomization (ASLR) to reduce run-to-run
 variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
 'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
 'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.

tuned-adm profile was set to virtual-host using 'tuned-adm profile virtual-host'



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Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP_CPU_AFFINITY = "0-7"

LD_LIBRARY_PATH =

"/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib32:"

LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"

MALLOC_CONF = "retain:true"

OMP_DYNAMIC = "false"

OMP_SCHEDULE = "static"

OMP_STACKSIZE = "128M"

OMP_THREAD_LIMIT = "8"

Environment variables set by runcpu during the 603.bwaves_s peak run:

GOMP_CPU_AFFINITY = "0-7"

Environment variables set by runcpu during the 638.imagick_s peak run:

GOMP_CPU_AFFINITY = "0-7"

Environment variables set by runcpu during the 644.nab_s peak run:

GOMP_CPU_AFFINITY = "0-7"

Environment variables set by runcpu during the 649.fotonik3d_s peak run:

GOMP_CPU_AFFINITY = "0-7"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB

Memory using Ubuntu 22.04

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configurations : Parameters are selected in the order shown below

Workload Profile set to General Peak Frequency Compute

AMD SMT Option set to Disabled

Determinism Control set to Manual

Performance Determinism set to Power Deterministic

Memory Patrol Scrubbing set to Disabled

AMD Periodic Directory Rinse Tuning set to Blended

ACPI CST C2 Latency set to 18 microseconds

Thermal Configuration set to Maximum Cooling

Package Power Limit Control Mode set to Manual

Package Power Limit Value set to 100

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost Wed Jul 8 01:45:55 2026

SUT (System Under Test) info as seen by some common utilities.

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Platform Notes (Continued)

```
1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.27+suse.167.g130293e510)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS
```

```
-----
1. uname -a
Linux localhost 6.4.0-150700.53.6-default #1 SMP PREEMPT_DYNAMIC Tue Jul 1 14:54:47 UTC 2025 (8ab7501)
x86_64 x86_64 x86_64 GNU/Linux
```

```
-----
2. w
01:45:55 up 1 min, 5 users, load average: 3.07, 1.49, 0.55
USER      TTY      FROM          LOGIN@   IDLE   JCPU   PCPU WHAT
```

```
-----
3. Username
From environment variable $USER: root
```

```
-----
4. ulimit -a
core file size          (blocks, -c) 0
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 771634
max locked memory       (kbytes, -l) 2097152
max memory size         (kbytes, -m) unlimited
open files              (-n) 999999
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 999999
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

```
-----
5. sysinfo process ancestry
```

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Platform Notes (Continued)

```
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
/bin/bash $SPEC/run_fpspeed_sh.sh
sudo ./run_fpspeed.py
python3 ./run_fpspeed.py
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fpspeed
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/temlogs/preenv.fpspeed.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

6. /proc/cpuinfo

```
model name      : AMD EPYC 8025P 8-Core Processor
vendor_id       : AuthenticAMD
cpu family      : 26
model           : 2
stepping        : 1
microcode       : 0xb002162
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass srso
TLB size        : 192 4K pages
cpu cores       : 8
siblings        : 8
1 physical ids (chips)
8 processors (hardware threads)
physical id 0:  core ids 0-7
physical id 0:  apicids 0-7
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.40.4:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 8
On-line CPU(s) list:    0-7
Vendor ID:              AuthenticAMD
Model name:             AMD EPYC 8025P 8-Core Processor
CPU family:             26
Model:                  2
Thread(s) per core:     1
Core(s) per socket:     8
Socket(s):              1
Stepping:               1
BogoMIPS:               5791.40
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                        pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt
                        pdpe1gb rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl
                        nonstop_tsc cpuid extd_apicid aperfmperf rapl pni pclmulqdq
                        monitor ssse3 fma cx16 pcid sse4_1 sse4_2 movbe popcnt aes xsave
                        avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm
                        sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext
```

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Platform Notes (Continued)

perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3
hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp ibrs_enhanced
vmcall fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm
rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb
avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves
cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local user_shstk
avx_vnni avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd
amd_ppin cpcp amd_ibpb_ret arat npt lbrv svm_lock nrip_save
tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi
avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid
bus_lock_detect movdiri movdir64b overflow_recov succor smca fsrm
avx512_vp2intersect flush_l1d debug_swap hv_inuse_wr_allowed
srso_user_kernel_no amd_lbr_pmc_freeze

Virtualization:

L1d cache:

L1i cache:

L2 cache:

L3 cache:

NUMA node(s):

NUMA node0 CPU(s):

Vulnerability Gather data sampling:

Vulnerability Indirect target selection:

Vulnerability Itlb multihit:

Vulnerability L1tf:

Vulnerability Mds:

Vulnerability Meltdown:

Vulnerability Mmio stale data:

Vulnerability Reg file data sampling:

Vulnerability Retbleed:

Vulnerability Spec rstack overflow:

Vulnerability Spec store bypass:

Vulnerability Spectre v1:

Vulnerability Spectre v2:

Vulnerability Srbds:

Vulnerability Tsx async abort:

AMD-V

384 KiB (8 instances)

256 KiB (8 instances)

8 MiB (8 instances)

64 MiB (2 instances)

1

0-7

Not affected

Not affected

Not affected

Not affected

Not affected

Not affected

Not affected

Not affected

Mitigation; IBPB on VMEXIT only

Mitigation; Speculative Store Bypass disabled via prctl

Mitigation; usercopy/swaps barriers and __user pointer

sanitization

Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP

disabled; PBRSE-eIBRS Not affected; BHI Not affected

Not affected

Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	384K	12	Data	1	64	1	64
L1i	32K	256K	8	Instruction	1	64	1	64
L2	1M	8M	16	Unified	2	1024	1	64
L3	32M	64M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 1 nodes (0)

node 0 cpus: 0-7

node 0 size: 192937 MB

node 0 free: 191933 MB

node distances:

node 0

0: 10

9. /proc/meminfo

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Platform Notes (Continued)

MemTotal: 197568384 kB

10. who -r
run-level 3 Jul 8 01:45

11. Systemd service manager version: systemd 254 (254.27+suse.167.g130293e510)
Default Target Status
multi-user running

12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd
bluetooth cron display-manager getty@ irqbalance issue-generator kbdsettings klog
lvm2-monitor nscd postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore tuned
wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant
enabled-runtime systemd-remount-fs
disabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon autofs
autostart-initscripts blk-availability bluetooth-mesh boot-sysctl ca-certificates
chrony-wait chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables
exchange-bmc-os-info firewalld fsidd gnome-remote-desktop gpm grub2-once haveged
hwloc-dump-hwdata ipmi ipmievdev issue-add-ssh-keys kexec-load lunmask man-db-create
multipathd nfs nfs-blkmap nmb openvpn@ ostree-remount ostree-state-overlay@ rpcbind
rpmconfigcheck rsyncd rtkit-daemon samba-bgqd serial-getty@ smartd_generate_opts smb snmpd
snmptrapd speech-dispatcherd systemd-boot-check-no-failures systemd-confext
systemd-network-generator systemd-sysexit systemd-time-wait-sync systemd-timesyncd udisks2
update-system-flatpaks upower vncserver@ wpa_supplicant@
indirect pcsd saned@ systemd-userdbd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150700.53.6-default
root=UUID=a5bd0644-2f89-4629-93cd-27eba6a46f18
splash=silent
mitigations=auto
quiet
security=apparmor

14. cpupower frequency-info
analyzing CPU 2:
Unable to determine current policy
boost state support:
Supported: yes
Active: yes

15. tuned-adm active
Current active profile: virtual-host

16. sysctl
kernel.numa_balancing 0
kernel.randomize_va_space 0
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 5
vm.dirty_bytes 0

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Platform Notes (Continued)

```
vm.dirty_expire_centisecs    3000
vm.dirty_ratio               8
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds  43200
vm.extfrag_threshold         500
vm.min_unmapped_ratio        1
vm.nr_hugepages              0
vm.nr_hugepages_mempolicy    0
vm.nr_overcommit_hugepages   0
vm.swappiness                 1
vm.watermark_boost_factor    15000
vm.watermark_scale_factor    10
vm.zone_reclaim_mode         1
```

```
-----
17. /sys/kernel/mm/transparent_hugepage
defrag      [always] defer defer+madvise madvise never
enabled     [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force
-----
```

```
-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag                1
max_ptes_none         511
max_ptes_shared       256
max_ptes_swap         64
pages_to_scan         4096
scan_sleep_millisecs  10000
-----
```

```
-----
19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP7
-----
```

```
-----
20. Disk information
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       btrfs 447G   20G  424G   5% /home
-----
```

```
-----
21. /sys/devices/virtual/dmi/id
Vendor:      HPE
Product:     ProLiant DL145 Gen11
Product Family: ProLiant
Serial:      DL145GEN11
-----
```

```
-----
22. dmidecode
Additional information from dmidecode 3.6 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  6x Micron MTC20F2085S1RC64BD2 QSFF 32 GB 2 rank 6400
-----
```

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Platform Notes (Continued)

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE
BIOS Version: 2.10
BIOS Date: 04/02/2026
BIOS Revision: 2.10
Firmware Revision: 1.60

Compiler Version Notes

=====
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin
=====

=====
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)
Target: x86_64-unknown-linux-gnu
Thread model: posix
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AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin
=====

=====
Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin
=====

=====
Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin
=====



SPEC CPU®2017 Floating Point Speed Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL145 Gen11

(2.90 GHz, AMD EPYC 8025P)

SPECspeed®2017_fp_base = 110

SPECspeed®2017_fp_peak = 110

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jul-2026

Hardware Availability: Jun-2026

Software Availability: Jul-2025

Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

603.bwaves_s: -DSPEC_LP64

607.cactuBSSN_s: -DSPEC_LP64

619.lbm_s: -DSPEC_LP64

621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64

627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64

628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64

638.imagick_s: -DSPEC_LP64

644.nab_s: -DSPEC_LP64

649.fotonik3d_s: -DSPEC_LP64

654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5

-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto

-fremap-arrays -fstrip-mining -fstruct-layout=7

-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3

-mllvm -unroll-threshold=50 -zopt -mrecip=none -fopenmp=libomp -lomp

-lamdlibm -lamdalloc -lflang

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3

-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver5

-fveclib=AMDLIBM -ffast-math -fopenmp -flto -funroll-loops

-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3

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Base Optimization Flags (Continued)

Fortran benchmarks (continued):

-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang

Benchmarks using both Fortran and C:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -funroll-loops
-mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang

Benchmarks using Fortran, C, and C++:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt
-mllvm -loop-unswitch-threshold=2000000 -mllvm -unroll-threshold=100
-funroll-loops -mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument



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Software Availability: Jul-2025

Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang

644.nab_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mrecip=none
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

Fortran benchmarks:

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Peak Optimization Flags (Continued)

603.bwaves_s: -m64 -Wl, -mllvm -Wl, -align-all-nofallthru-blocks=6
-Wl, -mllvm -Wl, -reduce-array-computations=3
-Wl, -mllvm -Wl, -enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math
-fopenmp -fscalar-transform -fvector-transform
-mllvm -reduce-array-computations=3 -Mrecursive
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

649.fotonik3d_s: -m64 -Wl, -mllvm -Wl, -align-all-nofallthru-blocks=6
-Wl, -mllvm -Wl, -reduce-array-computations=3
-Wl, -mllvm -Wl, -enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math
-fopenmp -flto -mllvm -reduce-array-computations=3
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm
-lamdalloc -lflang

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Sorano-rev1.5.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Sorano-rev1.5.xml>

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