



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL340e Gen12

(2.70 GHz, Intel Xeon 6747P)

SPECrate®2017_int_base = 546

SPECrate®2017_int_peak = 566

CPU2017 License: 3

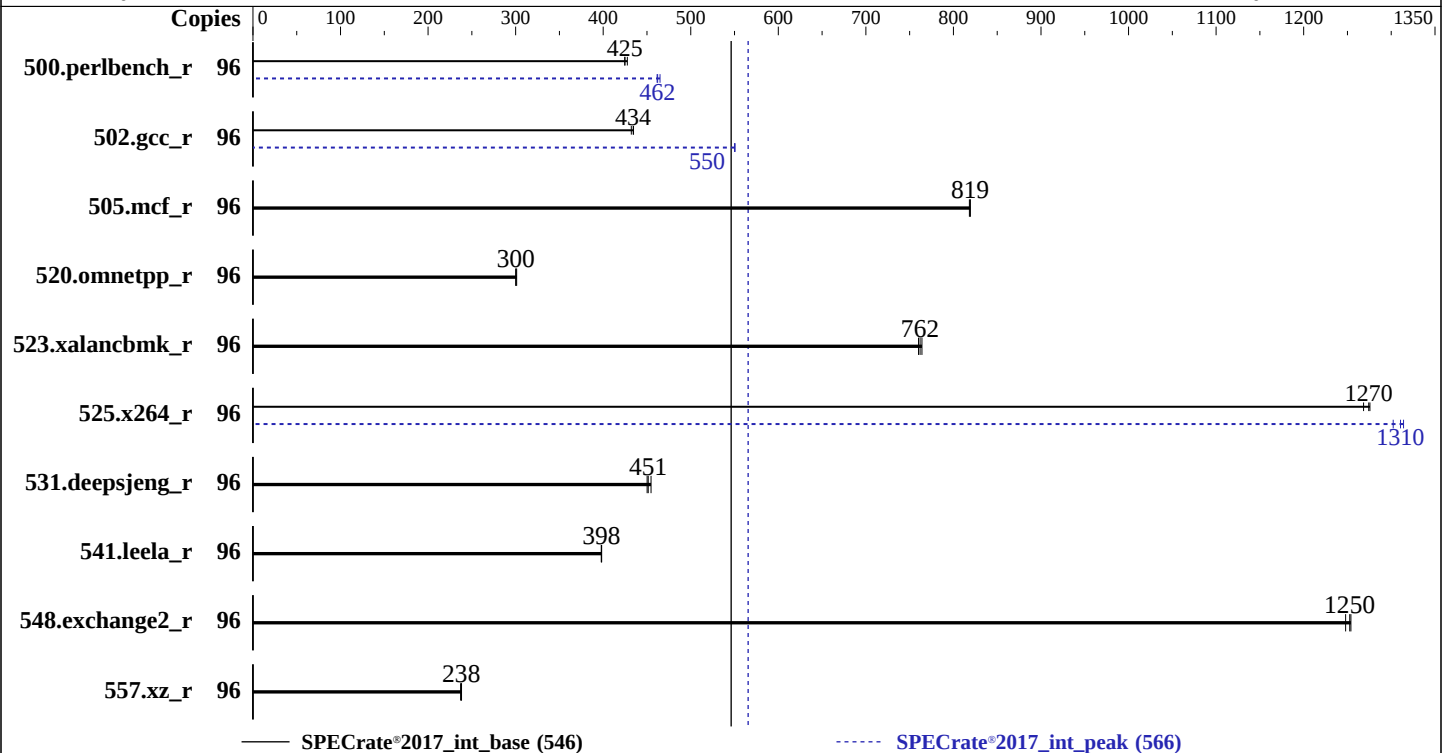
Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: May-2026

Software Availability: Mar-2026



Hardware

CPU Name: Intel Xeon 6747P
Max MHz: 3900
Nominal: 2700
Enabled: 48 cores, 1 chip, 2 threads/core
Orderable: 1 Chip
Cache L1: 64 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 288 MB I+D on chip per chip
Other: None
Memory: 256 GB (8 x 32 GB 2Rx8 PC5-6400B-R)
Storage: 1 x 6.4 TB NVMe SSD
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP7
Kernel 6.4.0-150700.53.6-default
Compiler: C/C++: Version 2026.0 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2026.0 of Intel Fortran Compiler for Linux;
C/C++: Version 2024.2 of Intel oneAPI DPC++/C++ Compiler for Linux;
Parallel: No
Firmware: HPE BIOS Version v1.62 02/06/2026 released Feb-2026
File System: btrfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS is set to prefer performance at the cost of additional power usage



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL340e Gen12

(2.70 GHz, Intel Xeon 6747P)

SPECrate®2017_int_base = 546

SPECrate®2017_int_peak = 566

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: May-2026

Software Availability: Mar-2026

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	96	357	428	<u>360</u>	<u>425</u>	360	425	96	329	465	331	462	<u>331</u>	<u>462</u>
502.gcc_r	96	313	435	315	432	<u>313</u>	<u>434</u>	96	247	550	<u>247</u>	<u>550</u>	247	550
505.mcf_r	96	190	818	189	819	<u>189</u>	<u>819</u>	96	190	818	189	819	<u>189</u>	<u>819</u>
520.omnetpp_r	96	420	300	418	301	<u>419</u>	<u>300</u>	96	420	300	418	301	<u>419</u>	<u>300</u>
523.xalancbmk_r	96	133	760	<u>133</u>	<u>762</u>	133	764	96	133	760	<u>133</u>	<u>762</u>	133	764
525.x264_r	96	132	1280	133	1270	<u>132</u>	<u>1270</u>	96	129	1300	128	1310	<u>128</u>	<u>1310</u>
531.deepsjeng_r	96	242	455	<u>244</u>	<u>451</u>	244	450	96	242	455	<u>244</u>	<u>451</u>	244	450
541.leela_r	96	<u>399</u>	<u>398</u>	400	398	399	398	96	<u>399</u>	<u>398</u>	400	398	399	398
548.exchange2_r	96	<u>201</u>	<u>1250</u>	201	1250	202	1250	96	<u>201</u>	<u>1250</u>	201	1250	202	1250
557.xz_r	96	435	238	<u>436</u>	<u>238</u>	437	237	96	435	238	<u>436</u>	<u>238</u>	437	237

SPECrate®2017_int_base = 546

SPECrate®2017_int_peak = 566

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
tuned-adm profile was set to virtual-host using 'tuned-adm profile virtual-host'

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32"
MALLOCONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL340e Gen12

(2.70 GHz, Intel Xeon 6747P)

SPECrate®2017_int_base = 546

SPECrate®2017_int_peak = 566

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: May-2026

Software Availability: Mar-2026

General Notes (Continued)

is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS Configuration: Parameters are selected in the order shown below

Workload Profile set to General Throughput Compute

Enhanced Processor Performance Profile set to Aggressive

Thermal Configuration set to Maximum Cooling

Memory Patrol Scrubbing set to Disabled

Workload Profile set to Custom

Power Regulator set to OS Control Mode

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost Tue Jun 23 05:08:22 2026

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.27+suse.167.g130293e510)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

```
1. uname -a
Linux localhost 6.4.0-150700.53.6-default #1 SMP PREEMPT_DYNAMIC Tue Jul 1 14:54:47 UTC 2025 (8ab7501)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
05:08:22 up 5:08, 2 users, load average: 2.08, 43.86, 73.89
USER      TTY      FROM          LOGIN@      IDLE        JCPU   PCPU WHAT
root      pts/0    172.16.0.100  00:07       6.00s      2.12s  0.05s -bash
```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL340e Gen12

(2.70 GHz, Intel Xeon 6747P)

SPECrate®2017_int_base = 546

SPECrate®2017_int_peak = 566

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: May-2026

Software Availability: Mar-2026

Platform Notes (Continued)

3. Username

From environment variable \$USER: root

4. ulimit -a

```
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 1030567
max locked memory       (kbytes, -l) 8192
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 1030567
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@pts/0
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=96 -c
ic2026.0-lin-graniterapids-rate-20260429.cfg --define smt-on --define cores=48 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --reportable -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=96 --configfile
ic2026.0-lin-graniterapids-rate-20260429.cfg --define smt-on --define cores=48 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --reportable --output_format all
--nopower --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.006/templots/preenv.intrate.006.0.log --lognum 006.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) 6747P
vendor_id      : GenuineIntel
cpu family     : 6
model          : 173
stepping       : 1
microcode      : 0x1000411
bugs           : spectre_v1 spectre_v2 spec_store_bypass swaps bhi
cpu cores      : 48
siblings       : 96
1 physical ids (chips)
96 processors (hardware threads)
physical id 0: core ids 0-23,64-87
physical id 0: apicids 0-47,128-175
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL340e Gen12

(2.70 GHz, Intel Xeon 6747P)

SPECrate®2017_int_base = 546

SPECrate®2017_int_peak = 566

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: May-2026

Software Availability: Mar-2026

Platform Notes (Continued)

7. lscpu

From lscpu from util-linux 2.40.4:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	46 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	96
On-line CPU(s) list:	0-95
Vendor ID:	GenuineIntel
Model name:	Intel(R) Xeon(R) 6747P
CPU family:	6
Model:	173
Thread(s) per core:	2
Core(s) per socket:	48
Socket(s):	1
Stepping:	1
Frequency boost:	enabled
CPU(s) scaling MHz:	100%
CPU max MHz:	2701.0000
CPU min MHz:	800.0000
BogoMIPS:	5400.00
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts hfi vnmi avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile amx_int8 flush_l1d arch_capabilities
Virtualization:	VT-x
L1d cache:	2.3 MiB (48 instances)
L1i cache:	3 MiB (48 instances)
L2 cache:	96 MiB (48 instances)
L3 cache:	288 MiB (1 instance)
NUMA node(s):	2
NUMA node0 CPU(s):	0-23,48-71
NUMA node1 CPU(s):	24-47,72-95
Vulnerability Gather data sampling:	Not affected
Vulnerability Indirect target selection:	Not affected
Vulnerability Itlb multihit:	Not affected
Vulnerability L1tf:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Mmio stale data:	Not affected
Vulnerability Reg file data sampling:	Not affected

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL340e Gen12

(2.70 GHz, Intel Xeon 6747P)

SPECrate®2017_int_base = 546

SPECrate®2017_int_peak = 566

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: May-2026

Software Availability: Mar-2026

Platform Notes (Continued)

Vulnerability Retbleed:	Not affected
Vulnerability Spec rstack overflow:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:	Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced / Automatic IBRS; IBPB conditional; PBRSE-eIBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	2.3M	12	Data	1	64	1	64
L1i	64K	3M	16	Instruction	1	64	1	64
L2	2M	96M	16	Unified	2	2048	1	64
L3	288M	288M	16	Unified	3	294912	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 2 nodes (0-1)
node 0 cpus: 0-23,48-71
node 0 size: 128685 MB
node 0 free: 105946 MB
node 1 cpus: 24-47,72-95
node 1 size: 128988 MB
node 1 free: 107611 MB
node distances:
node  0  1
  0:  10  12
  1:  12  10
```

9. /proc/meminfo

MemTotal: 263858988 kB

10. who -r

run-level 3 Jun 23 00:00

11. Systemd service manager version: systemd 254 (254.27+suse.167.g130293e510)

Default Target	Status
multi-user	running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron display-manager getty@ issue-generator kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore tuned wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant
enabled-runtime	systemd-remount-fs
disabled	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon autofs autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables exchange-bmc-os-info firewalld fsidd gnome-remote-desktop gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievdt irqbalance issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nmb openvpn@ ostree-remount ostree-state-overlay@ rpcbind rpmconfigcheck rsyncd rtkit-daemon samba-bgqd serial-getty@ smartd-generate_opts

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL340e Gen12

(2.70 GHz, Intel Xeon 6747P)

SPECrate®2017_int_base = 546

SPECrate®2017_int_peak = 566

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: May-2026

Software Availability: Mar-2026

Platform Notes (Continued)

indirect
smb snmpd snmptrapd speech-dispatcherd systemd-boot-check-no-failures systemd-confext
systemd-network-generator systemd-sysextd systemd-time-wait-sync systemd-timesyncd udisks2
update-system-flatpaks upower vncserver@ wpa_supplicant@
pcscd saned@ systemd-userdbd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=/boot/vmlinuz-6.4.0-150700.53.6-default
root=UUID=282b0587-97e1-413a-84ab-5f19f75f0318
splash=silent
mitigations=auto
quiet
security=apparmor

14. cpupower frequency-info

analyzing CPU 65:
current policy: frequency should be within 800 MHz and 2.70 GHz.
The governor "performance" may decide which speed to use
within this range.
boost state support:
Supported: yes
Active: yes

15. tuned-adm active

Current active profile: virtual-host

16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	5
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	40
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	10
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	0

17. /sys/kernel/mm/transparent_hugepage

defrag always defer defer+madvice [madvice] never
enabled [always] madvice never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

18. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs 60000

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL340e Gen12

(2.70 GHz, Intel Xeon 6747P)

SPECrate®2017_int_base = 546

SPECrate®2017_int_peak = 566

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: May-2026

Software Availability: Mar-2026

Platform Notes (Continued)

```
defrag                1
max_ptes_none         511
max_ptes_shared       256
max_ptes_swap         64
pages_to_scan         4096
scan_sleep_millisecs  10000
```

```
-----
19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP7
-----
```

```
-----
20. Disk information
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       btrfs 5.9T  314G  5.6T   6% /home
-----
```

```
-----
21. /sys/devices/virtual/dmi/id
Vendor:      HPE
Product:     HPE ProLiant Compute DL340e Gen12
Product Family: ProLiant
Serial:      VPR069-P02-04U-H
-----
```

```
-----
22. dmidecode
Additional information from dmidecode 3.6 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  7x Hynix HMC88AHBRA471N 32 GB 2 rank 6400
  1x Micron MTC20F2085S1RC64BD2 QSFF 32 GB 2 rank 6400
-----
```

```
-----
23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      HPE
BIOS Version:     1.62
BIOS Date:        02/06/2026
BIOS Revision:    1.62
Firmware Revision: 1.22
-----
```

Compiler Version Notes

```
=====
C      | 502.gcc_r(peak)
-----
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.2.1 Build 20240711
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

```
=====
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
      | 557.xz_r(base, peak)
-----
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2026.0.0 Build 20260331

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL340e Gen12

(2.70 GHz, Intel Xeon 6747P)

SPECrate®2017_int_base = 546

SPECrate®2017_int_peak = 566

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: May-2026

Software Availability: Mar-2026

Compiler Version Notes (Continued)

Copyright (C) 1985-2026 Intel Corporation. All rights reserved.

=====
C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.2.1 Build 20240711

Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====
C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2026.0.0 Build 20260331

Copyright (C) 1985-2026 Intel Corporation. All rights reserved.

=====
C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
| 541.leela_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2026.0.0 Build 20260331

Copyright (C) 1985-2026 Intel Corporation. All rights reserved.

=====
Fortran | 548.exchange2_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2026.0.0 Build 20260331

Copyright (C) 1985-2026 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64

502.gcc_r: -DSPEC_LP64

505.mcf_r: -DSPEC_LP64

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL340e Gen12

(2.70 GHz, Intel Xeon 6747P)

SPECrate®2017_int_base = 546

SPECrate®2017_int_peak = 566

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: May-2026

Software Availability: Mar-2026

Base Portability Flags (Continued)

520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xgraniterapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/intel-compilers/compiler/latest/lib -lqkmallo

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xgraniterapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-fdelayed-template-parsing
-L/home/specdev/intel-compilers/compiler/latest/lib -lqkmallo

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xgraniterapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/home/specdev/intel-compilers/compiler/latest/lib -lqkmallo

Peak Compiler Invocation

C benchmarks (except as noted below):

icx

502.gcc_r: icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL340e Gen12

(2.70 GHz, Intel Xeon 6747P)

SPECrate®2017_int_base = 546

SPECrate®2017_int_peak = 566

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: May-2026

Software Availability: Mar-2026

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -w -std=c11 -m64 -Wl, -z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow -fno-strict-aliasing
-L/home/specdev/intel-compilers/compiler/latest/lib
-lqkmallocc

502.gcc_r: -m32 -L/home/specdev/intel-compilers/compiler/2024.2/lib32
-std=gnu89 -Wl, -z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl, -z,muldefs -xgraniterapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/home/specdev/intel-compilers/compiler/latest/lib
-lqkmallocc

557.xz_r: basepeak = yes

C++ benchmarks:

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL340e Gen12

(2.70 GHz, Intel Xeon 6747P)

SPECrate®2017_int_base = 546

SPECrate®2017_int_peak = 566

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2026

Hardware Availability: May-2026

Software Availability: Mar-2026

Peak Optimization Flags (Continued)

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-GNR-rev1.8.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2026-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-GNR-rev1.8.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2026-official-linux64.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2026-06-22 19:38:21-0400.

Report generated on 2026-07-29 10:29:37 by CPU2017 PDF formatter v6716.

Originally published on 2026-07-28.