



# SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS X215c M8  
(AMD EPYC 9575F 3.30 GHz Processor)

SPECrate®2026\_int\_base = 630

SPECrate®2026\_int\_peak = 630

CPU2026 License: 9019

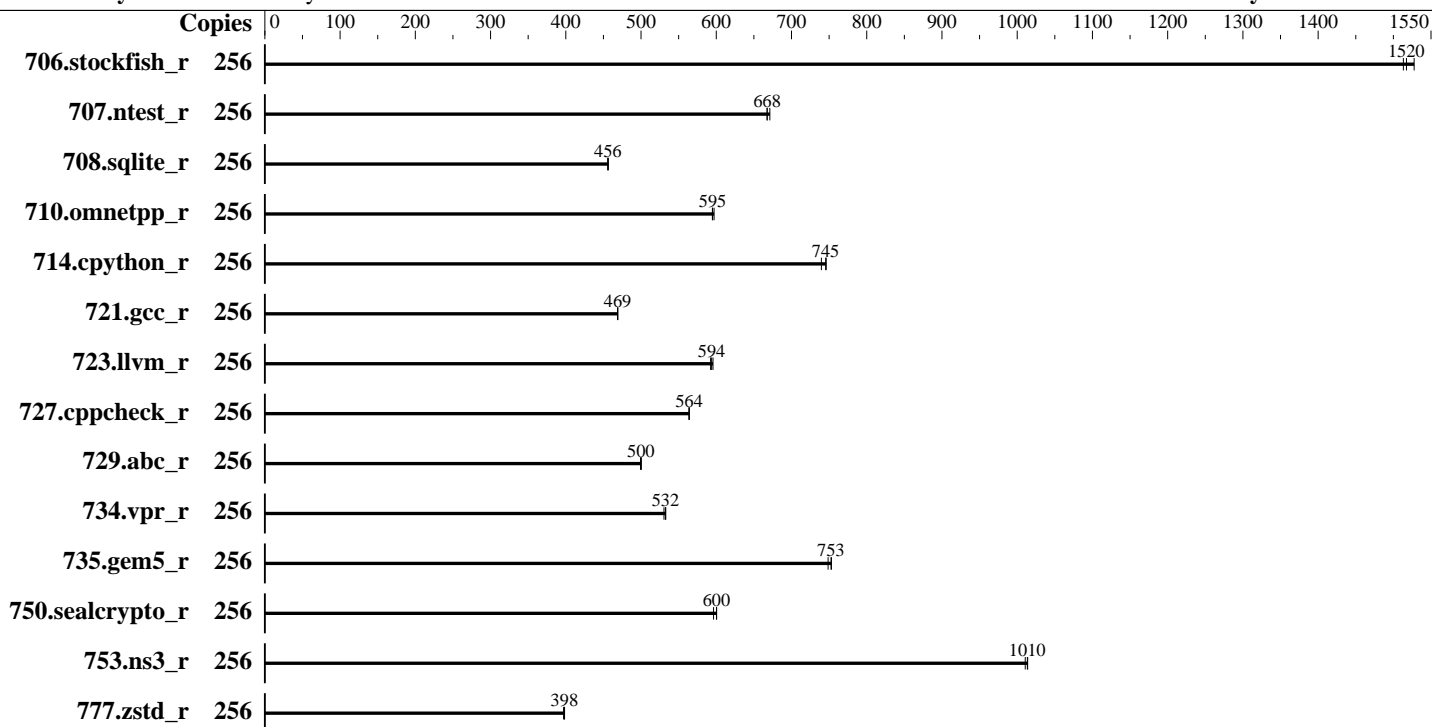
Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Feb-2026

Hardware Availability: Feb-2025

Software Availability: Jan-2026



### Hardware

CPU Name: AMD EPYC 9575F  
 Max MHz: 5000  
 Nominal: 3300  
 Enabled: 128 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 256 MB I+D on chip per chip, 32 MB shared / 8 cores  
 Other: None  
 Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R, running at 5600)  
 Storage: 960 GB M.2 SATA SSD  
 Cooling: Air  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP6  
 kernel version 6.4.0-150600.21-default  
 C/C++/Fortran: Version 5.1.0 of AOCC  
 Vendor  
 Compiler:  
 Compiler Category: Vendor  
 Firmware: Version 4.3.6a released Mar-2025  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



# SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS X215c M8  
(AMD EPYC 9575F 3.30 GHz Processor)

SPECrate®2026\_int\_base = 630

SPECrate®2026\_int\_peak = 630

**CPU2026 License:** 9019  
**Test Sponsor:** Cisco Systems  
**Tested by:** Cisco Systems

**Test Date:** Feb-2026  
**Hardware Availability:** Feb-2025  
**Software Availability:** Jan-2026

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
706.stockfish_r	256	<b>213</b>	<b>1520</b>	213	1510	211	1530	256	<b>213</b>	<b>1520</b>	213	1510	211	1530
707.ntest_r	256	226	671	<b>227</b>	<b>668</b>	227	668	256	226	671	<b>227</b>	<b>668</b>	227	668
708.sqlite_r	256	296	456	<b>296</b>	<b>456</b>	297	456	256	296	456	<b>296</b>	<b>456</b>	297	456
710.omnetpp_r	256	209	595	208	597	<b>209</b>	<b>595</b>	256	209	595	208	597	<b>209</b>	<b>595</b>
714.cpython_r	256	164	746	166	740	<b>165</b>	<b>745</b>	256	164	746	166	740	<b>165</b>	<b>745</b>
721.gcc_r	256	<b>374</b>	<b>469</b>	374	469	375	469	256	<b>374</b>	<b>469</b>	374	469	375	469
723.llvm_r	256	<b>219</b>	<b>594</b>	218	596	219	592	256	<b>219</b>	<b>594</b>	218	596	219	592
727.cppcheck_r	256	163	564	163	564	<b>163</b>	<b>564</b>	256	163	564	163	564	<b>163</b>	<b>564</b>
729.abc_r	256	235	500	<b>235</b>	<b>500</b>	235	499	256	235	500	<b>235</b>	<b>500</b>	235	499
734.vpr_r	256	221	533	<b>222</b>	<b>532</b>	222	530	256	221	533	<b>222</b>	<b>532</b>	222	530
735.gem5_r	256	166	753	167	749	<b>166</b>	<b>753</b>	256	166	753	167	749	<b>166</b>	<b>753</b>
750.sealcrypto_r	256	230	596	<b>229</b>	<b>600</b>	229	600	256	230	596	<b>229</b>	<b>600</b>	229	600
753.ns3_r	256	<b>155</b>	<b>1010</b>	155	1010	155	1010	256	<b>155</b>	<b>1010</b>	155	1010	155	1010
777.zstd_r	256	414	398	<b>414</b>	<b>398</b>	415	397	256	414	398	<b>414</b>	<b>398</b>	415	397

SPECrate®2026\_int\_base = **630**

SPECrate®2026\_int\_peak = **630**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.

(Continued on next page)



# SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS X215c M8  
(AMD EPYC 9575F 3.30 GHz Processor)

SPECrate®2026\_int\_base = 630

SPECrate®2026\_int\_peak = 630

**CPU2026 License:** 9019

**Test Sponsor:** Cisco Systems

**Tested by:** Cisco Systems

**Test Date:** Feb-2026

**Hardware Availability:** Feb-2025

**Software Availability:** Jan-2026

## Operating System Notes (Continued)

To free node-local memory and avoid remote memory usage,  
 'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
 To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
 To disable address space layout randomization (ASLR) to reduce run-to-run  
 variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.  
 To enable Transparent Hugepages (THP) for all allocations,  
 'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
 'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
 LD\_LIBRARY\_PATH =  
 "/home/cpu2026.902/amd\_rate\_aocc510\_znver5\_A\_lib/lib:/home/cpu2026.902/a  
 md\_rate\_aocc510\_znver5\_A\_lib/lib32:"  
 MALLOC\_CONF = "retain:true"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC Venice256 CPU + 2TiB Memory using Ubuntu 24.04

## Platform Notes

BIOS Settings:  
 NUMA nodes per socket set to NPS2  
 Determinism Slider set to Powerbled  
 Enhanced CPU Performance set to Auto

Sysinfo program /home/cpu2026.902/bin/sysinfo  
 Rev: 069f95da7e7f5d81b2ce48a82150e54f  
 running on sles15sp6nvme Tue Feb 3 19:52:57 2026

SUT (System Under Test) info as seen by some common utilities.

-----  
 Table of contents  
 -----

1. uname -srvm
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry

(Continued on next page)



# SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS X215c M8  
(AMD EPYC 9575F 3.30 GHz Processor)

SPECrate®2026\_int\_base = 630

SPECrate®2026\_int\_peak = 630

**CPU2026 License:** 9019  
**Test Sponsor:** Cisco Systems  
**Tested by:** Cisco Systems

**Test Date:** Feb-2026  
**Hardware Availability:** Feb-2025  
**Software Availability:** Jan-2026

### Platform Notes (Continued)

- 6. /proc/cpuinfo
- 7. lscpu
- 8. numactl --hardware
- 9. /proc/meminfo
- 10. who -r
- 11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
- 12. Failed units, from systemctl list-units --state=failed
- 13. Services, from systemctl list-unit-files
- 14. Linux kernel boot-time arguments, from /proc/cmdline
- 15. cpupower frequency-info
- 16. tuned-adm active
- 17. sysctl
- 18. /sys/kernel/mm/transparent\_hugepage
- 19. /sys/kernel/mm/transparent\_hugepage/khugepaged
- 20. OS release
- 21. Disk information
- 22. /sys/devices/virtual/dmi/id
- 23. dmidecode
- 24. BIOS

```
-----
1. uname -srvm
Linux 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09) x86_64
```

```
-----
2. w
19:52:57 up 1 min, 1 user, load average: 0.47, 0.22, 0.08
USER      TTY      FROM          LOGIN@      IDLE       JCPU      PCPU      WHAT
root      tty2    -             19:52      17.00s    1.07s    0.12s    /bin/bash ./amd_rate_aocc510_znver5_A1.sh
```

```
-----
3. Username
From environment variable $USER: root
```

```
-----
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size                (blocks, -f) unlimited
pending signals         (-i) 6190375
max locked memory       (kbytes, -l) 2097152
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
```

(Continued on next page)



# SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS X215c M8  
(AMD EPYC 9575F 3.30 GHz Processor)

SPECrate®2026\_int\_base = 630

SPECrate®2026\_int\_peak = 630

**CPU2026 License:** 9019  
**Test Sponsor:** Cisco Systems  
**Tested by:** Cisco Systems

**Test Date:** Feb-2026  
**Hardware Availability:** Feb-2025  
**Software Availability:** Jan-2026

### Platform Notes (Continued)

```
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 6190375
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

#### 5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
login -- root
-bash
python3 ./run_amd_rate_aocc510_znver5_A1.py
/bin/bash ./amd_rate_aocc510_znver5_A1.sh
runcpu --config amd_rate_aocc510_znver5_A1.cfg --tune base --reportable --iterations 3 intrate
runcpu --configfile amd_rate_aocc510_znver5_A1.cfg --tune base --reportable --iterations 3 --nopower
--runmode rate --tune base --size test:train:refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2026.002/templogs/preenv.intrate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2026.902
```

#### 6. /proc/cpuinfo

```
model name      : AMD EPYC 9575F 64-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 26
model          : 2
stepping       : 1
microcode      : 0xb00211e
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size      : 192 4K pages
cpu cores      : 64
siblings       : 128
2 physical ids (chips)
256 processors (hardware threads)
physical id 0: core ids 0-63
physical id 1: core ids 0-63
physical id 0: apicids 0-127
physical id 1: apicids 128-255
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

#### 7. lscpu

```
From lscpu from util-linux 2.39.3:
Architecture: x86_64
```

(Continued on next page)



# SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS X215c M8  
(AMD EPYC 9575F 3.30 GHz Processor)

SPECrate®2026\_int\_base = 630

SPECrate®2026\_int\_peak = 630

**CPU2026 License:** 9019  
**Test Sponsor:** Cisco Systems  
**Tested by:** Cisco Systems

**Test Date:** Feb-2026  
**Hardware Availability:** Feb-2025  
**Software Availability:** Jan-2026

### Platform Notes (Continued)

```

CPU op-mode(s):          32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 256
On-line CPU(s) list:   0-255
Vendor ID:              AuthenticAMD
BIOS Vendor ID:        Advanced Micro Devices, Inc.
Model name:             AMD EPYC 9575F 64-Core Processor
BIOS Model name:       AMD EPYC 9575F 64-Core Processor          Unknown CPU @ 3.3GHz
BIOS CPU family:       107
CPU family:             26
Model:                  2
Thread(s) per core:    2
Core(s) per socket:    64
Socket(s):              2
Stepping:               1
Frequency boost:       enabled
CPU(s) scaling MHz:    66%
CPU max MHz:            5008.0068
CPU min MHz:            1500.0000
BogoMIPS:               6590.79
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                        pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                        rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                        extd_apicid aperfmpperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                        sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
                        cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                        oswb ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                        perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
                        ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
                        smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                        xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                        cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
                        xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock
                        nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
                        pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi
                        avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
                        avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
                        movdiri movdir64b overflow_recov succor smca fsmr avx512_vp2intersect
                        flush_lld debug_swap
Virtualization:         AMD-V
L1d cache:              6 MiB (128 instances)
L1i cache:              4 MiB (128 instances)
L2 cache:               128 MiB (128 instances)
L3 cache:               512 MiB (16 instances)
NUMA node(s):          8

```

(Continued on next page)



# SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS X215c M8  
(AMD EPYC 9575F 3.30 GHz Processor)

SPECrate®2026\_int\_base = 630

SPECrate®2026\_int\_peak = 630

**CPU2026 License:** 9019  
**Test Sponsor:** Cisco Systems  
**Tested by:** Cisco Systems

**Test Date:** Feb-2026  
**Hardware Availability:** Feb-2025  
**Software Availability:** Jan-2026

### Platform Notes (Continued)

```

NUMA node0 CPU(s):          0-15,128-143
NUMA node1 CPU(s):          16-31,144-159
NUMA node2 CPU(s):          32-47,160-175
NUMA node3 CPU(s):          48-63,176-191
NUMA node4 CPU(s):          64-79,192-207
NUMA node5 CPU(s):          80-95,208-223
NUMA node6 CPU(s):          96-111,224-239
NUMA node7 CPU(s):          112-127,240-255
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit:       Not affected
Vulnerability L1tf:                 Not affected
Vulnerability Mds:                  Not affected
Vulnerability Meltdown:             Not affected
Vulnerability Mmio stale data:      Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed:             Not affected
Vulnerability Spec rstack overflow:  Not affected
Vulnerability Spec store bypass:    Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:           Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:           Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
always-on; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected
Vulnerability Srbds:                Not affected
Vulnerability Tsx async abort:      Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	6M	12	Data	1	64	1	64
L1i	32K	4M	8	Instruction	1	64	1	64
L2	1M	128M	16	Unified	2	1024	1	64
L3	32M	512M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0-15,128-143
node 0 size: 193112 MB
node 0 free: 192497 MB
node 1 cpus: 16-31,144-159
node 1 size: 193527 MB
node 1 free: 193013 MB
node 2 cpus: 32-47,160-175
node 2 size: 193527 MB
node 2 free: 192964 MB
node 3 cpus: 48-63,176-191
node 3 size: 193527 MB
node 3 free: 193066 MB

```

(Continued on next page)



# SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS X215c M8  
(AMD EPYC 9575F 3.30 GHz Processor)

SPECrate®2026\_int\_base = 630

SPECrate®2026\_int\_peak = 630

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Feb-2026

Hardware Availability: Feb-2025

Software Availability: Jan-2026

### Platform Notes (Continued)

```

node 4 cpus: 64-79,192-207
node 4 size: 193527 MB
node 4 free: 192959 MB
node 5 cpus: 80-95,208-223
node 5 size: 193488 MB
node 5 free: 192953 MB
node 6 cpus: 96-111,224-239
node 6 size: 193527 MB
node 6 free: 193061 MB
node 7 cpus: 112-127,240-255
node 7 size: 193391 MB
node 7 free: 192863 MB
node distances:
node  0  1  2  3  4  5  6  7
  0:  10 12 12 12 32 32 32 32
  1:  12 10 12 12 32 32 32 32
  2:  12 12 10 12 32 32 32 32
  3:  12 12 12 10 32 32 32 32
  4:  32 32 32 32 10 12 12 12
  5:  32 32 32 32 12 10 12 12
  6:  32 32 32 32 12 12 10 12
  7:  32 32 32 32 12 12 12 10

```

```

-----
9. /proc/meminfo
   MemTotal:          1584773132 kB

```

```

-----
10. who -r
    run-level 3 Feb 3 19:52 last=5

```

```

-----
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
    Default Target  Status
    graphical       degraded

```

```

-----
12. Failed units, from systemctl list-units --state=failed
    UNIT                                LOAD  ACTIVE SUB    DESCRIPTION
    * udisks2.service loaded failed failed Disk Manager

```

```

-----
13. Services, from systemctl list-unit-files
    STATE      UNIT FILES
    enabled    YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron
              display-manager getty@ irqbalance issue-generator kbdsettings klog lvm2-monitor nscd
              nvme-fc-boot-connections nvmmf-autoconnect postfix purge-kernels rollback rsyslog smartd

```

(Continued on next page)



# SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS X215c M8  
(AMD EPYC 9575F 3.30 GHz Processor)

SPECrate®2026\_int\_base = 630

SPECrate®2026\_int\_peak = 630

**CPU2026 License:** 9019

**Test Sponsor:** Cisco Systems

**Tested by:** Cisco Systems

**Test Date:** Feb-2026

**Hardware Availability:** Feb-2025

**Software Availability:** Jan-2026

### Platform Notes (Continued)

```

enabled-runtime  sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
disabled         systemd-remount-fs
                 accounts-daemon autofs autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl
                 ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell ebttables
                 exchange-bmc-os-info firewalld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievd
                 issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nmb
                 ostree-remount rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@
                 smartd_generate_opts smb snmpd snmptrapd speech-dispatcherd systemd-boot-check-no-failures
                 systemd-confext systemd-network-generator systemd-sysextd systemd-time-wait-sync
                 systemd-timesyncd tuned udisks2 update-system-flatpaks upower vncserver@
indirect        systemd-userdbd wickedd

```

```

-----
14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd2,gpt12)/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=deb13dd2-1372-4d2b-ac60-6c702361c648
splash=silent
mitigations=auto
quiet

```

```

-----
15. cpupower frequency-info
analyzing CPU 123:
    current policy: frequency should be within 1.50 GHz and 3.30 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.

    boost state support:
        Supported: yes
        Active: yes

```

```

-----
16. tuned-adm active
    No current active profile.

```

```

-----
17. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space     0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio     10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500

```

(Continued on next page)



# SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS X215c M8  
(AMD EPYC 9575F 3.30 GHz Processor)

SPECrate®2026\_int\_base = 630

SPECrate®2026\_int\_peak = 630

**CPU2026 License:** 9019  
**Test Sponsor:** Cisco Systems  
**Tested by:** Cisco Systems

**Test Date:** Feb-2026  
**Hardware Availability:** Feb-2025  
**Software Availability:** Jan-2026

### Platform Notes (Continued)

```

vm.min_unmapped_ratio      1
vm.nr_hugepages             0
vm.nr_hugepages_mempolicy   0
vm.nr_overcommit_hugepages  0
vm.swappiness               1
vm.watermark_boost_factor   15000
vm.watermark_scale_factor    10
vm.zone_reclaim_mode        1

```

```

-----
18. /sys/kernel/mm/transparent_hugepage
defrag      [always] defer defer+madvise madvise never
enabled     [always] madvise never
hpage_pmd_size  2097152
shmem_enabled  always within_size advise [never] deny force

```

```

-----
19. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none           511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096
scan_sleep_millisecs   10000

```

```

-----
20. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6

```

```

-----
21. Disk information
SPEC is set to: /home/cpu2026.902
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/nvme0n1p26 xfs   731G  114G  618G  16% /home

```

```

-----
22. /sys/devices/virtual/dmi/id
Vendor:      Cisco Systems Inc
Product:     UCSX-215C-M8
Product Family: Cisco UCS Rack Server
Serial:      FCH282172BL

```

```

-----
23. dmidecode
Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.

```

(Continued on next page)



# SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS X215c M8  
(AMD EPYC 9575F 3.30 GHz Processor)

SPECrate®2026\_int\_base = 630

SPECrate®2026\_int\_peak = 630

**CPU2026 License:** 9019  
**Test Sponsor:** Cisco Systems  
**Tested by:** Cisco Systems

**Test Date:** Feb-2026  
**Hardware Availability:** Feb-2025  
**Software Availability:** Jan-2026

### Platform Notes (Continued)

The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

24x 0xAD00 HMC94AGBRA181N 64 GB 2 rank 5600

#### 24. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Cisco Systems, Inc.  
BIOS Version: X215M8.4.3.6a.0.0317251004  
BIOS Date: 03/17/2025  
BIOS Revision: 5.35

### Compiler Version Notes

=====  
C | 708.sqlite\_r(base) 714.cpython\_r(base) 777.zstd\_r(base)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.1.0-Build#1994 2025\_12\_23)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.1.0/bin  
=====

=====  
C++ | 706.stockfish\_r(base) 707.ntest\_r(base) 727.cppcheck\_r(base)  
| 753.ns3\_r(base)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.1.0-Build#1994 2025\_12\_23)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.1.0/bin  
=====

=====  
C++, C | 710.omnetpp\_r(base) 721.gcc\_r(base) 723.llvm\_r(base) 729.abc\_r(base)  
| 734.vpr\_r(base) 735.gem5\_r(base) 750.sealcrypto\_r(base)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.1.0-Build#1994 2025\_12\_23)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.1.0/bin  
=====



# SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS X215c M8  
(AMD EPYC 9575F 3.30 GHz Processor)

SPECrate®2026\_int\_base = 630

SPECrate®2026\_int\_peak = 630

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Feb-2026

Hardware Availability: Feb-2025

Software Availability: Jan-2026

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Benchmarks using both C and C++:

clang++ clang

## Base Portability Flags

706.stockfish\_r: -DSPEC\_LP64  
707.ntest\_r: -DSPEC\_LP64  
708.sqlite\_r: -DSPEC\_LP64  
710.omnetpp\_r: -DSPEC\_LP64  
714.cpython\_r: -DSPEC\_LP64  
721.gcc\_r: -DSPEC\_LP64  
723.llvm\_r: -DSPEC\_LP64  
727.cppcheck\_r: -DSPEC\_LP64  
729.abc\_r: -DSPEC\_LP64  
734.vpr\_r: -DSPEC\_LP64  
735.gem5\_r: -DSPEC\_LP64  
750.sealcrypto\_r: -DSPEC\_LP64  
753.ns3\_r: -DSPEC\_LP64  
777.zstd\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -std=c18 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather  
-Wl,-mllvm -Wl,-extra-inliner -O3 -march=znver5 -fveclib=AMDLIBM  
-fno-PIE -no-pie -flto -fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang  
-lamdalloc

C++ benchmarks:

-m64 -std=c++17 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5  
-fveclib=AMDLIBM -flto -mllvm -unroll-threshold=100

(Continued on next page)



# SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS X215c M8  
(AMD EPYC 9575F 3.30 GHz Processor)

SPECrate®2026\_int\_base = 630

SPECrate®2026\_int\_peak = 630

**CPU2026 License:** 9019

**Test Sponsor:** Cisco Systems

**Tested by:** Cisco Systems

**Test Date:** Feb-2026

**Hardware Availability:** Feb-2025

**Software Availability:** Jan-2026

## Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -fno-PIE -no-pie
-fvirtual-function-elimination -fvisibility=hidden -lamdlibm -lflang
-lamdalloc
```

Benchmarks using both C and C++:

```
-m64 -std=c++17 -std=c18 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIBM -fno-PIE -no-pie -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -fvirtual-function-elimination
-fvisibility=hidden -lamdlibm -lflang -lamdalloc
```

## Peak Optimization Flags

C benchmarks:

708.sqlite\_r: basepeak = yes

714.cpython\_r: basepeak = yes

777.zstd\_r: basepeak = yes

C++ benchmarks:

706.stockfish\_r: basepeak = yes

707.ntest\_r: basepeak = yes

727.cppcheck\_r: basepeak = yes

753.ns3\_r: basepeak = yes

Benchmarks using both C and C++:

710.omnetpp\_r: basepeak = yes

721.gcc\_r: basepeak = yes

723.llvm\_r: basepeak = yes

(Continued on next page)



# SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS X215c M8  
(AMD EPYC 9575F 3.30 GHz Processor)

SPECrate®2026\_int\_base = 630

SPECrate®2026\_int\_peak = 630

**CPU2026 License:** 9019

**Test Sponsor:** Cisco Systems

**Tested by:** Cisco Systems

**Test Date:** Feb-2026

**Hardware Availability:** Feb-2025

**Software Availability:** Jan-2026

## Peak Optimization Flags (Continued)

729.abc\_r: basepeak = yes

734.vpr\_r: basepeak = yes

735.gem5\_r: basepeak = yes

750.sealcrypto\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2026/results/flags/aocc-flags.2026-05-04.00.html>

<http://www.spec.org/cpu2026/results/flags/Cisco-Platform-Settings-AMD-Turin-v1.1-revG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2026/results/flags/aocc-flags.2026-05-04.00.xml>

<http://www.spec.org/cpu2026/results/flags/Cisco-Platform-Settings-AMD-Turin-v1.1-revG.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2026 v0.902.0 on 2026-02-03 22:52:56-0500.

Report generated on 2026-05-11 16:35:46 by CPU2026 PDF formatter (unknown).

Originally published on 2026-05-05.