



SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2026_int_base = 329

PowerEdge R570 (Intel Xeon 6787P)

SPECrate®2026_int_peak = 346

CPU2026 License: 6573

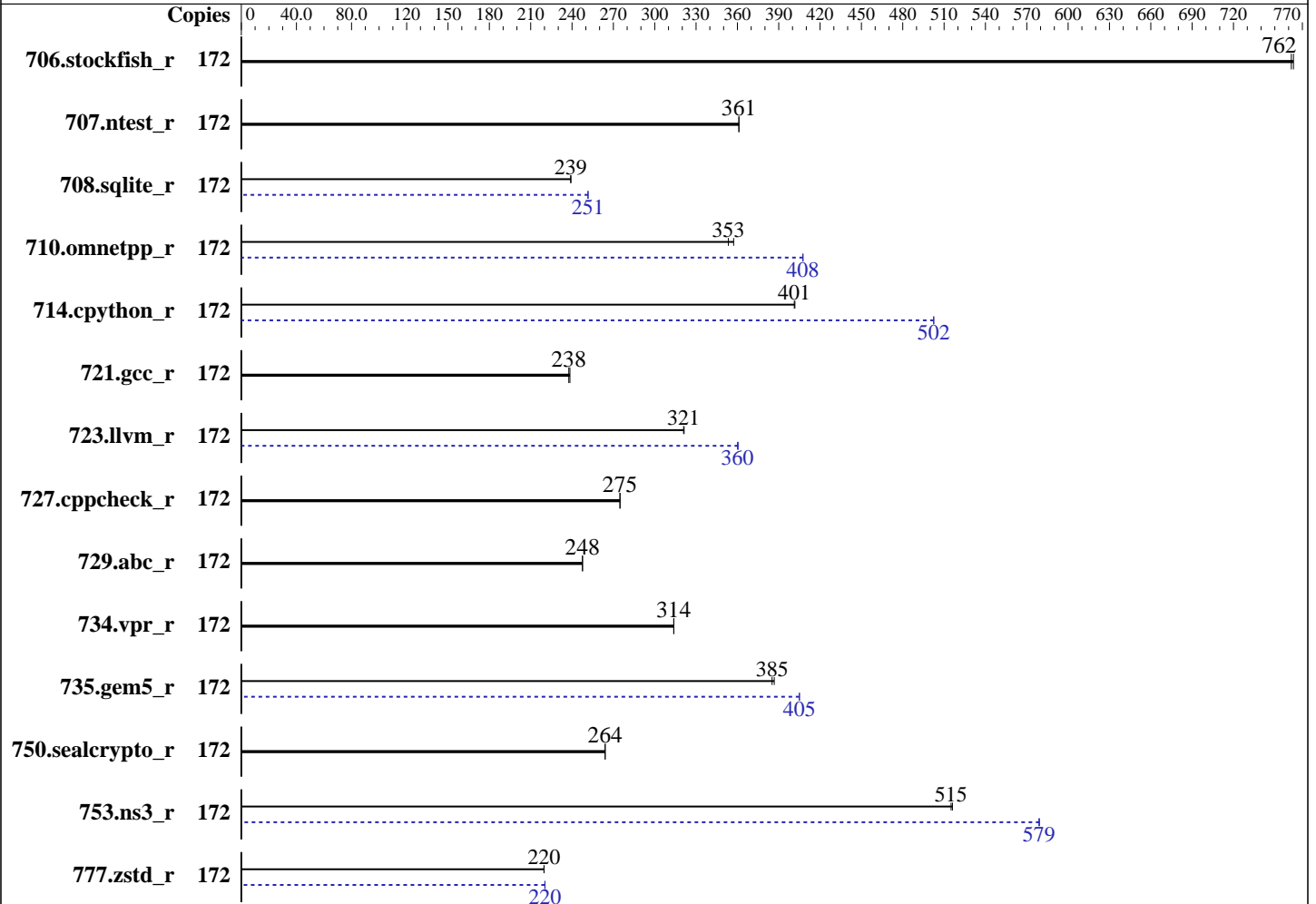
Test Date: Feb-2026

Test Sponsor: Dell Inc.

Hardware Availability: Mar-2025

Tested by: Dell Inc.

Software Availability: Nov-2025



Hardware

CPU Name: Intel Xeon 6787P
 Max MHz: 3800
 Nominal: 2000
 Enabled: 86 cores, 1 chip, 2 threads/core
 Orderable: 1 chip
 Cache L1: 64 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 336 MB I+D on chip per chip
 Other: None
 Memory: 512 GB (8 x 64 GB 2Rx4 PC5-6400B-R)
 Storage: 120 GB on tmpfs
 Cooling: Air
 Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP6
 6.4.0-150600.21-default
 Compiler: C/C++: Version 2025.3 of Intel oneAPI DPC++/C++
 Compiler for Linux;
 Fortran: Version 2025.3 of Intel Fortran
 Compiler for Linux
 Compiler Category: Vendor
 Firmware: Version 1.6.4 released Nov-2025
 File System: tmpfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: None
 Power Management: BIOS set to prefer performance at the cost of
 additional power usage.



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
706.stockfish_r	172	284	764	284	762			172	284	764	284	762		
707.ntest_r	172	282	361	282	361			172	282	361	282	361		
708.sqlite_r	172	380	239	380	239			172	361	252	361	251		
710.omnetpp_r	172	234	357	237	353			172	205	408	205	408		
714.cpython_r	172	205	402	205	401			172	164	502	164	503		
721.gcc_r	172	495	238	497	238			172	495	238	497	238		
723.llvm_r	172	272	321	271	321			172	242	360	242	361		
727.cppcheck_r	172	225	275	225	275			172	225	275	225	275		
729.abc_r	172	319	248	319	248			172	319	248	319	248		
734.vpr_r	172	253	314	253	314			172	253	314	253	314		
735.gem5_r	172	217	387	217	385			172	207	405	207	405		
750.sealcrypto_r	172	349	264	349	264			172	349	264	349	264		
753.ns3_r	172	204	516	205	515			172	182	579	182	579		
777.zstd_r	172	504	220	504	220			172	503	220	503	220		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2026-RC2/lib"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using CentOS Stream 9.
Transparent Huge Pages enabled by default

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General Notes (Continued)

Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
 sync; echo 3> /proc/sys/vm/drop_caches
 runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>
 Benchmark run from a 120 GB ramdisk created with the cmd: "mount -t tmpfs -o size=120G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:

Sub NUMA Cluster : Enabled

System Profile : Custom

CPU Power Management : Maximum Performance

C-States : Autonomous

Latency Optimized Mode : Enabled

Energy Efficient Policy : Performance

Sysinfo program /mnt/ramdisk/cpu2026-RC2/bin/sysinfo

Rev: 069f95da7e7f5d81b2ce48a82150e54f

running on XRZT94-R570 Wed Feb 4 11:03:40 2026

SUT (System Under Test) info as seen by some common utilities.

 Table of contents

1. uname -srvm
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. sysctl
15. /sys/kernel/mm/transparent_hugepage
16. /sys/kernel/mm/transparent_hugepage/khugepaged
17. OS release
18. Disk information

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Platform Notes (Continued)

19. /sys/devices/virtual/dmi/id
20. dmidecode
21. BIOS

1. uname -srvm
Linux 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09) x86_64

2. w
11:03:40 up 8 min, 1 user, load average: 0.40, 0.13, 0.04
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root tty1 - 10:55 39.00s 1.11s 0.00s /bin/bash
/home/DellFiles/bin/Intel/dell-run-specpcpu.sh rate --define DL-VERS=7.0_T01 --output_format html,pdf,txt

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 2060626
max locked memory (kbytes, -l) 8192
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 2060626
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=31
login -- root
-bash
/bin/bash /home/DellFiles/bin/DELL_rate.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate

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Platform Notes (Continued)

```

/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-VERS=7.0_T01 --output_format
html,pdf,txt
/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-VERS=7.0_T01 --output_format
html,pdf,txt
runcpu --nobuild --reportable --action validate --define default-platform-flags --copies 172 -c
ic2025.3-graniterapids-cpu2026-0.902-rate-20260121.cfg --define smt-on --define cores=86 --define
physicalfirst --define invoke_with_interleave --define drop_caches --tune base,peak -o all --iterations 2
--define DL-VERS=7.0_T01 --output_format html,pdf,txt intrate
runcpu --nobuild --reportable --action validate --define default-platform-flags --copies 172 --configfile
ic2025.3-graniterapids-cpu2026-0.902-rate-20260121.cfg --define smt-on --define cores=86 --define
physicalfirst --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all
--iterations 2 --define DL-VERS=7.0_T01 --output_format html,pdf,txt --nopower --runmode rate --tune
base:peak --size refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2026.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2026-RC2

```

6. /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) 6787P
vendor_id       : GenuineIntel
cpu family      : 6
model           : 173
stepping        : 1
microcode       : 0x10003f3
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores       : 86
siblings        : 172
1 physical ids (chips)
172 processors (hardware threads)
physical id 0:  core ids 0-42,64-106
physical id 0:  apicids 0-85,128-213

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                172
On-line CPU(s) list:   0-171
Vendor ID:             GenuineIntel
BIOS Vendor ID:       Intel

```

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Platform Notes (Continued)

```

Model name: Intel(R) Xeon(R) 6787P
BIOS Model name: Intel(R) Xeon(R) 6787P CPU @ 2.0GHz
BIOS CPU family: 179
CPU family: 6
Model: 173
Thread(s) per core: 2
Core(s) per socket: 86
Socket(s): 1
Stepping: 1
BogoMIPS: 4000.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
nopl xtopology nonstop_tsc cpuid aperfperf tsc_known_freq pni
pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2
ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid
rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
arat pln pts vnmi avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni
vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57
rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear
serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile
amx_int8 flush_lld arch_capabilities

Virtualization: VT-x
L1d cache: 4 MiB (86 instances)
L1i cache: 5.4 MiB (86 instances)
L2 cache: 172 MiB (86 instances)
L3 cache: 336 MiB (1 instance)
NUMA node(s): 2
NUMA node0 CPU(s): 0-42,86-128
NUMA node1 CPU(s): 43-85,129-171
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl

```

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Platform Notes (Continued)

Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBR SB-eIBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	4M	12	Data	1	64	1	64
L1i	64K	5.4M	16	Instruction	1	64	1	64
L2	2M	172M	16	Unified	2	2048	1	64
L3	336M	336M	16	Unified	3	344064	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 2 nodes (0-1)
node 0 cpus: 0-42,86-128
node 0 size: 257559 MB
node 0 free: 256625 MB
node 1 cpus: 43-85,129-171
node 1 size: 257622 MB
node 1 free: 229951 MB
node distances:
node  0  1
  0: 10 12
  1: 12 10
```

9. /proc/meminfo

MemTotal: 527546692 kB

10. who -r

run-level 3 Feb 4 10:55

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

```
Default Target Status
multi-user      running
```

12. Services, from systemctl list-unit-files

```
STATE UNIT FILES
enabled  apparmor auditd cron firewalld getty@ irqbalance issue-generator kbdsettings kdump
kdump-early kdump-notify nvme-fc-boot-connections nvme-autoconnect postfix purge-kernels
rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6
```

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Platform Notes (Continued)

```

wickedd-nanny
enabled-runtime systemd-remount-fs
disabled boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell ebttables fsidd
grub2-once haveged issue-add-ssh-keys kexec-load lunmask nfs nfs-blkmap rpcbind
rpmconfigcheck rsyncd serial-getty@ systemd-boot-check-no-failures systemd-confext
systemd-network-generator systemd-sysextd systemd-time-wait-sync systemd-timesyncd
indirect systemd-userdbd wickedd

```

13. Linux kernel boot-time arguments, from /proc/cmdline

```

BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=a8a19c3c-99a5-4525-a09c-4861b1a9607b
splash=silent
resume=/dev/disk/by-uuid/28e7a6f8-9664-47dc-9c65-d94db27aeb03
mitigations=auto
quiet
security=apparmor
crashkernel=324M,high
crashkernel=72M,low

```

14. sysctl

```

kernel.numa_balancing 1
kernel.randomize_va_space 2
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 20
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 60
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 0

```

15. /sys/kernel/mm/transparent_hugepage

```

defrag always defer defer+madvise [madvise] never
enabled [always] madvise never
hpage_pmd_size 2097152

```

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Platform Notes (Continued)

shmem_enabled always within_size advise [never] deny force

```

-----
16. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000

```

```

-----
17. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6

```

```

-----
18. Disk information
SPEC is set to: /mnt/ramdisk/cpu2026-RC2
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 120G 13G 108G 11% /mnt/ramdisk

```

```

-----
19. /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge R570
Product Family: PowerEdge
Serial: 7XRZT94

```

```

-----
20. dmidecode
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
2x 00AD042300AD HMC94AHBRA480N 64 GB 2 rank 6400
6x 00AD063200AD HMC94AHBRA277N 64 GB 2 rank 6400

```

```

-----
21. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: Dell Inc.
BIOS Version: 1.6.4
BIOS Date: 11/02/2025

```

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Platform Notes (Continued)

BIOS Revision: 1.6

Compiler Version Notes

```
=====  
C      | 708.sqlite_r(base, peak) 714.cpython_r(base, peak) 777.zstd_r(base,  
      | peak)  
-----
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2025.3.0 Build 20251010
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.

```
=====  
C++    | 706.stockfish_r(base, peak) 707.ntest_r(base, peak)  
      | 727.cppcheck_r(base, peak) 753.ns3_r(base, peak)  
-----
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
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Copyright (C) 1985-2025 Intel Corporation. All rights reserved.

```
=====  
C++, C | 710.omnetpp_r(base, peak) 721.gcc_r(base, peak) 723.llvm_r(base,  
      | peak) 729.abc_r(base, peak) 734.vpr_r(base, peak) 735.gem5_r(base,  
      | peak) 750.sealcrypto_r(base, peak)  
-----
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
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Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Benchmarks using both C and C++:
icpx icx



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Base Portability Flags

706.stockfish_r: -DSPEC_LP64
707.ntest_r: -DSPEC_LP64
708.sqlite_r: -DSPEC_LP64
710.omnetpp_r: -DSPEC_LP64
714.cpython_r: -DSPEC_LP64
721.gcc_r: -DSPEC_LP64
723.llvm_r: -DSPEC_LP64
727.cppcheck_r: -DSPEC_LP64
729.abc_r: -DSPEC_LP64
734.vpr_r: -DSPEC_LP64
735.gem5_r: -DSPEC_LP64
750.sealcrypto_r: -DSPEC_LP64
753.ns3_r: -DSPEC_LP64
777.zstd_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -std=c18 -Wl,-z,muldefs -xgraniterapids
-mprefer-vector-width=512 -O3 -ffp-model=fast -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2025.3/lib -lqkmalloc

C++ benchmarks:

-m64 -std=c++17 -Wl,-z,muldefs -xgraniterapids
-mprefer-vector-width=512 -O3 -ffp-model=fast -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2025.3/lib -lqkmalloc

Benchmarks using both C and C++:

-m64 -std=c++17 -std=c18 -Wl,-z,muldefs -xgraniterapids
-mprefer-vector-width=512 -O3 -ffp-model=fast -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2025.3/lib -lqkmalloc

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

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Peak Compiler Invocation (Continued)

Benchmarks using both C and C++:

icpx icx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
-m64 -std=c18 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xHost(pass 1) -ffp-model=fast
-xgraniterapids(pass 2) -flto -mprefer-vector-width=512
-qopt-mem-layout-trans=4 -O3 -mfpmath=sse -funroll-loops
-L/opt/intel/oneapi/compiler/2025.3/lib -lqkmalloc
```

C++ benchmarks:

706.stockfish_r: basepeak = yes

707.ntest_r: basepeak = yes

727.cppcheck_r: basepeak = yes

```
753.ns3_r: -m64 -std=c++17 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xHost(pass 1)
-ffp-model=fast -xgraniterapids(pass 2) -flto
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -O3
-mfpmath=sse -funroll-loops
-L/opt/intel/oneapi/compiler/2025.3/lib -lqkmalloc
```

Benchmarks using both C and C++:

```
710.omnetpp_r: -m64 -std=c++17 -std=c18 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xHost(pass 1)
-ffp-model=fast -xgraniterapids(pass 2) -flto
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -O3
-mfpmath=sse -funroll-loops
```

(Continued on next page)



SPEC CPU®2026 Integer Rate Result

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Dell Inc.

SPECrate®2026_int_base = 329

PowerEdge R570 (Intel Xeon 6787P)

SPECrate®2026_int_peak = 346

CPU2026 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Feb-2026

Hardware Availability: Mar-2025

Software Availability: Nov-2025

Peak Optimization Flags (Continued)

710.omnetpp_r (continued):

-L/opt/intel/oneapi/compiler/2025.3/lib -lqkmalloc

721.gcc_r: basepeak = yes

723.llvm_r: Same as 710.omnetpp_r

729.abc_r: basepeak = yes

734.vpr_r: basepeak = yes

735.gem5_r: Same as 710.omnetpp_r

750.sealcrypto_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2026/results/flags/Intel-ic2025-official-linux64-cpu2026-0.902.html>

<http://www.spec.org/cpu2026/results/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.19.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2026/results/flags/Intel-ic2025-official-linux64-cpu2026-0.902.xml>

<http://www.spec.org/cpu2026/results/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.19.xml>

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