



SPEC CPU®2026 Floating Point Speed Result

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Dell Inc.

SPECspeed®2026_fp_base = 18.3

PowerEdge R7725 (AMD EPYC 9755 128-Core Processor)

SPECspeed®2026_fp_peak = 18.3

CPU2026 License: 6573

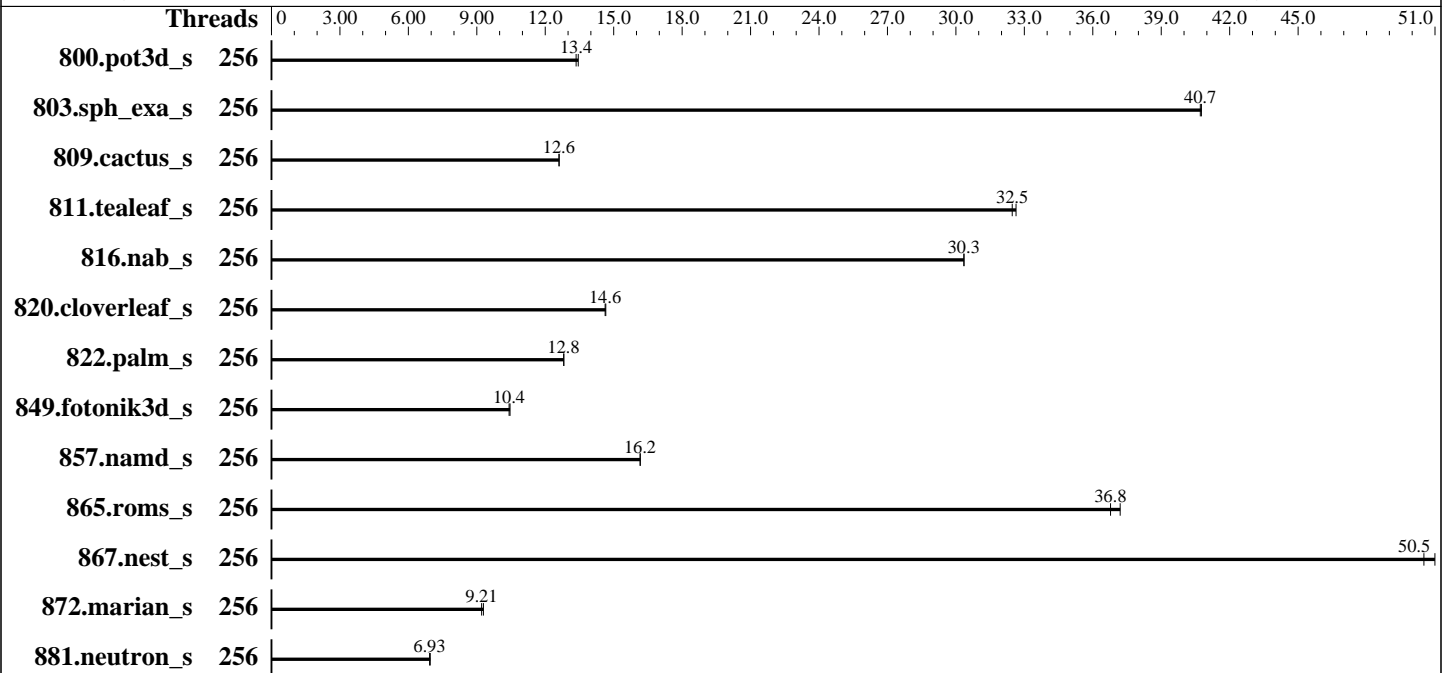
Test Date: Feb-2026

Test Sponsor: Dell Inc.

Hardware Availability: Nov-2025

Tested by: Dell Inc.

Software Availability: Jan-2026



Hardware

CPU Name: AMD EPYC 9755
 Max MHz: 4100
 Nominal: 2700
 Enabled: 256 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 512 MB I+D on chip per chip, 32 MB shared / 8 cores
 Other: None
 Memory: 3 TB (24 x 128 GB 2Rx4 PC5-6400B-R)
 Storage: 250 GB on tmpfs
 Cooling: Air
 Other: None

Software

OS: Ubuntu 24.04 LTS
 6.8.0-87-generic
 Compiler: C/C++: Version 5.1.0 of AOCC
 Fortran: Flang v22
 Compiler Category: Vendor
 Firmware: Version 1.5.3 released Oct-2025
 File System: tmpfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: None
 Power Management: BIOS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
800.pot3d_s	256	50.1	13.4	<u>50.4</u>	<u>13.4</u>			256	50.1	13.4	<u>50.4</u>	<u>13.4</u>		
803.sph_exa_s	256	<u>30.4</u>	<u>40.7</u>	30.4	40.8			256	<u>30.4</u>	<u>40.7</u>	30.4	40.8		
809.cactus_s	256	89.0	12.6	<u>89.1</u>	<u>12.6</u>			256	89.0	12.6	<u>89.1</u>	<u>12.6</u>		
811.tealeaf_s	256	17.1	32.6	<u>17.2</u>	<u>32.5</u>			256	17.1	32.6	<u>17.2</u>	<u>32.5</u>		
816.nab_s	256	37.1	30.4	<u>37.1</u>	<u>30.3</u>			256	37.1	30.4	<u>37.1</u>	<u>30.3</u>		
820.cloverleaf_s	256	<u>58.6</u>	<u>14.6</u>	58.5	14.7			256	<u>58.6</u>	<u>14.6</u>	58.5	14.7		
822.palm_s	256	95.8	12.8	<u>95.9</u>	<u>12.8</u>			256	95.8	12.8	<u>95.9</u>	<u>12.8</u>		
849.fotonik3d_s	256	<u>63.4</u>	<u>10.4</u>	63.1	10.5			256	<u>63.4</u>	<u>10.4</u>	63.1	10.5		
857.namd_s	256	<u>89.9</u>	<u>16.2</u>	89.8	16.2			256	<u>89.9</u>	<u>16.2</u>	89.8	16.2		
865.roms_s	256	<u>29.6</u>	<u>36.8</u>	29.3	37.2			256	<u>29.6</u>	<u>36.8</u>	29.3	37.2		
867.nest_s	256	42.4	51.0	<u>42.8</u>	<u>50.5</u>			256	42.4	51.0	<u>42.8</u>	<u>50.5</u>		
872.marian_s	256	116	9.29	<u>117</u>	<u>9.21</u>			256	116	9.29	<u>117</u>	<u>9.21</u>		
881.neutron_s	256	<u>118</u>	<u>6.93</u>	117	6.96			256	<u>118</u>	<u>6.93</u>	117	6.96		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>
Flang v22 is available at <https://flang.llvm.org/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,

(Continued on next page)



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Operating System Notes (Continued)

```
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.
To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
GOMP_CPU_AFFINITY = "0-255"
LD_LIBRARY_PATH =
  "/mnt/ramdisk/cpu2026rc2/amd_speed_aocc510_flang22_znver5_A_lib/lib:/mnt
  /ramdisk/cpu2026rc2/amd_speed_aocc510_flang22_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with an AMD EPYC 9754 CPU + 768 GiB Memory using Ubuntu 24.04
 Benchmark run from a 250 GB ramdisk created with the cmd: "mount -t tmpfs -o size=250G tmpfs /mnt/ramdisk"

Platform Notes

```
BIOS Settings:
  Virtualization Technology : Disabled
  NUMA Nodes Per Socket : 4

  System Profile : Custom
  C-States : Disabled
  Memory Patrol Scrub : Disabled
  PCI ASPM L1 Link Power Management : Disabled
  Periodic Directory Rinse Tuning : Blended
  Determinism Control : Manual
  Determinism Slider : Power Determinism
  Optimizer Mode : Enabled
  Algorithm Performance Boost Disable : Enabled
  ApbDis Fixed DF P-State : P0
  Dram Refresh Delay : Performance
  DIMM Self Healing -
  on Uncorrectable Memory Error : Disabled
```

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Platform Notes (Continued)

sysinfo program /mnt/ramdisk/cpu2026rc2/bin/sysinfo
Rev: 069f95da7e7f5d81b2ce48a82150e54f
running on SLR7736-R7725 Thu Feb 5 12:43:51 2026

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -srvm
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 255 (255.4-lubuntu8.8)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

```
1. uname -srvm
Linux 6.8.0-87-generic #88-Ubuntu SMP PREEMPT_DYNAMIC Sat Oct 11 09:28:41 UTC 2025 x86_64
```

```
2. w
12:43:51 up 2 min, 1 user, load average: 0.40, 0.24, 0.10
USER      TTY      FROM          LOGIN@      IDLE       JCPU      PCPU  WHAT
root      tty1    -             12:41      1:19      2.38s    1.14s /bin/bash
./amd_speed_aocc510_flang22_znver5_A1.sh
```

```
3. Username
From environment variable $USER: root
```

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Platform Notes (Continued)

```

-----
4. ulimit -a
time(seconds)          unlimited
file(blocks)           unlimited
data(kbytes)           unlimited
stack(kbytes)          unlimited
coredump(blocks)      0
memory(kbytes)         unlimited
locked memory(kbytes) 2097152
process                12381603
nofiles                1024
vmemory(kbytes)        unlimited
locks                  unlimited
rtprio                 0

-----
5. sysinfo process ancestry
/sbin/init
/bin/login -p --
-bash
/bin/bash /home/DellFiles/bin/DELL_speed.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh speed
/bin/bash /home/DellFiles/bin/dell-run-main.sh speed
/bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh speed --define DL-VERS=7.0_T01 --output_format
html, pdf, txt
python3 ./run_amd_speed_aocc510_flang22_znver5_A1.py
/bin/bash ./amd_speed_aocc510_flang22_znver5_A1.sh
runcpu --config amd_speed_aocc510_flang22_znver5_A1.cfg --tune base --reportable --iterations 2 --define
DL-VERS=7.0_T01 --output_format html, pdf, txt fpspeed
runcpu --configfile amd_speed_aocc510_flang22_znver5_A1.cfg --tune base --reportable --iterations 2 --define
DL-VERS=7.0_T01 --output_format html, pdf, txt --nopower --runmode speed --tune base --size
test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2026.001/tempslogs/preenv.fpspeed.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2026rc2

-----
6. /proc/cpuinfo
model name      : AMD EPYC 9755 128-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 26
model          : 2
stepping       : 1
microcode      : 0xb00215a
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size       : 192 4K pages

```

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Platform Notes (Continued)

```

cpu cores      : 128
siblings       : 256
2 physical ids (chips)
512 processors (hardware threads)
physical id 0: core ids 0-127
physical id 1: core ids 0-127
physical id 0: apicids 0-255
physical id 1: apicids 256-511

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```

Architecture:                x86_64
CPU op-mode(s):              32-bit, 64-bit
Address sizes:               52 bits physical, 57 bits virtual
Byte Order:                  Little Endian
CPU(s):                      512
On-line CPU(s) list:        0-511
Vendor ID:                   AuthenticAMD
BIOS Vendor ID:             AMD
Model name:                  AMD EPYC 9755 128-Core Processor
BIOS Model name:            AMD EPYC 9755 128-Core Processor      CPU @ 2.7GHz
BIOS CPU family:            107
CPU family:                  26
Model:                       2
Thread(s) per core:         2
Core(s) per socket:         128
Socket(s):                   2
Stepping:                    1
Frequency boost:             enabled
CPU(s) scaling MHz:         56%
CPU max MHz:                 2700.0000
CPU min MHz:                 1500.0000
BogoMIPS:                    5392.79

```

```

Flags:                        fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
rdtsmp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
smep bmi2 invpcid cqm rdt_a avx512f avx512dq rdseed adx smap

```

(Continued on next page)



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Platform Notes (Continued)

```

avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
xsaveerptr rdpru wbnoinvd amd_ppin cppc amd_ibpb_ret arat npt lbrv
svm_lock nrrip_save tsc_scale vmcb_clean flushbyasid decodeassists
pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl
vnni avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
movdiri movdir64b overflow_recov succor smca avx512_vp2intersect
flush_llc debug_swap

```

```

L1d cache: 12 MiB (256 instances)
L1i cache: 8 MiB (256 instances)
L2 cache: 256 MiB (256 instances)
L3 cache: 1 GiB (32 instances)
NUMA node(s): 2
NUMA node0 CPU(s): 0-127,256-383
NUMA node1 CPU(s): 128-255,384-511
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
always-on; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected
Vulnerability Vmscape: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	12M	12	Data	1	64	1	64
L1i	32K	8M	8	Instruction	1	64	1	64
L2	1M	256M	16	Unified	2	1024	1	64
L3	32M	1G	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0-127,256-383
node 0 size: 1547387 MB

```

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Platform Notes (Continued)

```
node 0 free: 1543895 MB
node 1 cpus: 128-255,384-511
node 1 size: 1548090 MB
node 1 free: 1531630 MB
node distances:
node    0    1
  0:   10   32
  1:   32   10
```

```
-----
9. /proc/meminfo
   MemTotal:      3169769608 kB
```

```
-----
10. who -r
    run-level 3 Feb 5 12:41
```

```
-----
11. Systemd service manager version: systemd 255 (255.4-lubuntu8.8)
    Default Target Status
    multi-user      running
```

```
-----
12. Services, from systemctl list-unit-files
    STATE          UNIT FILES
enabled           apparmor appport blk-availability cloud-config cloud-final cloud-init cloud-init-local
                  console-setup cron e2scrub_reap finalrd getty@ gpu-manager grub-common
                  grub-initrd-fallback keyboard-setup lm-sensors lvm2-monitor multipathd networkd-dispatcher
                  open-iscsi pollinate secureboot-db setvtrgb systemd-networkd systemd-networkd-wait-online
                  systemd-pstore systemd-resolved systemd-timesyncd thermald
enabled-runtime   netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
disabled          console-getty debug-shell iscsid serial-getty@ ssh systemd-boot-check-no-failures
                  systemd-confext systemd-network-generator systemd-networkd-wait-online@
                  systemd-PCRlock-file-system systemd-PCRlock-firmware-code systemd-PCRlock-firmware-config
                  systemd-PCRlock-machine-id systemd-PCRlock-make-policy
                  systemd-PCRlock-secureboot-authority systemd-PCRlock-secureboot-policy systemd-sysext
                  systemd-time-wait-sync upower
indirect          systemd-sysupdate systemd-sysupdate-reboot
masked            cryptdisks cryptdisks-early hwclock multipath-tools-boot sudo x11-common
```

```
-----
13. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=/boot/vmlinuz-6.8.0-87-generic
    root=UUID=a7565640-059d-4a86-afc8-58cd1cadb778
    ro
```

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Platform Notes (Continued)

14. cpupower frequency-info

analyzing CPU 415:

current policy: frequency should be within 1.50 GHz and 2.70 GHz.

The governor "schedutil" may decide which speed to use within this range.

boost state support:

Supported: yes

Active: yes

Boost States: 0

Total States: 3

Pstate-P0: 2700MHz

15. sysctl

```

kernel.numa_balancing          1
kernel.randomize_va_space      0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1

```

16. /sys/kernel/mm/transparent_hugepage

```

defrag          [always] defer defer+madvice madvice never
enabled        [always] madvice never
hpage_pmd_size 2097152
shmem_enabled  always within_size advise [never] deny force

```

17. /sys/kernel/mm/transparent_hugepage/khugepaged

```

alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256

```

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Platform Notes (Continued)

```

max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs  10000

```

18. OS release

```

From /etc/*-release /etc/*-version
os-release Ubuntu 24.04 LTS

```

19. Disk information

```

SPEC is set to: /mnt/ramdisk/cpu2026rc2
Filesystem      Type      Size  Used Avail Use% Mounted on
tmpfs            tmpfs     250G   11G  240G   5% /mnt/ramdisk

```

20. /sys/devices/virtual/dmi/id

```

Vendor:          Dell Inc.
Product:         PowerEdge R7725
Product Family: PowerEdge
Serial:          SLR7736

```

21. dmidecode

Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```

Memory:
  24x 802C0000802C MTC40F2047S1RC64BB1 128 GB 2 rank 6400

```

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

```

BIOS Vendor:      Dell Inc.
BIOS Version:     1.5.3
BIOS Date:        10/29/2025
BIOS Revision:    1.5

```

Compiler Version Notes

```

=====
C      | 811.tealeaf_s(base) 816.nab_s(base) 881.neutron_s(base)
=====

```

AMD clang version 17.0.6 (CLANG: AOCC_5.1.0-Build#1994 2025_12_23)

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Test Date: Feb-2026

Test Sponsor: Dell Inc.

Hardware Availability: Nov-2025

Tested by: Dell Inc.

Software Availability: Jan-2026

Compiler Version Notes (Continued)

Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.1.0/bin

=====
C++ | 803.sph_exa_s(base) 857.namd_s(base) 867.nest_s(base)
| 872.marian_s(base)

=====
AMD clang version 17.0.6 (CLANG: AOCC_5.1.0-Build#1994 2025_12_23)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.1.0/bin

=====
C++, C | 809.cactus_s(base)

=====
AMD clang version 17.0.6 (CLANG: AOCC_5.1.0-Build#1994 2025_12_23)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.1.0/bin

=====
Fortran | 800.pot3d_s(base) 820.cloverleaf_s(base) 822.palm_s(base)
| 849.fotonik3d_s(base) 865.roms_s(base)

=====
flang version 22.1.0-rc2 (<https://github.com/llvm/llvm-project>
a47b42eb9f9b302167b4fc413e6c92798d65dd0b)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/llvm/llvm-22.1.0-rc2/install/bin

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang-22

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SPEC CPU®2026 Floating Point Speed Result

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PowerEdge R7725 (AMD EPYC 9755 128-Core Processor)

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Base Compiler Invocation (Continued)

Benchmarks using both C and C++:

clang++ clang

Base Portability Flags

800.pot3d_s: -DSPEC_LP64
 803.sph_exa_s: -DSPEC_LP64
 809.cactus_s: -DSPEC_LP64
 811.tealeaf_s: -DSPEC_LP64
 816.nab_s: -DSPEC_LP64
 820.cloverleaf_s: -DSPEC_LP64
 822.palm_s: -DSPEC_LP64
 849.fotonik3d_s: -DSPEC_LP64
 857.namd_s: -DSPEC_LP64
 865.roms_s: -DSPEC_LP64
 867.nest_s: -fno-finite-math-only -DSPEC_LP64
 872.marian_s: -DSPEC_LP64
 881.neutron_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -std=c18 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
 -Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -flto -march=znver5
 -fveclib=AMDLIBM -ffast-math -fremap-arrays -fstrip-mining
 -fstruct-layout=7 -mllvm -inline-threshold=1000
 -mllvm -reduce-array-computations=3 -mllvm -unroll-threshold=50 -zopt
 -mrecip=none -fopenmp -DSPEC_OPENMP -lamdalloc -lamdlibm
 -fopenmp=libomp -lomp

C++ benchmarks:

-m64 -std=c++17 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
 -Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -flto -march=znver5
 -fveclib=AMDLIBM -ffast-math -mllvm -unroll-threshold=100
 -mllvm -loop-unswitch-threshold=200000
 -mllvm -reduce-array-computations=3 -zopt -fopenmp -DSPEC_OPENMP
 -pthread -lamdalloc -lamdlibm -fopenmp=libomp -lomp

Fortran benchmarks:

-m64 -std=f2018 -O3 -flto -march=znver5 -fveclib=AMDLIBM
 -ffast-math -funroll-loops -DSPEC_OPENMP -fopenmp
 -fdo-concurrent-to-openmp=host -lamdalloc -lamdlibm -fopenmp=libomp

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Base Optimization Flags (Continued)

Fortran benchmarks (continued):

-lomp

Benchmarks using both C and C++:

-m64 -std=c++17 -std=c18 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -flto -march=znver5
-fveclib=AMDLIBM -ffast-math -fremap-arrays -fstrip-mining
-fstruct-layout=7 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -mllvm -unroll-threshold=50 -zopt
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mrecip=none -fopenmp -DSPEC_OPENMP -pthread -lamdalloc -lamdlibm
-fopenmp=libomp -lomp

Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-return-type -Wno-unused-command-line-argument

Peak Optimization Flags

C benchmarks:

811.tealeaf_s: basepeak = yes

816.nab_s: basepeak = yes

881.neutron_s: basepeak = yes

C++ benchmarks:

803.sph_exa_s: basepeak = yes

857.namd_s: basepeak = yes

867.nest_s: basepeak = yes

872.marian_s: basepeak = yes

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Peak Optimization Flags (Continued)

Fortran benchmarks:

800.pot3d_s: basepeak = yes

820.cloverleaf_s: basepeak = yes

822.palm_s: basepeak = yes

849.fotonik3d_s: basepeak = yes

865.roms_s: basepeak = yes

Benchmarks using both C and C++:

809.cactus_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2026/results/flags/aocc-flags.html>

<http://www.spec.org/cpu2026/results/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2026/results/flags/aocc-flags.xml>

<http://www.spec.org/cpu2026/results/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.xml>

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