



SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2026_int_base = 1060

PowerEdge R7725 (AMD EPYC 9755 128-Core Processor)

SPECrate®2026_int_peak = 1060

CPU2026 License: 6573

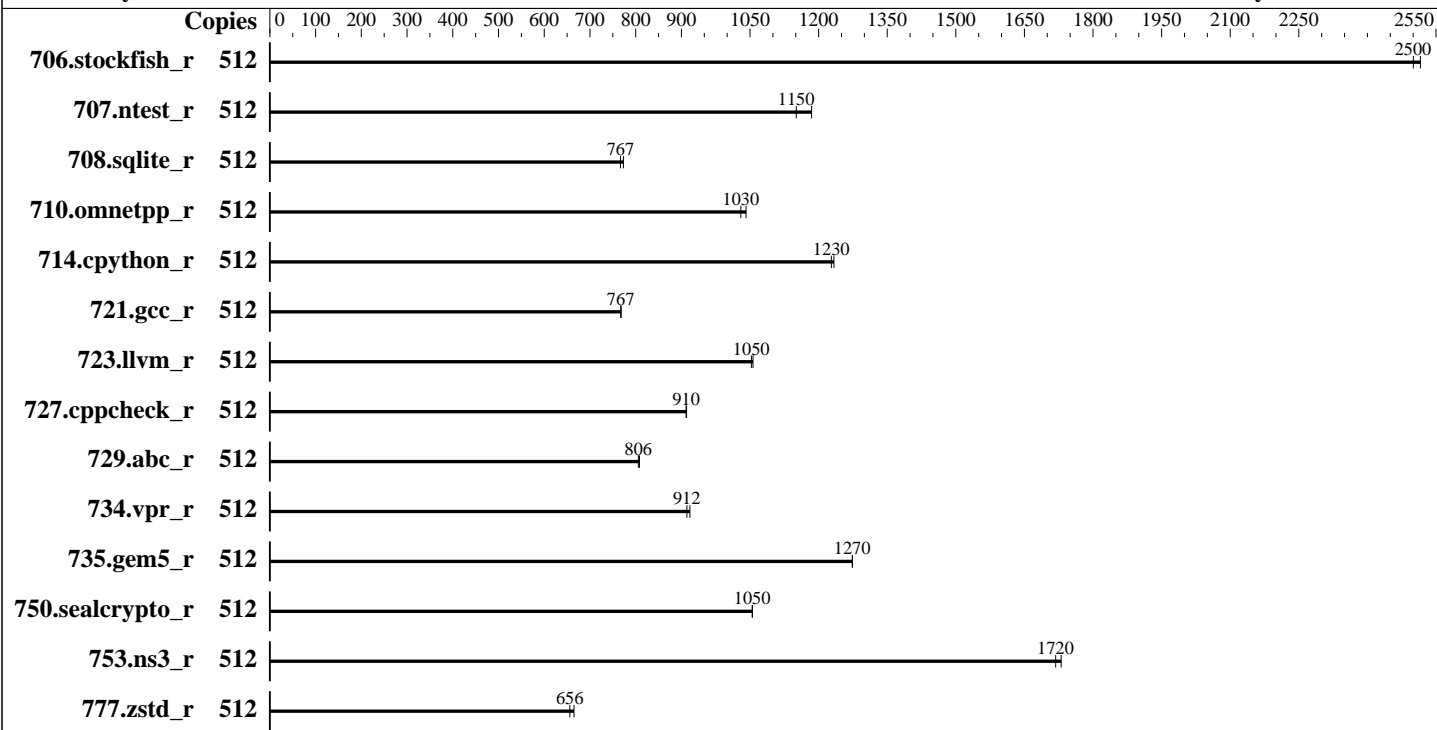
Test Date: Feb-2026

Test Sponsor: Dell Inc.

Hardware Availability: Mar-2025

Tested by: Dell Inc.

Software Availability: Jan-2026



Hardware

CPU Name: AMD EPYC 9755
 Max MHz: 4100
 Nominal: 2700
 Enabled: 256 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 512 MB I+D on chip per chip, 32 MB shared / 8 cores
 Other: None
 Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R)
 Storage: 250 GB on tmpfs
 Cooling: Air
 Other: None

Software

OS: Ubuntu 24.04.2 LTS
 6.8.0-55-generic
 Compiler: C/C++/Fortran: Version 5.1.0 of AOCC
 Compiler Category: Vendor
 Firmware: Version 1.5.3 released Oct-2025
 File System: tmpfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: None
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
706.stockfish_r	512	258	2500	256	2520			512	258	2500	256	2520		
707.ntest_r	512	256	1180	263	1150			512	256	1180	263	1150		
708.sqlite_r	512	353	767	350	773			512	353	767	350	773		
710.omnetpp_r	512	239	1040	242	1030			512	239	1040	242	1030		
714.cpython_r	512	200	1230	199	1230			512	200	1230	199	1230		
721.gcc_r	512	458	767	457	768			512	458	767	457	768		
723.llvm_r	512	247	1050	246	1060			512	247	1050	246	1060		
727.cppcheck_r	512	202	910	202	911			512	202	910	202	911		
729.abc_r	512	292	806	291	808			512	292	806	291	808		
734.vpr_r	512	257	918	259	912			512	257	918	259	912		
735.gem5_r	512	196	1270	196	1270			512	196	1270	196	1270		
750.sealcrypto_r	512	260	1050	260	1050			512	260	1050	260	1050		
753.ns3_r	512	181	1730	183	1720			512	181	1730	183	1720		
777.zstd_r	512	496	665	503	656			512	496	665	503	656		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.

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Operating System Notes (Continued)

To free node-local memory and avoid remote memory usage,
 'sysctl -w vm.zone_reclaim_mode=1' run as root.
 To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
 To disable address space layout randomization (ASLR) to reduce run-to-run
 variability, 'sysctl -w kernel.randomize_va_space=0' run as root.
 To enable Transparent Hugepages (THP) for all allocations,
 'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
 'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
 LD_LIBRARY_PATH =
 "/mnt/ramdisk/cpu2026rc2/amd_rate_aocc510_znver5_A_lib/lib:/mnt/ramdisk/
 cpu2026rc2/amd_rate_aocc510_znver5_A_lib/lib32:"
 MALLOC_CONF = "retain:true"

General Notes

Binaries were compiled on a system with 2x AMD EPYC Venice256 CPU + 2TiB Memory using Ubuntu 24.04
 Benchmark run from a 250 GB ramdisk created with the cmd: "mount -t tmpfs -o size=250G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:
 Virtualization Technology : Disabled
 NUMA Nodes Per Socket : 4

 System Profile : Custom
 C-States : Disabled
 Memory Patrol Scrub : Disabled
 PCI ASPM L1 Link Power Management : Disabled
 Periodic Directory Rinse Tuning : Blended
 Determinism Control : Manual
 Determinism Slider : Power Determinism
 Optimizer Mode : Enabled
 Adaptive Allocation : Enabled
 Dram Refresh Delay : Performance
 DIMM Self Healing -
 on Uncorrectable Memory Error : Disabled

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Platform Notes (Continued)

sysinfo program /mnt/ramdisk/cpu2026rc2/bin/sysinfo
Rev: 069f95da7e7f5d81b2ce48a82150e54f
running on SLR7753-R7725 Wed Feb 4 18:46:05 2026

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -srvm
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 255 (255.4-lubuntu8.6)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

```
1. uname -srvm
Linux 6.8.0-55-generic #57-Ubuntu SMP PREEMPT_DYNAMIC Wed Feb 12 23:42:21 UTC 2025 x86_64
```

```
2. w
18:46:05 up 3 min, 1 user, load average: 0.21, 0.12, 0.04
USER      TTY      FROM          LOGIN@      IDLE        JCPU      PCPU      WHAT
root      tty1    -             18:42       1:02       2.36s    1.10s    /bin/bash ./amd_rate_aocc510_znver5_A1.sh
```

```
3. Username
From environment variable $USER: root
```

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Platform Notes (Continued)

```

-----
4. ulimit -a
time(seconds)          unlimited
file(blocks)           unlimited
data(kbytes)           unlimited
stack(kbytes)          unlimited
coredump(blocks)       0
memory(kbytes)         unlimited
locked memory(kbytes)  2097152
process                6188464
nofiles                1024
vmemory(kbytes)        unlimited
locks                  unlimited
rtprio                 0

-----
5. sysinfo process ancestry
/sbin/init
/bin/login -p --
-bash
/bin/bash /home/DellFiles/bin/DELL_rate.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh rate --define DL-VERS=6.4_T17 --output_format
html,pdf,txt
python3 ./run_amd_rate_aocc510_znver5_A1.py
/bin/bash ./amd_rate_aocc510_znver5_A1.sh
runcpu --config amd_rate_aocc510_znver5_A1.cfg --tune base --reportable --iterations 2 --define
DL-VERS=6.4_T17 --output_format html,pdf,txt intrate
runcpu --configfile amd_rate_aocc510_znver5_A1.cfg --tune base --reportable --iterations 2 --define
DL-VERS=6.4_T17 --output_format html,pdf,txt --nopower --runmode rate --tune base --size
test:train:refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2026.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2026rc2

-----
6. /proc/cpuinfo
model name          : AMD EPYC 9755 128-Core Processor
vendor_id           : AuthenticAMD
cpu family          : 26
model               : 2
stepping            : 1
microcode           : 0xb00215a
bugs                 : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size            : 192 4K pages

```

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Platform Notes (Continued)

```

cpu cores      : 128
siblings       : 256
2 physical ids (chips)
512 processors (hardware threads)
physical id 0: core ids 0-127
physical id 1: core ids 0-127
physical id 0: apicids 0-255
physical id 1: apicids 256-511

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```

Architecture:                x86_64
CPU op-mode(s):              32-bit, 64-bit
Address sizes:               52 bits physical, 57 bits virtual
Byte Order:                  Little Endian
CPU(s):                      512
On-line CPU(s) list:        0-511
Vendor ID:                   AuthenticAMD
BIOS Vendor ID:             AMD
Model name:                  AMD EPYC 9755 128-Core Processor
BIOS Model name:             AMD EPYC 9755 128-Core Processor      CPU @ 2.7GHz
BIOS CPU family:            107
CPU family:                  26
Model:                      2
Thread(s) per core:         2
Core(s) per socket:         128
Socket(s):                  2
Stepping:                   1
Frequency boost:             enabled
CPU(s) scaling MHz:         66%
CPU max MHz:                 4121.1909
CPU min MHz:                 1500.0000
BogoMIPS:                   5392.90

```

```

Flags:                        fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
rdtsmp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
smep bmi2 invpcid cqm rdt_a avx512f avx512dq rdseed adx smap

```

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Platform Notes (Continued)

```

avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock
nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
pftthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnni
avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
movdiri movdir64b overflow_recov succor smca avx512_vp2intersect

```

```

L1d cache: 12 MiB (256 instances)
L1i cache: 8 MiB (256 instances)
L2 cache: 256 MiB (256 instances)
L3 cache: 1 GiB (32 instances)
NUMA node(s): 8
NUMA node0 CPU(s): 0-31,256-287
NUMA node1 CPU(s): 32-63,288-319
NUMA node2 CPU(s): 64-95,320-351
NUMA node3 CPU(s): 96-127,352-383
NUMA node4 CPU(s): 128-159,384-415
NUMA node5 CPU(s): 160-191,416-447
NUMA node6 CPU(s): 192-223,448-479
NUMA node7 CPU(s): 224-255,480-511
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
always-on; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	12M	12	Data	1	64	1	64
L1i	32K	8M	8	Instruction	1	64	1	64
L2	1M	256M	16	Unified	2	1024	1	64
L3	32M	1G	16	Unified	3	32768	1	64

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Platform Notes (Continued)

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0-31,256-287
node 0 size: 192728 MB
node 0 free: 191107 MB
node 1 cpus: 32-63,288-319
node 1 size: 193510 MB
node 1 free: 192865 MB
node 2 cpus: 64-95,320-351
node 2 size: 193510 MB
node 2 free: 192808 MB
node 3 cpus: 96-127,352-383
node 3 size: 193494 MB
node 3 free: 192782 MB
node 4 cpus: 128-159,384-415
node 4 size: 193510 MB
node 4 free: 192850 MB
node 5 cpus: 160-191,416-447
node 5 size: 193510 MB
node 5 free: 181623 MB
node 6 cpus: 192-223,448-479
node 6 size: 193467 MB
node 6 free: 192694 MB
node 7 cpus: 224-255,480-511
node 7 size: 193459 MB
node 7 free: 192703 MB
node distances:
node   0   1   2   3   4   5   6   7
  0:  10  12  12  12  32  32  32  32
  1:  12  10  12  12  32  32  32  32
  2:  12  12  10  12  32  32  32  32
  3:  12  12  12  10  32  32  32  32
  4:  32  32  32  32  10  12  12  12
  5:  32  32  32  32  12  10  12  12
  6:  32  32  32  32  12  12  10  12
  7:  32  32  32  32  12  12  12  10

```

9. /proc/meminfo

MemTotal: 1584322692 kB

10. who -r

run-level 3 Feb 4 18:42

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Platform Notes (Continued)

11. Systemd service manager version: systemd 255 (255.4-lubuntu8.6)

Default Target	Status
multi-user	running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	apparmor apport blk-availability cloud-config cloud-final cloud-init cloud-init-local console-setup e2scrub_reap finalrd getty@ gpu-manager grub-common grub-initrd-fallback keyboard-setup lm-sensors lvm2-monitor multipathd networkd-dispatcher nvme-fc-boot-connections nvme-autoconnect open-iscsi pollinate secureboot-db setvtrgb systemd-networkd systemd-networkd-wait-online systemd-pstore systemd-resolved systemd-timesyncd thermald tuned
enabled-runtime	netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
disabled	console-getty debug-shell fio iscsid serial-getty@ ssh systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext systemd-time-wait-sync upower
indirect	systemd-sysupdate systemd-sysupdate-reboot uidd
masked	cryptdisks cryptdisks-early hwclock multipath-tools-boot sudo x11-common

13. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/boot/vmlinuz-6.8.0-55-generic
root=UUID=12012b0a-8b6b-4db0-98ad-44c145a96bdb
ro
```

14. cpupower frequency-info

```
analyzing CPU 421:
  current policy: frequency should be within 1.50 GHz and 2.70 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.

boost state support:
  Supported: yes
  Active: yes
  Boost States: 0
  Total States: 3
  Pstate-P0: 2700MHz
```

15. tuned-adm active

Current active profile: latency-performance

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Platform Notes (Continued)

16. sysctl

```

kernel.numa_balancing          1
kernel.randomize_va_space      0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      3
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                  8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1

```

17. /sys/kernel/mm/transparent_hugepage

```

defrag          [always] defer defer+madvise madvise never
enabled         [always] madvise never
hpage_pmd_size 2097152
shmem_enabled   always within_size advise [never] deny force

```

18. /sys/kernel/mm/transparent_hugepage/khugepaged

```

alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none           511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096
scan_sleep_millisecs   10000

```

19. OS release

```

From /etc/*-release /etc/*-version
os-release Ubuntu 24.04.2 LTS

```

20. Disk information

```

SPEC is set to: /mnt/ramdisk/cpu2026rc2
Filesystem      Type      Size Used Avail Use% Mounted on

```

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Platform Notes (Continued)

tmpfs tmpfs 250G 10G 241G 4% /mnt/ramdisk

21. /sys/devices/virtual/dmi/id

Vendor: Dell Inc.
Product: PowerEdge R7725
Product Family: PowerEdge
Serial: SLR7753

22. dmidecode

Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

23x 80AD000080AD HMC94AHBRA277N 64 GB 2 rank 6400
1x 80AD000080AD HMC94AHBRA480N 64 GB 2 rank 6400

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Dell Inc.
BIOS Version: 1.5.3
BIOS Date: 10/29/2025
BIOS Revision: 1.5

Compiler Version Notes

C | 708.sqlite_r(base) 714.cpython_r(base) 777.zstd_r(base)

AMD clang version 17.0.6 (CLANG: AOCC_5.1.0-Build#1994 2025_12_23)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-5.1.0/bin

C++ | 706.stockfish_r(base) 707.ntest_r(base) 727.cppcheck_r(base)
| 753.ns3_r(base)

AMD clang version 17.0.6 (CLANG: AOCC_5.1.0-Build#1994 2025_12_23)

Target: x86_64-unknown-linux-gnu

Thread model: posix

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SPECrate®2026_int_peak = 1060

CPU2026 License: 6573

Test Date: Feb-2026

Test Sponsor: Dell Inc.

Hardware Availability: Mar-2025

Tested by: Dell Inc.

Software Availability: Jan-2026

Compiler Version Notes (Continued)

InstalledDir: /opt/AMD/aocc/aocc-compiler-5.1.0/bin

```

=====
C++, C | 710.omnetpp_r(base) 721.gcc_r(base) 723.llvm_r(base) 729.abc_r(base)
      | 734.vpr_r(base) 735.gem5_r(base) 750.sealcrypto_r(base)
=====

```

AMD clang version 17.0.6 (CLANG: AOCC_5.1.0-Build#1994 2025_12_23)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-5.1.0/bin

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Benchmarks using both C and C++:

clang++ clang

Base Portability Flags

706.stockfish_r: -DSPEC_LP64

707.ntest_r: -DSPEC_LP64

708.sqlite_r: -DSPEC_LP64

710.omnetpp_r: -DSPEC_LP64

714.cpython_r: -DSPEC_LP64

721.gcc_r: -DSPEC_LP64

723.llvm_r: -DSPEC_LP64

727.cppcheck_r: -DSPEC_LP64

729.abc_r: -DSPEC_LP64

734.vpr_r: -DSPEC_LP64

735.gem5_r: -DSPEC_LP64

750.sealcrypto_r: -DSPEC_LP64

753.ns3_r: -DSPEC_LP64

777.zstd_r: -DSPEC_LP64



SPEC CPU®2026 Integer Rate Result

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Base Optimization Flags

C benchmarks:

```
-m64 -std=c18 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-Wl,-mllvm -Wl,-extra-inliner -O3 -march=znver5 -fveclib=AMDLIBM
-fno-PIE -no-pie -flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdalloc
```

C++ benchmarks:

```
-m64 -std=c++17 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIBM -flto -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -fno-PIE -no-pie
-fvirtual-function-elimination -fvisibility=hidden -lamdlibm -lflang
-lamdalloc
```

Benchmarks using both C and C++:

```
-m64 -std=c++17 -std=c18 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIBM -fno-PIE -no-pie -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -fvirtual-function-elimination
-fvisibility=hidden -lamdlibm -lflang -lamdalloc
```

Peak Optimization Flags

C benchmarks:

708.sqlite_r: basepeak = yes

714.cpython_r: basepeak = yes

777.zstd_r: basepeak = yes

C++ benchmarks:

706.stockfish_r: basepeak = yes

(Continued on next page)



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Software Availability: Jan-2026

Peak Optimization Flags (Continued)

707.ntest_r: basepeak = yes

727.cppcheck_r: basepeak = yes

753.ns3_r: basepeak = yes

Benchmarks using both C and C++:

710.omnetpp_r: basepeak = yes

721.gcc_r: basepeak = yes

723.llvm_r: basepeak = yes

729.abc_r: basepeak = yes

734.vpr_r: basepeak = yes

735.gem5_r: basepeak = yes

750.sealcrypto_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2026/results/flags/aocc-flags.2026-05-04.html>

<http://www.spec.org/cpu2026/results/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2026/results/flags/aocc-flags.2026-05-04.xml>

<http://www.spec.org/cpu2026/results/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.xml>

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Tested with SPEC CPU®2026 v0.902.0 on 2026-02-04 13:46:05-0500.

Report generated on 2026-05-11 16:38:33 by CPU2026 PDF formatter (unknown).

Originally published on 2026-05-05.