



SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Supermicro
(Test Sponsor: Intel Corporation)

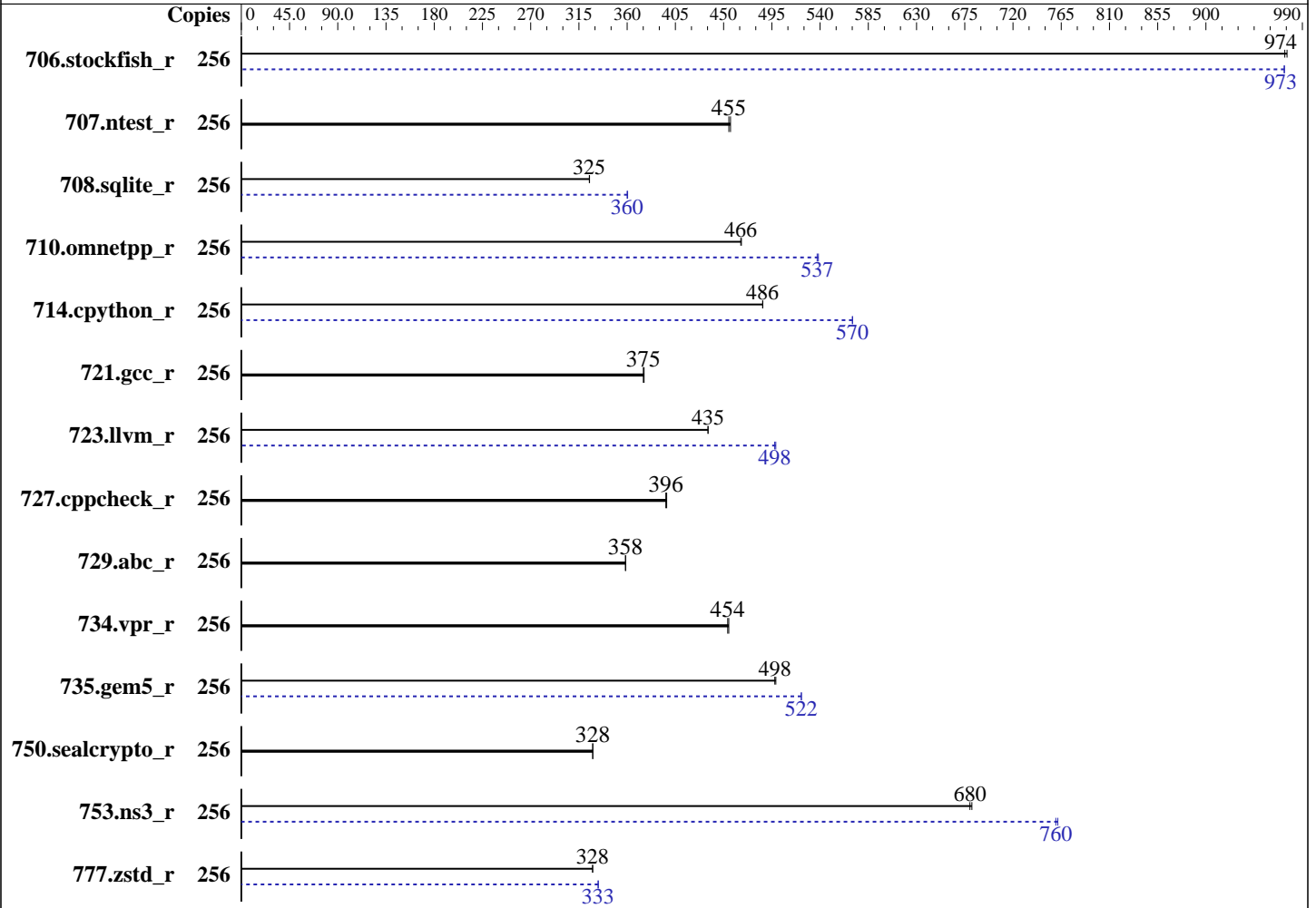
SPECrate®2026_int_base = 446

Hyper SuperServer SYS-221H-TNR

SPECrate®2026_int_peak = 470

CPU2026 License: 13
Test Sponsor: Intel Corporation
Tested by: Intel Corporation

Test Date: Feb-2026
Hardware Availability: Mar-2023
Software Availability: Nov-2025



Hardware

CPU Name: Intel Xeon Platinum 8592+
 Max MHz: 3900
 Nominal: 1900
 Enabled: 128 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 320 MB I+D on chip per chip
 Other: None
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-6400B-R, running at 5600)
 Storage: 1 x 1.8 TB PCIe NVMe SSD
 Cooling: Air
 Other: None

Software

OS: Ubuntu 24.04 LTS
 6.8.0-90-generic
 Compiler: C/C++: Version 2025.3 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2025.3 of Intel Fortran Compiler for Linux
 Compiler Category: Vendor
 Firmware: Version 5.32 released Jul-2025
 File System: ext4
 System State: Run level 5 (Multiuser with networking)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: None
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Supermicro
(Test Sponsor: Intel Corporation)

SPECrate®2026_int_base = 446

Hyper SuperServer SYS-221H-TNR

SPECrate®2026_int_peak = 470

CPU2026 License: 13
Test Sponsor: Intel Corporation
Tested by: Intel Corporation

Test Date: Feb-2026
Hardware Availability: Mar-2023
Software Availability: Nov-2025

Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
706.stockfish_r	256	331	976	331	974			256	332	973	331	973		
707.ntest_r	256	333	455	332	456			256	333	455	332	456		
708.sqlite_r	256	416	325	416	325			256	376	360	375	360		
710.omnetpp_r	256	267	466	267	467			256	232	537	231	538		
714.cpython_r	256	252	486	252	487			256	215	570	215	570		
721.gcc_r	256	467	376	468	375			256	467	376	468	375		
723.llvm_r	256	298	435	298	435			256	261	498	260	499		
727.cppcheck_r	256	232	397	232	396			256	232	397	232	396		
729.abc_r	256	328	358	328	359			256	328	358	328	359		
734.vpr_r	256	260	454	259	455			256	260	454	259	455		
735.gem5_r	256	250	498	250	499			256	239	522	239	523		
750.sealcrypto_r	256	418	328	419	328			256	418	328	419	328		
753.ns3_r	256	230	682	231	680			256	206	760	206	762		
777.zstd_r	256	503	328	503	328			256	495	333	495	333		

SPECrate®2026_int_base = **446**

SPECrate®2026_int_peak = **470**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/root/cpu2026/lib"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using CentOS Stream 9.
Transparent Huge Pages enabled by default

(Continued on next page)



SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Supermicro

(Test Sponsor: Intel Corporation)

SPECrate®2026_int_base = 446

Hyper SuperServer SYS-221H-TNR

SPECrate®2026_int_peak = 470

CPU2026 License: 13

Test Sponsor: Intel Corporation

Tested by: Intel Corporation

Test Date: Feb-2026

Hardware Availability: Mar-2023

Software Availability: Nov-2025

General Notes (Continued)

Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configuration :
Workload Profile set to Disabled
Power Performance Tuning set to BIOS Controls EPB
Energy Perf Bias CFG mode set to Extreme Performance
KTI Prefetch set to Enabled
LLC Dead Line Alloc set to Disabled
Sub NUMA Clustering (SNC) set to Enable SNC2 (2-clusters)
Stale A to S Dir optimization set to Disabled
BMC Configuration:
System Fan speed set to full speed

Sysinfo program /root/cpu2026/bin/sysinfo
Rev: 069f95da7e7f5d81b2ce48a82150e54f
running on smc-emr Fri Feb 6 01:12:56 2026

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -srvm
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r

(Continued on next page)



SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Supermicro
(Test Sponsor: Intel Corporation)

SPECrate®2026_int_base = 446

Hyper SuperServer SYS-221H-TNR

SPECrate®2026_int_peak = 470

CPU2026 License: 13
Test Sponsor: Intel Corporation
Tested by: Intel Corporation

Test Date: Feb-2026
Hardware Availability: Mar-2023
Software Availability: Nov-2025

Platform Notes (Continued)

- 11. Systemd service manager version: systemd 255 (255.4-lubuntu8.8)
- 12. Failed units, from systemctl list-units --state=failed
- 13. Services, from systemctl list-unit-files
- 14. Linux kernel boot-time arguments, from /proc/cmdline
- 15. cpupower frequency-info
- 16. sysctl
- 17. /sys/kernel/mm/transparent_hugepage
- 18. /sys/kernel/mm/transparent_hugepage/khugepaged
- 19. OS release
- 20. Disk information
- 21. /sys/devices/virtual/dmi/id
- 22. dmidecode
- 23. BIOS

```
-----
1. uname -srvm
   Linux 6.8.0-90-generic #91-Ubuntu SMP PREEMPT_DYNAMIC Tue Nov 18 14:14:30 UTC 2025 x86_64
-----
```

```
-----
2. w
   01:12:56 up 2:45, 5 users, load average: 0.19, 60.13, 157.02
USER      TTY      FROM          LOGIN@      IDLE        JCPU      PCPU  WHAT
root      -        10.125.66.56  00:53      1:50m      0.00s    0.09s  sshd: root@notty
root      -        10.125.66.56  00:53      1:50m      0.00s    0.32s  sshd: root@pts/0
root      -        10.241.241.144 23:21      1:50m      0.00s    0.08s  sshd: root@notty
root      tty1    -             23:21      1:51m      0.16s    0.01s  -bash
-----
```

```
-----
3. Username
   From environment variable $USER: root
-----
```

```
-----
4. ulimit -a
time(seconds)      unlimited
file(blocks)       unlimited
data(kbytes)       unlimited
stack(kbytes)      unlimited
coredump(blocks)   0
memory(kbytes)     unlimited
locked memory(kbytes) 132054144
process            4126381
nofiles            1024
vmemory(kbytes)    unlimited
locks              unlimited
rtprio             0
-----
```

(Continued on next page)



SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Supermicro
(Test Sponsor: Intel Corporation)

SPECrate®2026_int_base = 446

Hyper SuperServer SYS-221H-TNR

SPECrate®2026_int_peak = 470

CPU2026 License: 13
Test Sponsor: Intel Corporation
Tested by: Intel Corporation

Test Date: Feb-2026
Hardware Availability: Mar-2023
Software Availability: Nov-2025

Platform Notes (Continued)

```

-----
5. sysinfo process ancestry
  /sbin/init
  tmux new -s spec
  -bash
  -bash
  runcpu --nobuild --reportable --action validate --define default-platform-flags --copies 256 -c
    ic2025.3-sapphirerapids-cpu2026-0.902-rate-20260121.cfg --define smt-on --define cores=128 --define
    physicalfirst --define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate
  runcpu --nobuild --reportable --action validate --define default-platform-flags --copies 256 --configfile
    ic2025.3-sapphirerapids-cpu2026-0.902-rate-20260121.cfg --define smt-on --define cores=128 --define
    physicalfirst --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all
  --nopower --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
    $SPEC/tmp/CPU2026.007/templogs/preenv.intrate.007.0.log --lognum 007.0 --from_runcpu 2
  specperl $SPEC/bin/sysinfo
  $SPEC = /root/cpu2026

```

```

-----
6. /proc/cpuinfo
  model name      : INTEL(R) XEON(R) PLATINUM 8592+
  vendor_id      : GenuineIntel
  cpu family     : 6
  model          : 207
  stepping       : 2
  microcode      : 0x210002b3
  bugs           : spectre_v1 spectre_v2 spec_store_bypass swappgs eibrs_pbrsb bhi vmscape
  cpu cores     : 64
  siblings      : 128
  2 physical ids (chips)
  256 processors (hardware threads)
  physical id 0: core ids 0-63
  physical id 1: core ids 0-63
  physical id 0: apicids 0-127
  physical id 1: apicids 128-255

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                256
On-line CPU(s) list:   0-255

```

(Continued on next page)



SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Supermicro

(Test Sponsor: Intel Corporation)

SPECrate®2026_int_base = 446

Hyper SuperServer SYS-221H-TNR

SPECrate®2026_int_peak = 470

CPU2026 License: 13

Test Sponsor: Intel Corporation

Tested by: Intel Corporation

Test Date: Feb-2026

Hardware Availability: Mar-2023

Software Availability: Nov-2025

Platform Notes (Continued)

Vendor ID:	GenuineIntel
BIOS Vendor ID:	Intel(R) Corporation
Model name:	INTEL(R) XEON(R) PLATINUM 8592+
BIOS Model name:	INTEL(R) XEON(R) PLATINUM 8592+ CPU @ 1.9GHz
BIOS CPU family:	179
CPU family:	6
Model:	207
Thread(s) per core:	2
Core(s) per socket:	64
Socket(s):	2
Stepping:	2
CPU(s) scaling MHz:	100%
CPU max MHz:	3900.0000
CPU min MHz:	800.0000
BogoMIPS:	3800.00

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
nopl xtopology nonstop_tsc cpuid aperfperf tsc_known_freq pni
pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2
ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm
rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb
intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1
xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
arat pln pts vnni avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni
vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57
rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear
serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile
amx_int8 flush_lld arch_capabilities ibpb_exit_to_user

Virtualization:	VT-x
L1d cache:	6 MiB (128 instances)
L1i cache:	4 MiB (128 instances)
L2 cache:	256 MiB (128 instances)
L3 cache:	640 MiB (2 instances)
NUMA node(s):	4
NUMA node0 CPU(s):	0-31,128-159
NUMA node1 CPU(s):	32-63,160-191
NUMA node2 CPU(s):	64-95,192-223
NUMA node3 CPU(s):	96-127,224-255
Vulnerability Gather data sampling:	Not affected
Vulnerability Itlb multihit:	Not affected
Vulnerability L1tf:	Not affected

(Continued on next page)



SPEC CPU[®]2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Supermicro

(Test Sponsor: Intel Corporation)

SPECrate[®]2026_int_base = 446

Hyper SuperServer SYS-221H-TNR

SPECrate[®]2026_int_peak = 470

CPU2026 License: 13

Test Sponsor: Intel Corporation

Tested by: Intel Corporation

Test Date: Feb-2026

Hardware Availability: Mar-2023

Software Availability: Nov-2025

Platform Notes (Continued)

Vulnerability Mds: Not affected
 Vulnerability Meltdown: Not affected
 Vulnerability Mmio stale data: Not affected
 Vulnerability Reg file data sampling: Not affected
 Vulnerability Retbleed: Not affected
 Vulnerability Spec rstack overflow: Not affected
 Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
 Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
 Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBR SB-eIBRS SW sequence; BHI BHI_DIS_S
 Vulnerability Srbds: Not affected
 Vulnerability Tsx async abort: Not affected
 Vulnerability Vmscape: Mitigation; IBPB before exit to userspace

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	6M	12	Data	1	64	1	64
L1i	32K	4M	8	Instruction	1	64	1	64
L2	2M	256M	16	Unified	2	2048	1	64
L3	320M	640M	20	Unified	3	262144	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0-31,128-159
node 0 size: 257650 MB
node 0 free: 235278 MB
node 1 cpus: 32-63,160-191
node 1 size: 257982 MB
node 1 free: 233634 MB
node 2 cpus: 64-95,192-223
node 2 size: 258026 MB
node 2 free: 235188 MB
node 3 cpus: 96-127,224-255
node 3 size: 258013 MB
node 3 free: 237985 MB
node distances:
node  0  1  2  3
 0:  10  12  21  21
 1:  12  10  21  21
 2:  21  21  10  12
 3:  21  21  12  10

```

9. /proc/meminfo

MemTotal: 1056433172 kB

(Continued on next page)



SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Supermicro
(Test Sponsor: Intel Corporation)

SPECrate®2026_int_base = 446

Hyper SuperServer SYS-221H-TNR

SPECrate®2026_int_peak = 470

CPU2026 License: 13
Test Sponsor: Intel Corporation
Tested by: Intel Corporation

Test Date: Feb-2026
Hardware Availability: Mar-2023
Software Availability: Nov-2025

Platform Notes (Continued)

10. who -r
run-level 5 Feb 5 22:30

11. Systemd service manager version: systemd 255 (255.4-lubuntu8.8)
Default Target Status
graphical degraded

12. Failed units, from systemctl list-units --state=failed
UNIT LOAD ACTIVE SUB DESCRIPTION
* fwupd-refresh.service loaded failed failed Refresh fwupd metadata and update motd
* systemd-networkd-wait-online.service loaded failed failed Wait for Network to be Configured
Legend: LOAD -> Reflects whether the unit definition was properly loaded.
ACTIVE -> The high-level unit activation state, i.e. generalization of SUB.
SUB -> The low-level unit activation state, values depend on unit type.
2 loaded units listed.

13. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager apparmor apport blk-availability cloud-config cloud-final cloud-init
cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager
grub-common grub-initrd-fallback keyboard-setup lvm2-monitor multipathd
networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb
snapd sysstat systemd-networkd systemd-networkd-wait-online systemd-pstore
systemd-resolved systemd-timesyncd thermald ua-reboot-cmds ubuntu-advantage udisks2 ufw
unattended-upgrades vgauth
enabled-runtime netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
disabled console-getty debug-shell iscsid nftables rsync serial-getty@ ssh
systemd-boot-check-no-failures systemd-confext systemd-network-generator
systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code
systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy
systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysex
systemd-time-wait-sync upower
indirect systemd-sysupdate systemd-sysupdate-reboot uidd
masked cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common

14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.8.0-90-generic
root=UUID=637c87f4-ebc0-4fd2-bd43-98bf140085e1
ro

(Continued on next page)



SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Supermicro

(Test Sponsor: Intel Corporation)

SPECrate®2026_int_base = 446

Hyper SuperServer SYS-221H-TNR

SPECrate®2026_int_peak = 470

CPU2026 License: 13

Test Sponsor: Intel Corporation

Tested by: Intel Corporation

Test Date: Feb-2026

Hardware Availability: Mar-2023

Software Availability: Nov-2025

Platform Notes (Continued)

15. cpupower frequency-info

analyzing CPU 8:

current policy: frequency should be within 800 MHz and 3.90 GHz.

The governor "performance" may decide which speed to use within this range.

boost state support:

Supported: yes

Active: yes

16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	20
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	60
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	0

17. /sys/kernel/mm/transparent_hugepage

defrag	always defer defer+madvise [madvise] never
enabled	always [madvise] never
hpage_pmd_size	2097152
shmem_enabled	always within_size advise [never] deny force

18. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

(Continued on next page)



SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Supermicro
(Test Sponsor: Intel Corporation)

SPECrate®2026_int_base = 446

Hyper SuperServer SYS-221H-TNR

SPECrate®2026_int_peak = 470

CPU2026 License: 13
Test Sponsor: Intel Corporation
Tested by: Intel Corporation

Test Date: Feb-2026
Hardware Availability: Mar-2023
Software Availability: Nov-2025

Platform Notes (Continued)

19. OS release
From /etc/*-release /etc/*-version
os-release Ubuntu 24.04 LTS

20. Disk information
SPEC is set to: /root/cpu2026
Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p2 ext4 1.8T 377G 1.3T 23% /

21. /sys/devices/virtual/dmi/id
Vendor: Supermicro
Product: SYS-221H-TNR
Product Family: Family
Serial: S495122X2832926

22. dmidecode
Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
2x Samsung M321R8GA0PB2-CCPEC 64 GB 2 rank 6400, configured at 5600
9x Samsung M321R8GA0PB2-CCPPC 64 GB 2 rank 6400, configured at 5600
5x Samsung M321R8GA0PB2-CCPWC 64 GB 2 rank 6400, configured at 5600

23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 2.7
BIOS Date: 07/11/2025
BIOS Revision: 5.32

Compiler Version Notes

=====
C | 708.sqlite_r(base, peak) 714.cpython_r(base, peak) 777.zstd_r(base,
| peak)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,

(Continued on next page)



SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Supermicro
(Test Sponsor: Intel Corporation)

SPECrate®2026_int_base = 446

Hyper SuperServer SYS-221H-TNR

SPECrate®2026_int_peak = 470

CPU2026 License: 13
Test Sponsor: Intel Corporation
Tested by: Intel Corporation

Test Date: Feb-2026
Hardware Availability: Mar-2023
Software Availability: Nov-2025

Compiler Version Notes (Continued)

Version 2025.3.0 Build 20251010
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.

=====
C++ | 706.stockfish_r(base, peak) 707.ntest_r(base, peak)
| 727.cppcheck_r(base, peak) 753.ns3_r(base, peak)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2025.3.0 Build 20251010
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.

=====
C++, C | 710.omnetpp_r(base, peak) 721.gcc_r(base, peak) 723.llvm_r(base,
| peak) 729.abc_r(base, peak) 734.vpr_r(base, peak) 735.gem5_r(base,
| peak) 750.sealcrypto_r(base, peak)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2025.3.0 Build 20251010
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Benchmarks using both C and C++:
icpx icx

Base Portability Flags

706.stockfish_r: -DSPEC_LP64
707.ntest_r: -DSPEC_LP64
708.sqlite_r: -DSPEC_LP64
710.omnetpp_r: -DSPEC_LP64
714.cpython_r: -DSPEC_LP64
721.gcc_r: -DSPEC_LP64
723.llvm_r: -DSPEC_LP64

(Continued on next page)



SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Supermicro
(Test Sponsor: Intel Corporation)

SPECrate®2026_int_base = 446

Hyper SuperServer SYS-221H-TNR

SPECrate®2026_int_peak = 470

CPU2026 License: 13
Test Sponsor: Intel Corporation
Tested by: Intel Corporation

Test Date: Feb-2026
Hardware Availability: Mar-2023
Software Availability: Nov-2025

Base Portability Flags (Continued)

727.cppcheck_r: -DSPEC_LP64
729.abc_r: -DSPEC_LP64
734.vpr_r: -DSPEC_LP64
735.gem5_r: -DSPEC_LP64
750.sealcrypto_r: -DSPEC_LP64
753.ns3_r: -DSPEC_LP64
777.zstd_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -std=c18 -Wl,-z,muldefs -xsapphirerapids
-mprefer-vector-width=512 -O3 -ffp-model=fast -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2025.3/lib -lqkmalloc

C++ benchmarks:

-m64 -std=c++17 -Wl,-z,muldefs -xsapphirerapids
-mprefer-vector-width=512 -O3 -ffp-model=fast -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2025.3/lib -lqkmalloc

Benchmarks using both C and C++:

-m64 -std=c++17 -std=c18 -Wl,-z,muldefs -xsapphirerapids
-mprefer-vector-width=512 -O3 -ffp-model=fast -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2025.3/lib -lqkmalloc

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Benchmarks using both C and C++:

icpx icx



SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Supermicro

(Test Sponsor: Intel Corporation)

SPECrate®2026_int_base = 446

Hyper SuperServer SYS-221H-TNR

SPECrate®2026_int_peak = 470

CPU2026 License: 13

Test Sponsor: Intel Corporation

Tested by: Intel Corporation

Test Date: Feb-2026

Hardware Availability: Mar-2023

Software Availability: Nov-2025

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
-m64 -std=c18 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xHost(pass 1) -ffp-model=fast
-xsapphirerapids(pass 2) -flto -mprefer-vector-width=512
-qopt-mem-layout-trans=4 -O3 -mfpmath=sse -funroll-loops
-L/opt/intel/oneapi/compiler/2025.3/lib -lqkmalloc
```

C++ benchmarks:

```
706.stockfish_r: -m64 -std=c++17 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xHost(pass 1)
-ffp-model=fast -xsapphirerapids(pass 2) -flto
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -O3
-mfpmath=sse -funroll-loops
-L/opt/intel/oneapi/compiler/2025.3/lib -lqkmalloc
```

707.ntest_r: basepeak = yes

727.cppcheck_r: basepeak = yes

753.ns3_r: Same as 706.stockfish_r

Benchmarks using both C and C++:

```
710.omnetpp_r: -m64 -std=c++17 -std=c18 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xHost(pass 1)
-ffp-model=fast -xsapphirerapids(pass 2) -flto
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -O3
-mfpmath=sse -funroll-loops
-L/opt/intel/oneapi/compiler/2025.3/lib -lqkmalloc
```

721.gcc_r: basepeak = yes

723.llvm_r: Same as 710.omnetpp_r

729.abc_r: basepeak = yes

(Continued on next page)



SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Supermicro
(Test Sponsor: Intel Corporation)

SPECrate®2026_int_base = 446

Hyper SuperServer SYS-221H-TNR

SPECrate®2026_int_peak = 470

CPU2026 License: 13
Test Sponsor: Intel Corporation
Tested by: Intel Corporation

Test Date: Feb-2026
Hardware Availability: Mar-2023
Software Availability: Nov-2025

Peak Optimization Flags (Continued)

734.vpr_r: basepeak = yes

735.gem5_r: Same as 710.omnetpp_r

750.sealcrypto_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2026/results/flags/Intel-ic2025-official-linux64-cpu2026-0.902.html>
<http://www.spec.org/cpu2026/results/flags/Intel-Platform-Settings-EMR-v2.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2026/results/flags/Intel-ic2025-official-linux64-cpu2026-0.902.xml>
<http://www.spec.org/cpu2026/results/flags/Intel-Platform-Settings-EMR-v2.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2026 v0.902.0 on 2026-02-05 20:12:55-0500.
Report generated on 2026-05-04 23:26:36 by CPU2026 PDF formatter (unknown).
Originally published on 2026-05-05.