



SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS C240 M8 (Intel Xeon 6787P 2.0 GHz processor)

SPECrate®2026_int_base = 647

SPECrate®2026_int_peak = 676

CPU2026 License: 9019

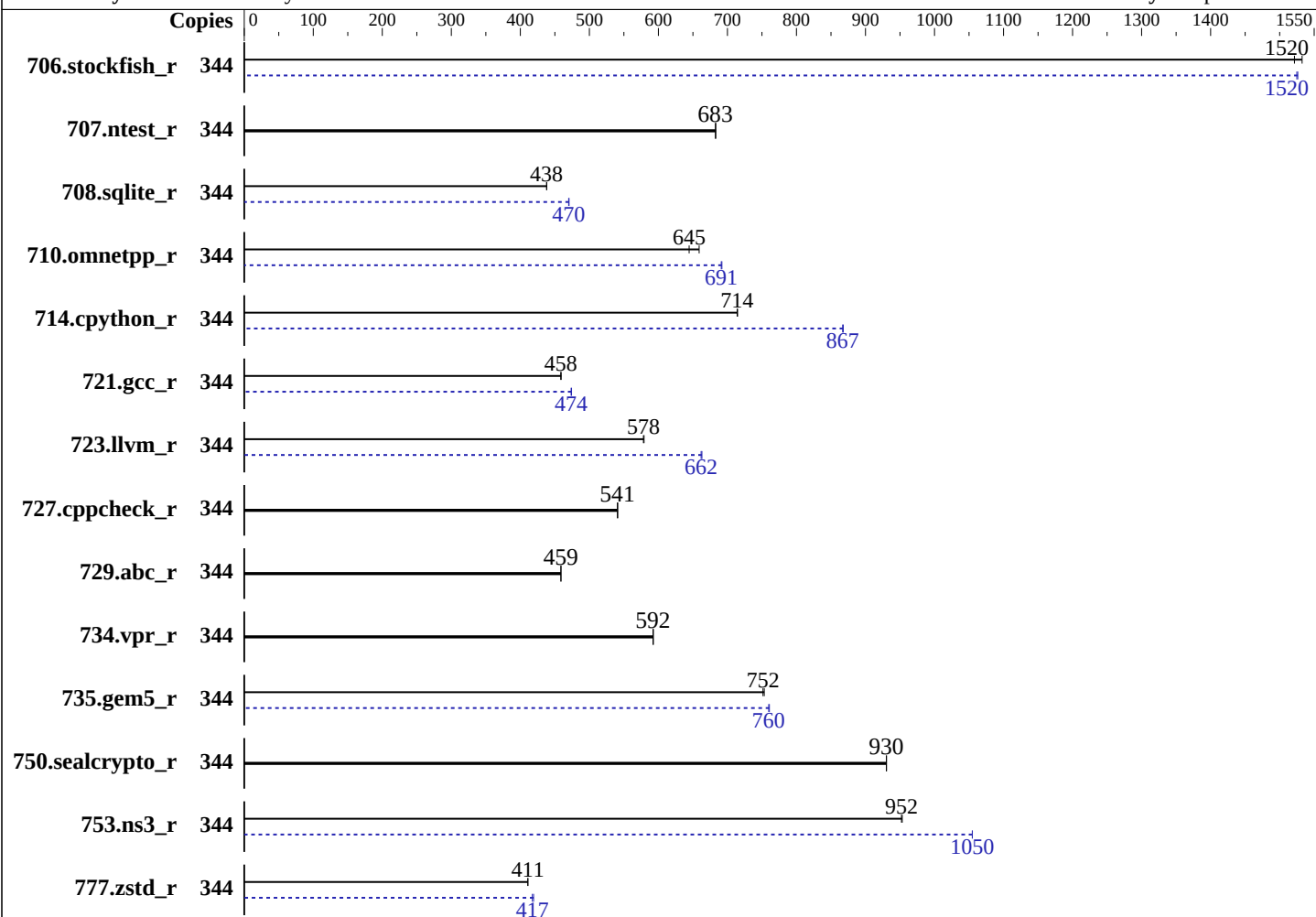
Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jun-2026

Hardware Availability: May-2025

Software Availability: Apr-2026



Hardware

CPU Name: Intel Xeon 6787P
Max MHz: 3800
Nominal: 2000
Enabled: 172 cores, 2 chips, 2 threads/core
Orderable: 1,2 Chips
Cache L1: 64 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 336 MB I+D on chip per chip
Other: None
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-6400B-R)
Storage: 1 x 1.9 TB NVME SSD
Cooling: Air
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP6
6.4.0-150600.21-default
Compiler: C/C++: Version 2026.0 of Intel oneAPI DPC++/C++
Compiler for Linux;
Fortran: Version 2026.0 of Intel Fortran
Compiler for Linux
Compiler Category: Vendor
Firmware: Version 4.3.6b released Apr-2025
File System: xfs
System State: Run Level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None

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Software (Continued)

Power Management: BIOS and OS set to prefer performance at the cost of additional power usage

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
706.stockfish_r	344	283	1530	<u>285</u>	<u>1520</u>			344	284	1530	<u>284</u>	<u>1520</u>		
707.ntest_r	344	298	683	298	683			344	298	683	298	683		
708.sqlite_r	344	415	438	<u>415</u>	<u>438</u>			344	386	471	<u>386</u>	<u>470</u>		
710.omnetpp_r	344	254	659	<u>259</u>	<u>645</u>			344	<u>242</u>	<u>691</u>	242	692		
714.cpython_r	344	231	715	<u>231</u>	<u>714</u>			344	190	868	<u>190</u>	<u>867</u>		
721.gcc_r	344	514	459	<u>515</u>	<u>458</u>			344	<u>498</u>	<u>474</u>	498	474		
723.llvm_r	344	<u>302</u>	<u>578</u>	301	579			344	263	663	<u>264</u>	<u>662</u>		
727.cppcheck_r	344	228	541	<u>228</u>	<u>541</u>			344	228	541	<u>228</u>	<u>541</u>		
729.abc_r	344	344	459	<u>344</u>	<u>459</u>			344	344	459	<u>344</u>	<u>459</u>		
734.vpr_r	344	268	593	<u>268</u>	<u>592</u>			344	268	593	<u>268</u>	<u>592</u>		
735.gem5_r	344	222	753	<u>223</u>	<u>752</u>			344	<u>221</u>	<u>760</u>	220	761		
750.sealcrypto_r	344	198	930	<u>198</u>	<u>930</u>			344	198	930	<u>198</u>	<u>930</u>		
753.ns3_r	344	221	953	<u>221</u>	<u>952</u>			344	200	1050	<u>200</u>	<u>1050</u>		
777.zstd_r	344	539	411	<u>539</u>	<u>411</u>			344	<u>531</u>	<u>417</u>	529	419		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2026/lib"
MALLOC_CONF = "retain:true"



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General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using CentOS Stream 9.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu <etc>
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS settings:

Adjacent cache line prefetcher set to Disabled

DCU Streamer Prefetch set to Disabled

UPI Power Management set to Enabled

Patrol scrub set to Enabled

XPT prefetch set to Disabled

Enhanced CPU performance set to Auto

Sub NUMA clustering set to Enabled

Sysinfo program /home/cpu2026/bin/sysinfo

Rev: 779ab21020787073335a329f3a45e2cd

running on localhost Sun Jun 14 16:14:54 2026

SUT (System Under Test) info as seen by some common utilities.

Table of contents

-
1. `uname -srv`
 2. `w`
 3. Username
 4. `ulimit -a`
 5. `sysinfo process ancestry`
 6. `/proc/cpuinfo`
 7. `lscpu`
 8. `numactl --hardware`
 9. `/proc/meminfo`

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Platform Notes (Continued)

```
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
```

```
-----
1. uname -srvmm
Linux 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09) x86_64
```

```
-----
2. w
 16:14:54 up 4 days, 19:43,  3 users,  load average: 0.72, 0.25, 0.09
USER      TTY      FROM          LOGIN@   IDLE   JCPU   PCPU WHAT
root      tty1      -              Tue20    1:13   5.82s  4.43s -bash
```

```
-----
3. Username
From environment variable $USER:  root
```

```
-----
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 4124303
max locked memory       (kbytes, -l) 8192
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 4124303
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

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Platform Notes (Continued)

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
login -- root
-bash
-bash
runcpu --nobuild --reportable --action validate --define default-platform-flags --copies 344 -c
ic2026.0-graniterapids-cpu2026-1.0.1-rate-20260429.cfg --define smt-on --define cores=172 --define
physicalfirst --define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate
runcpu --nobuild --reportable --action validate --define default-platform-flags --copies 344 --configfile
ic2026.0-graniterapids-cpu2026-1.0.1-rate-20260429.cfg --define smt-on --define cores=172 --define
physicalfirst --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all
--nopower --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2026.015/temlogs/preenv.intrate.015.0.log --lognum 015.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2026
```

6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) 6787P
vendor_id       : GenuineIntel
cpu family      : 6
model           : 173
stepping        : 1
microcode       : 0x1000380
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores       : 86
siblings        : 172
2 physical ids (chips)
344 processors (hardware threads)
physical id 0:  core ids 0-42,64-106
physical id 1:  core ids 0-42,64-106
physical id 0:  apicids 0-85,128-213
physical id 1:  apicids 256-341,384-469
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	46 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	344

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Platform Notes (Continued)

On-line CPU(s) list: 0-343
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
Model name: Intel(R) Xeon(R) 6787P
BIOS Model name: Intel(R) Xeon(R) 6787P CPU @ 2.0GHz
BIOS CPU family: 179
CPU family: 6
Model: 173
Thread(s) per core: 2
Core(s) per socket: 86
Socket(s): 2
Stepping: 1
CPU(s) scaling MHz: 24%
CPU max MHz: 3800.0000
CPU min MHz: 800.0000
BogoMIPS: 4000.00

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2
ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid
rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req vnmi avx512vbmi
umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk
pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile amx_int8 flush_l1d
arch_capabilities

Virtualization: VT-x
L1d cache: 8.1 MiB (172 instances)
L1i cache: 10.8 MiB (172 instances)
L2 cache: 344 MiB (172 instances)
L3 cache: 672 MiB (2 instances)
NUMA node(s): 4
NUMA node0 CPU(s): 0-42,172-214
NUMA node1 CPU(s): 43-85,215-257
NUMA node2 CPU(s): 86-128,258-300
NUMA node3 CPU(s): 129-171,301-343
Vulnerability Gather data sampling: Not affected

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Platform Notes (Continued)

Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSE-eIBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	8.1M	12	Data	1	64	1	64
L1i	64K	10.8M	16	Instruction	1	64	1	64
L2	2M	344M	16	Unified	2	2048	1	64
L3	336M	672M	16	Unified	3	344064	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)
node 0 cpus: 0-42,172-214
node 0 size: 257142 MB
node 0 free: 256011 MB
node 1 cpus: 43-85,215-257
node 1 size: 258025 MB
node 1 free: 256338 MB
node 2 cpus: 86-128,258-300
node 2 size: 258025 MB
node 2 free: 257046 MB
node 3 cpus: 129-171,301-343
node 3 size: 257908 MB
node 3 free: 256900 MB
node distances:
node 0 1 2 3
0: 10 12 21 21
1: 12 10 21 21
2: 21 21 10 12
3: 21 21 12 10

9. /proc/meminfo

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Platform Notes (Continued)

MemTotal: 1055849320 kB

10. who -r
run-level 3 Jun 9 20:32

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
Default Target Status
multi-user running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance iscsi issue-generator kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections nvmmf-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore virtqemu-wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables exchange-bmc-os-info firewall fsidd gpm grub2-once haveged hv-fcopy-daemon hv-kvp-daemon hv-vss-daemon ipmi ipmievad iscsi-init iscsid issue-add-ssh-keys kexec-load ksm kvm_stat libvirt-guests lunmask man-db-create multipathd nfs nfs-blkmap nfs-server nfs-server rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd-generate_opts snmpd snmptrapd strongswan strongswan-starter svnserv systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-nspawn@ systemd-sysextd systemd-time-wait-sync systemd-timesyncd tcsh udisks2 virtinterfaced virtlockd virtlogd virtnetworkd virtnodevdev virtnwfilterd virtsecret virtstoraged
indirect	pcscd systemd-userdbd tftp wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=164e42fe-9eb7-4cc5-9a7d-0451c23f36b9
splash=silent
resume=/dev/disk/by-uuid/b4ff94da-d908-4fdb-a575-9f9e7887f2f9
mitigations=auto
quiet
security=apparmor

14. cpupower frequency-info
analyzing CPU 184:
current policy: frequency should be within 800 MHz and 3.80 GHz.
The governor "performance" may decide which speed to use within this range.

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Platform Notes (Continued)

boost state support:

Supported: yes

Active: yes

15. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	20
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	1
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	0

16. /sys/kernel/mm/transparent_hugepage

defrag	always	defer	defer+madvise	[madvise]	never
enabled	[always]	madvise	never		
hpage_pmd_size	2097152				
shmem_enabled	always	within_size	advise	[never]	deny force

17. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

18. OS release

From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6

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Platform Notes (Continued)

19. Disk information

SPEC is set to: /home/cpu2026

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/nvme2n1p3	xfs	741G	52G	690G	7%	/home

20. /sys/devices/virtual/dmi/id

Vendor: Cisco Systems Inc
Product: UCSC-C240-M8SX
Serial: WZP29209S1T

21. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x 0xCE00 M321R8GA0PB2-CCPPC 64 GB 2 rank 6400

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Cisco Systems, Inc.
BIOS Version: C240M8.4.3.6b.0.0430251037
BIOS Date: 04/30/2025
BIOS Revision: 5.35

Compiler Version Notes

```
C      | 708.sqlite_r(base, peak) 714.cpython_r(base, peak) 777.zstd_r(base,
      | peak)
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2026.0.0 Build 20260331
Copyright (C) 1985-2026 Intel Corporation. All rights reserved.

```
C++    | 706.stockfish_r(base, peak) 707.ntest_r(base, peak)
      | 727.cppcheck_r(base, peak) 753.ns3_r(base, peak)
```

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Compiler Version Notes (Continued)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2026.0.0 Build 20260331
Copyright (C) 1985-2026 Intel Corporation. All rights reserved.

```
=====  
C++, C | 710.omnetpp_r(base, peak) 721.gcc_r(base, peak) 723.llvm_r(base,  
| peak) 729.abc_r(base, peak) 734.vpr_r(base, peak) 735.gem5_r(base,  
| peak) 750.sealcrypto_r(base, peak)  
=====
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2026.0.0 Build 20260331
Copyright (C) 1985-2026 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Benchmarks using both C and C++:

icpx icx

Base Optimization Flags

C benchmarks:

```
-m64 -std=c18 -Wl,-z,muldefs -Wl,-plugin-opt=-inline-threshold=1500  
-xgraniterapids -mprefer-vector-width=512 -O3 -ffp-model=fast -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-fno-strict-aliasing -L/opt/intel/oneapi/compiler/2026.0/lib -lqkmallo
```

C++ benchmarks:

```
-m64 -std=c++17 -Wl,-z,muldefs -Wl,-plugin-opt=-inline-threshold=1500  
-xgraniterapids -mprefer-vector-width=512 -O3 -ffp-model=fast -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/opt/intel/oneapi/compiler/2026.0/lib -lqkmallo
```

Benchmarks using both C and C++:

```
-m64 -std=c++17 -std=c18 -Wl,-z,muldefs  
-Wl,-plugin-opt=-inline-threshold=1500 -xgraniterapids  
-mprefer-vector-width=512 -O3 -ffp-model=fast -flto -mfpmath=sse
```

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SPEC CPU®2026 Integer Rate Result

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Cisco Systems

Cisco UCS C240 M8 (Intel Xeon 6787P 2.0 GHz processor)

SPECrate®2026_int_base = 647

SPECrate®2026_int_peak = 676

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jun-2026

Hardware Availability: May-2025

Software Availability: Apr-2026

Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):

-funroll-loops -qopt-mem-layout-trans=4 -fno-strict-aliasing
-L/opt/intel/oneapi/compiler/2026.0/lib -lqkmalloc

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Benchmarks using both C and C++:

icpx icx

Peak Optimization Flags

C benchmarks:

-m64 -std=c18 -Wl,-z,muldefs -Wl,-plugin-opt=-inline-threshold=1500
-fprofile-generate(pass 1) -fprofile-use=default.profdata(pass 2)
-xHost(pass 1) -ffp-model=fast -xgraniterapids(pass 2) -flto
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -O3 -mfpmath=sse
-funroll-loops -fno-strict-aliasing
-L/opt/intel/oneapi/compiler/2026.0/lib -lqkmalloc

C++ benchmarks:

706.stockfish_r: -m64 -std=c++17 -Wl,-z,muldefs
-Wl,-plugin-opt=-inline-threshold=1500
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xHost(pass 1)
-ffp-model=fast -xgraniterapids(pass 2) -flto
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -O3
-mfpmath=sse -funroll-loops
-L/opt/intel/oneapi/compiler/2026.0/lib -lqkmalloc

707.ntest_r: basepeak = yes

727.cppcheck_r: basepeak = yes

753.ns3_r: Same as 706.stockfish_r

(Continued on next page)



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Hardware Availability: May-2025

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Peak Optimization Flags (Continued)

Benchmarks using both C and C++:

```
710.omnetpp_r: -m64 -std=c++17 -std=c18 -wl, -z, muldefs
-wl, -plugin-opt=-inline-threshold=1500
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xHost(pass 1)
-ffp-model=fast -xgraniterapids(pass 2) -flto
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -O3
-mfpmath=sse -funroll-loops -fno-strict-aliasing
-L/opt/intel/oneapi/compiler/2026.0/lib -lqkmallo
```

```
721.gcc_r: -m64 -std=c++17 -std=c18 -wl, -z, muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xHost(pass 1)
-ffp-model=fast -xgraniterapids(pass 2) -flto
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -O3
-mfpmath=sse -funroll-loops -fno-strict-aliasing
-L/opt/intel/oneapi/compiler/2026.0/lib -lqkmallo
```

723.llvm_r: Same as 721.gcc_r

729.abc_r: basepeak = yes

734.vpr_r: basepeak = yes

735.gem5_r: Same as 710.omnetpp_r

750.sealcrypto_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2026/results/flags/Intel-ic2026-official-linux64-v1.1.html>

<http://www.spec.org/cpu2026/results/flags/Cisco-Platform-Settings-V2.0-GNR-revP.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2026/results/flags/Intel-ic2026-official-linux64-v1.1.xml>

<http://www.spec.org/cpu2026/results/flags/Cisco-Platform-Settings-V2.0-GNR-revP.xml>

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