SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Cisco Systems

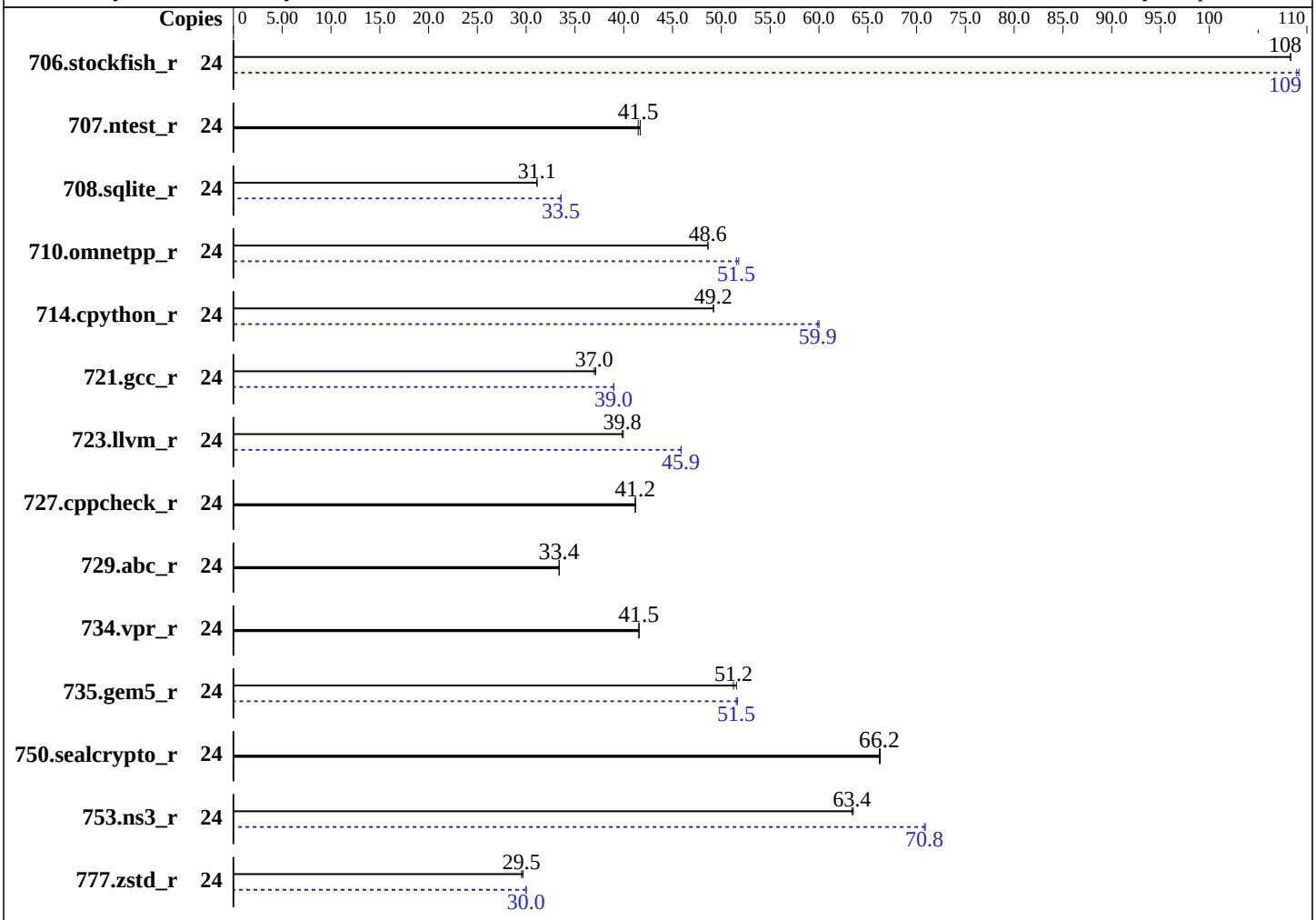
Cisco UCS XE130c M8 (Intel Xeon 6503P-B processor)

SPECrate®2026_int_base = 45.8

SPECrate®2026_int_peak = 48.0

CPU2026 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Aug-2026
Hardware Availability: Feb-2025
Software Availability: Apr-2026



Hardware

CPU Name:	Intel Xeon 6503P-B
Max MHz:	3500
Nominal:	2000
Enabled:	12 cores, 1 chip, 2 threads/core
Orderable:	1 Chips
Cache L1:	64 KB I + 48 KB D on chip per core
L2:	2 MB I+D on chip per core
L3:	48 MB I+D on chip per chip
Other:	None
Memory:	512 GB (8 x 64 GB 2Rx4 PC5-6400B-R, running at 4800)
Storage:	1 x 2.4 TB NVME SSD
Cooling:	Air
Other:	None

Software

OS:	SUSE Linux Enterprise Server 15 SP7 6.4.0-150700.51-default
Compiler:	C/C++: Version 2026.0 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2026.0 of Intel Fortran Compiler for Linux
Compiler Category:	Vendor
Firmware:	Version 6.0.2c released May-2026
File System:	xfs
System State:	Run Level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	None

(Continued on next page)

SPEC CPU®2026 Integer Rate Result

Copyright 2026 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS XE130c M8 (Intel Xeon 6503P-B processor)

SPECrate®2026_int_base = 45.8

SPECrate®2026_int_peak = 48.0

CPU2026 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Aug-2026
Hardware Availability: Feb-2025
Software Availability: Apr-2026

Software (Continued)

Power Management: BIOS and OS set to prefer performance at the cost of additional power usage

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
706.stockfish_r	24	**279**	**108**	279	108			24	**278**	**109**	277	109		
707.ntest_r	24	**343**	**41.5**	341	41.7			24	**343**	**41.5**	341	41.7		
708.sqlite_r	24	**407**	**31.1**	407	31.1			24	**378**	**33.5**	377	33.6		
710.omnetpp_r	24	240	48.6	**240**	**48.6**			24	**226**	**51.5**	225	51.8		
714.cpython_r	24	**234**	**49.2**	234	49.2			24	**192**	**59.9**	192	60.0		
721.gcc_r	24	443	37.1	**445**	**37.0**			24	422	39.0	**423**	**39.0**		
723.llvm_r	24	**305**	**39.8**	305	39.9			24	265	45.9	**265**	**45.9**		
727.cppcheck_r	24	209	41.2	**209**	**41.2**			24	209	41.2	**209**	**41.2**		
729.abc_r	24	**330**	**33.4**	330	33.4			24	**330**	**33.4**	330	33.4		
734.vpr_r	24	266	41.6	**266**	**41.5**			24	266	41.6	**266**	**41.5**		
735.gem5_r	24	**228**	**51.2**	227	51.5			24	226	51.7	**227**	**51.5**		
750.sealcrypto_r	24	**194**	**66.2**	194	66.3			24	**194**	**66.2**	194	66.3		
753.ns3_r	24	232	63.5	**232**	**63.4**			24	**208**	**70.8**	208	70.9		
777.zstd_r	24	**523**	**29.5**	521	29.7			24	515	30.0	**515**	**30.0**		

SPECrate®2026_int_base = 45.8

SPECrate®2026_int_peak = 48.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2026/lib"
MALLOC_CONF = "retain:true"
```

General Notes

```
 Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
 memory using CentOS Stream 9.
 Transparent Huge Pages enabled by default
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
 sync; echo 3>       /proc/sys/vm/drop_caches
 runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
```

Platform Notes

```
BIOS settings:
Adjacent cache line prefetcher set to Disabled
DCU Streamer Prefetch set to Disabled
UPI Power Management set to Enabled
Patrol scrub set to Enabled
XPT prefetch set to Disabled

 Sysinfo program /home/cpu2026/bin/sysinfo
 Rev: 779ab21020787073335a329f3a45e2cd
 running on localhost Sat Aug  8 15:06:30 2026

 SUT (System Under Test) info as seen by some common utilities.

 ------------------------------------------------------------
 Table of contents
 ------------------------------------------------------------
  1. uname -srvm
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)
```

(Continued on next page)

Platform Notes (Continued)

```
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
 14. cpupower frequency-info
 15. sysctl
 16. /sys/kernel/mm/transparent_hugepage
 17. /sys/kernel/mm/transparent_hugepage/khugepaged
 18. OS release
 19. Disk information
 20. /sys/devices/virtual/dmi/id
 21. dmidecode
 22. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -srvm
  Linux 6.4.0-150700.51-default #1 SMP PREEMPT_DYNAMIC Wed Apr 30 21:35:43 UTC 2025 (6930611) x86_64

------------------------------------------------------------
2. w
   15:06:30 up 50 min,  3 users,  load average: 0.00, 0.01, 0.07
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                14:17    6.00s  1.72s  0.02s -bash

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 2061684
  max locked memory       (kbytes, -l) 8192
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 2061684
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited

------------------------------------------------------------
```

(Continued on next page)

Platform Notes (Continued)

```
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize=42
 login -- root
 -bash
 -bash
 runcpu --nobuild --reportable --action validate --define default-platform-flags --copies 24 -c
   ic2026.0-graniterapids-cpu2026-1.0.1-rate-20260429.cfg --define smt-on --define cores=12 --define
   physicalfirst --define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate
 runcpu --nobuild --reportable --action validate --define default-platform-flags --copies 24 --configfile
   ic2026.0-graniterapids-cpu2026-1.0.1-rate-20260429.cfg --define smt-on --define cores=12 --define
   physicalfirst --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all
   --nopower --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
    $SPEC/tmp/CPU2026.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2026

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : INTEL(R) XEON(R) 6503P-B
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 174
    stepping        : 1
    microcode       : 0x10002b3
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
    cpu cores       : 12
    siblings        : 24
    1 physical ids (chips)
    24 processors (hardware threads)
    physical id 0: core ids 0-11
    physical id 0: apicids 0-23
 Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.40.4:
  Architecture:                         x86_64
  CPU op-mode(s):                       32-bit, 64-bit
  Address sizes:                        46 bits physical, 57 bits virtual
  Byte Order:                           Little Endian
  CPU(s):                               24
  On-line CPU(s) list:                  0-23
  Vendor ID:                            GenuineIntel
  Model name:                           INTEL(R) XEON(R) 6503P-B
  CPU family:                           6
```

(Continued on next page)

Platform Notes (Continued)

```
Model:                                174
Thread(s) per core:                   2
Core(s) per socket:                   12
Socket(s):                            1
Stepping:                             1
CPU(s) scaling MHz:                   54%
CPU max MHz:                          3500.0000
CPU min MHz:                          800.0000
BogoMIPS:                             4000.00
Flags:                                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                      pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
                                      pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
                                      nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
                                      pclmulqdq dtes64 monitor ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr
                                      pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer
                                      aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb
                                      cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp
                                      ibrs_enhanced fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid
                                      cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb
                                      intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1
                                      xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                                      split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
                                      arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip
                                      pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
                                      avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
                                      cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk
                                      pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile amx_int8 flush_l1d
                                      arch_capabilities
L1d cache:                            576 KiB (12 instances)
L1i cache:                            768 KiB (12 instances)
L2 cache:                             24 MiB (12 instances)
L3 cache:                             48 MiB (1 instance)
NUMA node(s):                         1
NUMA node0 CPU(s):                    0-23
Vulnerability Gather data sampling:   Not affected
Vulnerability Itlb multihit:          Not affected
Vulnerability L1tf:                   Not affected
Vulnerability Mds:                    Not affected
Vulnerability Meltdown:               Not affected
Vulnerability Mmio stale data:        Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed:               Not affected
Vulnerability Spec rstack overflow:   Not affected
Vulnerability Spec store bypass:      Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:             Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:             Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;
                                      PBRSB-eIBRS Not affected; BHI BHI_DIS_S
```

(Continued on next page)

Platform Notes (Continued)

```
  Vulnerability Srbds:                 Not affected
  Vulnerability Tsx async abort:       Not affected

From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
     L1d       48K     576K   12 Data            1    64        1             64
     L1i       64K     768K   16 Instruction     1    64        1             64
     L2         2M      24M   16 Unified         2  2048        1             64
     L3        48M      48M   16 Unified         3 49152        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0-23
  node 0 size: 515448 MB
  node 0 free: 477601 MB
  node distances:
  node   0
    0:  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       527819288 kB

------------------------------------------------------------
10. who -r
  run-level 3 Aug 8 14:16

------------------------------------------------------------
11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance iscsi
                   issue-generator kbdsettings klog lvm2-monitor nscd nvmefc-boot-connections
                   nvmf-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore
                   virtqemud wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
  enabled-runtime  systemd-remount-fs
  disabled         autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                   chronyd console-getty cups cups-browsed debug-shell dnsmasq ebtables exchange-bmc-os-info
                   firewalld fsidd gpm grub2-once haveged hv_fcopy_daemon hv_fcopy_uio_daemon hv_kvp_daemon
                   hv_vss_daemon ipmi ipmievd iscsi-init iscsid issue-add-ssh-keys kexec-load ksm kvm_stat
                   libvirt-guests lunmask man-db-create multipathd nfs nfs-blkmap nfs-server nfsserver
```

(Continued on next page)

Platform Notes (Continued)

```
                rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd_generate_opts snmpd
                 snmptrapd strongswan strongswan-starter svnserve systemd-boot-check-no-failures
                   systemd-confext systemd-network-generator systemd-nspawn@ systemd-sysext
                systemd-time-wait-sync systemd-timesyncd udisks2 virtinterfaced virtlockd virtlogd
                   virtnetworkd virtnodedevd virtnwfilterd virtsecretd virtstoraged
  indirect         pcscd systemd-userdbd tftp wickedd

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-6.4.0-150700.51-default
  root=UUID=3b4ce6e1-b593-4070-a649-689a9dc5aeea
  splash=silent
  resume=/dev/disk/by-uuid/af9e348b-357a-48ea-9411-a8ab3cd0fb15
  mitigations=auto
  quiet
  security=apparmor

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 16:
    current policy: frequency should be within 800 MHz and 3.50 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. sysctl
  kernel.numa_balancing               0
  kernel.randomize_va_space           2
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio          10
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                     20
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                      60
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
```

(Continued on next page)

Platform Notes (Continued)

```
  vm.zone_reclaim_mode                0

------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvise [madvise] never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
18. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP7

------------------------------------------------------------
19. Disk information
SPEC is set to: /home/cpu2026
  Filesystem     Type  Size  Used Avail Use% Mounted on
  /dev/nvme0n1p3 xfs   2.4T   80G  2.4T   4% /home

------------------------------------------------------------
20. /sys/devices/virtual/dmi/id
    Vendor:         Cisco Systems Inc
    Product:        UCSXE-130C-M8-12
    Product Family: Cisco Edge Server
    Serial:         WZP294093N6

------------------------------------------------------------
21. dmidecode
  Additional information from dmidecode 3.6 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    8x 0xCE00 M321R8GA0PB2-CCPKC 64 GB 2 rank 6400, configured at 4800
```

(Continued on next page)

Platform Notes (Continued)

```
 ------------------------------------------------------------
 22. BIOS
 (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:       Cisco Systems, Inc.
    BIOS Version:      XE1X0M8.6.0.2c.0.0526261754
    BIOS Date:         05/26/2026
    BIOS Revision:     5.35
```

Compiler Version Notes

```
==============================================================================
C      | 708.sqlite_r(base, peak) 714.cpython_r(base, peak) 777.zstd_r(base,
       | peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2026.0.0 Build 20260331
Copyright (C) 1985-2026 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C++    | 706.stockfish_r(base, peak) 707.ntest_r(base, peak)
       | 727.cppcheck_r(base, peak) 753.ns3_r(base, peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2026.0.0 Build 20260331
Copyright (C) 1985-2026 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C++, C | 710.omnetpp_r(base, peak) 721.gcc_r(base, peak) 723.llvm_r(base,
       | peak) 729.abc_r(base, peak) 734.vpr_r(base, peak) 735.gem5_r(base,
       | peak) 750.sealcrypto_r(base, peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2026.0.0 Build 20260331
Copyright (C) 1985-2026 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

(Continued on next page)

Base Compiler Invocation (Continued)

Benchmarks using both C and C++:
icpx icx

Base Optimization Flags

C benchmarks:
-m64 -std=c18 -Wl,-z,muldefs -Wl,-plugin-opt=-inline-threshold=1500
-xgraniterapids -mprefer-vector-width=512 -O3 -ffp-model=fast -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-fno-strict-aliasing -L/opt/intel/oneapi/compiler/2026.0/lib -lqkmalloc

C++ benchmarks:
-m64 -std=c++17 -Wl,-z,muldefs -Wl,-plugin-opt=-inline-threshold=1500
-xgraniterapids -mprefer-vector-width=512 -O3 -ffp-model=fast -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2026.0/lib -lqkmalloc

Benchmarks using both C and C++:
-m64 -std=c++17 -std=c18 -Wl,-z,muldefs
-Wl,-plugin-opt=-inline-threshold=1500 -xgraniterapids
-mprefer-vector-width=512 -O3 -ffp-model=fast -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -fno-strict-aliasing
-L/opt/intel/oneapi/compiler/2026.0/lib -lqkmalloc

Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Benchmarks using both C and C++:
icpx icx

Peak Optimization Flags

C benchmarks:
-m64 -std=c18 -Wl,-z,muldefs -Wl,-plugin-opt=-inline-threshold=1500
-fprofile-generate(pass 1) -fprofile-use=default.profdata(pass 2)
-xHost(pass 1) -ffp-model=fast -xgraniterapids(pass 2) -flto
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -O3 -mfpmath=sse
-funroll-loops -fno-strict-aliasing
-L/opt/intel/oneapi/compiler/2026.0/lib -lqkmalloc

C++ benchmarks:

706.stockfish_r: -m64 -std=c++17 -Wl,-z,muldefs
-Wl,-plugin-opt=-inline-threshold=1500
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xHost(pass 1)
-ffp-model=fast -xgraniterapids(pass 2) -flto
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -O3
-mfpmath=sse -funroll-loops
-L/opt/intel/oneapi/compiler/2026.0/lib -lqkmalloc

707.ntest_r: basepeak = yes

727.cppcheck_r: basepeak = yes

753.ns3_r: Same as 706.stockfish_r

Benchmarks using both C and C++:

710.omnetpp_r: -m64 -std=c++17 -std=c18 -Wl,-z,muldefs
-Wl,-plugin-opt=-inline-threshold=1500
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xHost(pass 1)
-ffp-model=fast -xgraniterapids(pass 2) -flto
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -O3
-mfpmath=sse -funroll-loops -fno-strict-aliasing
-L/opt/intel/oneapi/compiler/2026.0/lib -lqkmalloc

721.gcc_r: -m64 -std=c++17 -std=c18 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xHost(pass 1)
-ffp-model=fast -xgraniterapids(pass 2) -flto
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -O3
-mfpmath=sse -funroll-loops -fno-strict-aliasing
-L/opt/intel/oneapi/compiler/2026.0/lib -lqkmalloc

723.llvm_r: Same as 721.gcc_r

(Continued on next page)

Peak Optimization Flags (Continued)

729.abc_r: `basepeak = yes`

734.vpr_r: `basepeak = yes`

735.gem5_r: `Same as 710.omnetpp_r`

750.sealcrypto_r: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2026/results/flags/Intel-ic2026-official-linux64-v1.1.html`
`http://www.spec.org/cpu2026/results/flags/Cisco-Platform-Settings-V2.0-GNR-revP.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2026/results/flags/Intel-ic2026-official-linux64-v1.1.xml`
`http://www.spec.org/cpu2026/results/flags/Cisco-Platform-Settings-V2.0-GNR-revP.xml`

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2026 v1.0.1 on 2026-08-08 15:06:30-0400.
Report generated on 2026-08-26 13:57:16 by CPU2026 PS formatter (86729d3b).
Originally published on 2026-08-25.