



SPEC CPU®2026 Floating Point Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS XE130c M8 (Intel Xeon 6543P-B processor)

SPECspeed®2026_fp_base = 3.75

SPECspeed®2026_fp_peak = 3.75

CPU2026 License: 9019

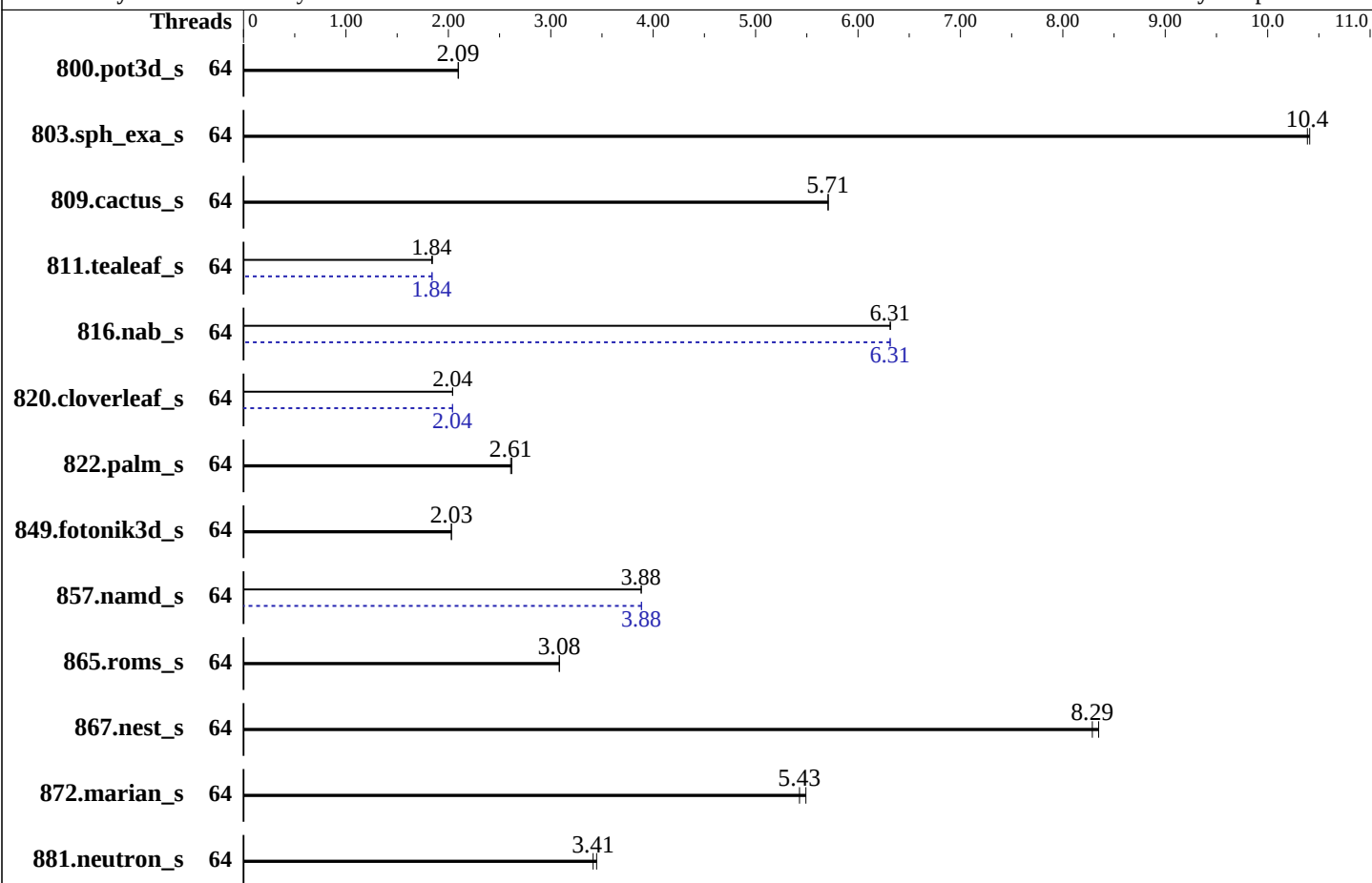
Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Aug-2026

Hardware Availability: Feb-2025

Software Availability: Apr-2026



Hardware

CPU Name: Intel Xeon 6543P-B
Max MHz: 3300
Nominal: 2000
Enabled: 32 cores, 1 chip, 2 threads/core
Orderable: 1 Chips
Cache L1: 64 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 128 MB I+D on chip per chip
Other: None
Memory: 512 GB (8 x 64 GB 2Rx4 PC5-6400B-R, running at 5200)
Storage: 1 x 2.4 TB NVME SSD
Cooling: Air
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP7 6.4.0-150700.51-default
Compiler: C/C++: Version 2026.0 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2026.0 of Intel Fortran Compiler for Linux
Compiler Category: Vendor
Firmware: Version 6.0.2c released May-2026
File System: xfs
System State: Run Level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator v5.3
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



SPEC CPU®2026 Floating Point Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS XE130c M8 (Intel Xeon 6543P-B processor)

SPECspeed®2026_fp_base = 3.75

SPECspeed®2026_fp_peak = 3.75

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Aug-2026

Hardware Availability: Feb-2025

Software Availability: Apr-2026

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
800.pot3d_s	64	321	2.10	<u>321</u>	<u>2.09</u>			64	321	2.10	<u>321</u>	<u>2.09</u>		
803.sph_exa_s	64	<u>119</u>	<u>10.4</u>	119	10.4			64	<u>119</u>	<u>10.4</u>	119	10.4		
809.cactus_s	64	<u>197</u>	<u>5.71</u>	196	5.71			64	<u>197</u>	<u>5.71</u>	196	5.71		
811.tealeaf_s	64	302	1.84	<u>303</u>	<u>1.84</u>			64	302	1.84	<u>303</u>	<u>1.84</u>		
816.nab_s	64	<u>178</u>	<u>6.31</u>	178	6.32			64	178	6.31	<u>178</u>	<u>6.31</u>		
820.cloverleaf_s	64	<u>420</u>	<u>2.04</u>	420	2.04			64	420	2.04	<u>420</u>	<u>2.04</u>		
822.palm_s	64	469	2.62	<u>471</u>	<u>2.61</u>			64	469	2.62	<u>471</u>	<u>2.61</u>		
849.fotonik3d_s	64	<u>325</u>	<u>2.03</u>	325	2.03			64	<u>325</u>	<u>2.03</u>	325	2.03		
857.namd_s	64	374	3.88	<u>374</u>	<u>3.88</u>			64	<u>374</u>	<u>3.88</u>	374	3.89		
865.roms_s	64	<u>354</u>	<u>3.08</u>	353	3.08			64	<u>354</u>	<u>3.08</u>	353	3.08		
867.nest_s	64	<u>261</u>	<u>8.29</u>	259	8.35			64	<u>261</u>	<u>8.29</u>	259	8.35		
872.marian_s	64	197	5.49	<u>199</u>	<u>5.43</u>			64	197	5.49	<u>199</u>	<u>5.43</u>		
881.neutron_s	64	236	3.45	<u>239</u>	<u>3.41</u>			64	236	3.45	<u>239</u>	<u>3.41</u>		

SPECspeed®2026_fp_base = 3.75

SPECspeed®2026_fp_peak = 3.75

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/home/cpu2026/lib"

MALLOC_CONF = "retain:true"

OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using CentOS Stream 9.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)

(Continued on next page)



SPEC CPU®2026 Floating Point Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS XE130c M8 (Intel Xeon 6543P-B processor)

SPECspeed®2026_fp_base = 3.75

SPECspeed®2026_fp_peak = 3.75

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Aug-2026

Hardware Availability: Feb-2025

Software Availability: Apr-2026

General Notes (Continued)

is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS settings:

Adjacent cache line prefetcher set to Enabled

DCU Streamer Prefetch set to Disabled

UPI Power Management set to Enabled

XPT prefetch set to Disabled

Patrol scrub set to Disabled

Sysinfo program /home/cpu2026/bin/sysinfo

Rev: 779ab21020787073335a329f3a45e2cd

running on localhost Wed Aug 12 03:49:41 2026

SUT (System Under Test) info as seen by some common utilities.

Table of contents

-
1. uname -srvn
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
 14. cpupower frequency-info
 15. sysctl
 16. /sys/kernel/mm/transparent_hugepage
 17. /sys/kernel/mm/transparent_hugepage/khugepaged
 18. OS release
 19. Disk information
 20. /sys/devices/virtual/dmi/id

(Continued on next page)



SPEC CPU®2026 Floating Point Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS XE130c M8 (Intel Xeon 6543P-B processor)

SPECspeed®2026_fp_base = 3.75

SPECspeed®2026_fp_peak = 3.75

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Aug-2026

Hardware Availability: Feb-2025

Software Availability: Apr-2026

Platform Notes (Continued)

21. dmidecode

22. BIOS

1. uname -srvrn

Linux 6.4.0-150700.51-default #1 SMP PREEMPT_DYNAMIC Wed Apr 30 21:35:43 UTC 2025 (6930611) x86_64

2. w

03:49:41 up 3 days, 13:37, 5 users, load average: 0.00, 0.00, 0.00

USER	TTY	FROM	LOGIN@	IDLE	JCPU	PCPU	WHAT
root	tty1	-	Sat14	5.00s	2.02s	0.00s	-bash
root	tty2	-	Sat23	29:47m	0.09s	0.09s	-bash

3. Username

From environment variable \$USER: root

4. ulimit -a

core file size	(blocks, -c)	unlimited
data seg size	(kbytes, -d)	unlimited
scheduling priority	(-e)	0
file size	(blocks, -f)	unlimited
pending signals	(-i)	2061588
max locked memory	(kbytes, -l)	8192
max memory size	(kbytes, -m)	unlimited
open files	(-n)	1024
pipe size	(512 bytes, -p)	8
POSIX message queues	(bytes, -q)	819200
real-time priority	(-r)	0
stack size	(kbytes, -s)	unlimited
cpu time	(seconds, -t)	unlimited
max user processes	(-u)	2061588
virtual memory	(kbytes, -v)	unlimited
file locks	(-x)	unlimited

5. sysinfo process ancestry

/usr/lib/systemd/systemd --switched-root --system --deserialize=42

login -- root

-bash

-bash

runcpu --nobuild --reportable --action validate --define default-platform-flags -c

ic2026.0-graniterapids-cpu2026-1.0.1-speed-20260429.cfg --threads 64 --define cores=32 --tune base,peak -o

all --define smt-on --define drop_caches fpspeed

(Continued on next page)



SPEC CPU®2026 Floating Point Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS XE130c M8 (Intel Xeon 6543P-B processor)

SPECspeed®2026_fp_base = 3.75

SPECspeed®2026_fp_peak = 3.75

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Aug-2026

Hardware Availability: Feb-2025

Software Availability: Apr-2026

Platform Notes (Continued)

```
runcpu --nobuild --reportable --action validate --define default-platform-flags --configfile
ic2026.0-graniterapids-cpu2026-1.0.1-speed-20260429.cfg --threads 64 --define cores=32 --tune base,peak
--output_format all --define smt-on --define drop_caches --nopower --runmode speed --tune base:peak --size
refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2026.003/temlogs/preenv.fpspeed.003.0.log --lognum 003.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2026
```

6. /proc/cpuinfo

```
model name      : INTEL(R) XEON(R) 6543P-B
vendor_id       : GenuineIntel
cpu family      : 6
model           : 174
stepping        : 1
microcode       : 0x10002b3
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores       : 32
siblings        : 64
1 physical ids (chips)
64 processors (hardware threads)
physical id 0: core ids 0-31
physical id 0: apicids 0-63
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.40.4:

```
Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:      46 bits physical, 57 bits virtual
Byte Order:        Little Endian
CPU(s):            64
On-line CPU(s) list: 0-63
Vendor ID:         GenuineIntel
Model name:        INTEL(R) XEON(R) 6543P-B
CPU family:        6
Model:            174
Thread(s) per core: 2
Core(s) per socket: 32
Socket(s):         1
Stepping:          1
CPU(s) scaling MHz: 40%
CPU max MHz:       3300.0000
CPU min MHz:       800.0000
```

(Continued on next page)



SPEC CPU®2026 Floating Point Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS XE130c M8 (Intel Xeon 6543P-B processor)

SPECspeed®2026_fp_base = 3.75

SPECspeed®2026_fp_peak = 3.75

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Aug-2026

Hardware Availability: Feb-2025

Software Availability: Apr-2026

Platform Notes (Continued)

BogoMIPS:

4000.00

Flags:

fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
nopl xtopology nonstop_tsc cpuid aperfperf tsc_known_freq pni
pclmulqdq dtes64 monitor ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr
pdc_m pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer
aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb
cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp
ibrs_enhanced fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid
cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb
intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1
xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip
pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk
pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile amx_int8 flush_l1d

L1d cache:

arch_capabilities
1.5 MiB (32 instances)

L1i cache:

2 MiB (32 instances)

L2 cache:

64 MiB (32 instances)

L3 cache:

128 MiB (1 instance)

NUMA node(s):

1

NUMA node0 CPU(s):

0-63

Vulnerability Gather data sampling:

Not affected

Vulnerability Itlb multihit:

Not affected

Vulnerability L1tf:

Not affected

Vulnerability Mds:

Not affected

Vulnerability Meltdown:

Not affected

Vulnerability Mmio stale data:

Not affected

Vulnerability Reg file data sampling:

Not affected

Vulnerability Retbleed:

Not affected

Vulnerability Spec rstack overflow:

Not affected

Vulnerability Spec store bypass:

Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1:

Mitigation; usercopy/swaps barriers and __user pointer sanitization

Vulnerability Spectre v2:

Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;
PBR SB-eIBRS Not affected; BHI BHI_DIS_S

Vulnerability Srbds:

Not affected

Vulnerability Tsx async abort:

Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.5M	12	Data	1	64	1	64
L1i	64K	2M	16	Instruction	1	64	1	64
L2	2M	64M	16	Unified	2	2048	1	64

(Continued on next page)



SPEC CPU®2026 Floating Point Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS XE130c M8 (Intel Xeon 6543P-B processor)

SPECspeed®2026_fp_base = 3.75

SPECspeed®2026_fp_peak = 3.75

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Aug-2026

Hardware Availability: Feb-2025

Software Availability: Apr-2026

Platform Notes (Continued)

L3	128M	128M	16 Unified	3 131072	1	64
----	------	------	------------	----------	---	----

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 1 nodes (0)

node 0 cpus: 0-63

node 0 size: 515424 MB

node 0 free: 498132 MB

node distances:

node 0

0: 10

9. /proc/meminfo

MemTotal: 527794564 kB

10. who -r

run-level 3 Aug 8 14:12

11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)

Default Target	Status
----------------	--------

multi-user	running
------------	---------

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
-------	------------

enabled	YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance iscsi issue-generator kbdsettings klog lvm2-monitor nsd nvme-fc-boot-connections nvmmf-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore virtgemud wickedd-wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
---------	--

enabled-runtime	systemd-remount-fs
-----------------	--------------------

disabled	autofs autotast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables exchange-bmc-os-info firewalld fsidd gpm grub2-once haveged hv_fcopy_daemon hv_fcopy_uio_daemon hv_kv_daemon hv_vss_daemon ipmi ipmievds iscsi-init iscsid issue-add-ssh-keys kexec-load ksm kvm_stat libvirt-guests lunmask man-db-create multipathd nfs nfs-blkmap nfs-server nfs-server rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd_generate_opts snmpd snmptrapd strongswan strongswan-starter svnserv systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-nspawn@ systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2 virtinterfaced virtlockd virtlogd virtnetworkd virtnodevdev virtnwfilterd virtsecret virtstorage vncserver@
----------	--

indirect	pcscd systemd-userdbd tftp wickedd
----------	------------------------------------

(Continued on next page)



SPEC CPU®2026 Floating Point Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS XE130c M8 (Intel Xeon 6543P-B processor)

SPECspeed®2026_fp_base = 3.75

SPECspeed®2026_fp_peak = 3.75

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Aug-2026

Hardware Availability: Feb-2025

Software Availability: Apr-2026

Platform Notes (Continued)

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=/boot/vmlinuz-6.4.0-150700.51-default

root=UUID=86ab0dee-90cf-4b4c-946a-7d30ab25e77b

splash=silent

resume=/dev/disk/by-uuid/5dc9ab87-6c70-4c1c-98ed-839677e40db9

mitigations=auto

quiet

security=apparmor

14. cpupower frequency-info

analyzing CPU 37:

current policy: frequency should be within 800 MHz and 3.30 GHz.

The governor "performance" may decide which speed to use within this range.

boost state support:

Supported: yes

Active: yes

15. sysctl

kernel.numa_balancing 0

kernel.randomize_va_space 2

vm.compaction_proactiveness 20

vm.dirty_background_bytes 0

vm.dirty_background_ratio 10

vm.dirty_bytes 0

vm.dirty_expire_centisecs 3000

vm.dirty_ratio 20

vm.dirty_writeback_centisecs 500

vm.dirtytime_expire_seconds 43200

vm.extfrag_threshold 500

vm.min_unmapped_ratio 1

vm.nr_hugepages 0

vm.nr_hugepages_mempolicy 0

vm.nr_overcommit_hugepages 0

vm.swappiness 1

vm.watermark_boost_factor 15000

vm.watermark_scale_factor 10

vm.zone_reclaim_mode 0

16. /sys/kernel/mm/transparent_hugepage

defrag [always] defer defer+madvice madvice never

enabled [always] madvice never

hpage_pmd_size 2097152

shmem_enabled always within_size advise [never] deny force

(Continued on next page)



SPEC CPU®2026 Floating Point Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS XE130c M8 (Intel Xeon 6543P-B processor)

SPECspeed®2026_fp_base = 3.75

SPECspeed®2026_fp_peak = 3.75

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Aug-2026

Hardware Availability: Feb-2025

Software Availability: Apr-2026

Platform Notes (Continued)

17. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

18. OS release

From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP7

19. Disk information

SPEC is set to: /home/cpu2026

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/nvme0n1p3	xfs	2.4T	123G	2.3T	6%	/home

20. /sys/devices/virtual/dmi/id

Vendor:	Cisco Systems Inc
Product:	UCSXE-130C-M8-32
Product Family:	Cisco Edge Server
Serial:	WZP301390T5

21. dmidecode

Additional information from dmidecode 3.6 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

8x 0xCE00 M321R8GA0PB2-CCPKC 64 GB 2 rank 6400, configured at 5200

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor:	Cisco Systems, Inc.
BIOS Version:	XE1X0M8.6.0.2c.0.0526261754
BIOS Date:	05/26/2026
BIOS Revision:	5.35



SPEC CPU®2026 Floating Point Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS XE130c M8 (Intel Xeon 6543P-B processor)

SPECspeed®2026_fp_base = 3.75

SPECspeed®2026_fp_peak = 3.75

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Aug-2026

Hardware Availability: Feb-2025

Software Availability: Apr-2026

Compiler Version Notes

```
=====
C      | 811.tealeaf_s(base, peak) 816.nab_s(base, peak) 881.neutron_s(base,
      | peak)
=====
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2026.0.0 Build 20260331
Copyright (C) 1985-2026 Intel Corporation. All rights reserved.
=====
```

```
=====
C++    | 803.sph_exa_s(base, peak) 857.namd_s(base, peak) 867.nest_s(base,
      | peak) 872.marian_s(base, peak)
=====
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2026.0.0 Build 20260331
Copyright (C) 1985-2026 Intel Corporation. All rights reserved.
=====
```

```
=====
C++, C | 809.cactus_s(base, peak)
=====
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2026.0.0 Build 20260331
Copyright (C) 1985-2026 Intel Corporation. All rights reserved.
=====
```

```
=====
Fortran | 800.pot3d_s(base, peak) 820.cloverleaf_s(base, peak)
      | 822.palm_s(base, peak) 849.fotonik3d_s(base, peak) 865.roms_s(base,
      | peak)
=====
```

```
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
2026.0.0 Build 20260331
Copyright (C) 1985-2026 Intel Corporation. All rights reserved.
=====
```

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

(Continued on next page)



SPEC CPU®2026 Floating Point Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS XE130c M8 (Intel Xeon 6543P-B processor)

SPECspeed®2026_fp_base = 3.75

SPECspeed®2026_fp_peak = 3.75

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Aug-2026

Hardware Availability: Feb-2025

Software Availability: Apr-2026

Base Compiler Invocation (Continued)

Benchmarks using both C and C++:

icpx icx

Base Optimization Flags

C benchmarks:

```
-m64 -std=c18 -Wl,-z,muldefs -Wl,-plugin-opt=-inline-threshold=1500
-xgraniterapids -mprefer-vector-width=512 -O3 -ffp-model=fast -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc-5.3.0/lib -ljemalloc
```

C++ benchmarks:

```
-m64 -std=c++17 -Wl,-z,muldefs -Wl,-plugin-opt=-inline-threshold=1500
-xgraniterapids -mprefer-vector-width=512 -O3 -ffp-model=fast -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -pthread
-fiopenmp -DSPEC_OPENMP -L/usr/local/jemalloc-5.3.0/lib -ljemalloc
```

Fortran benchmarks:

```
-m64 -stand f18 -Wl,-z,muldefs -Wl,-plugin-opt=-inline-threshold=1500
-xgraniterapids -mprefer-vector-width=512 -O3 -ffp-model=fast -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -DSPEC_OPENMP
-fiopenmp -L/usr/local/jemalloc-5.3.0/lib -ljemalloc
```

Benchmarks using both C and C++:

```
-m64 -std=c++17 -std=c18 -Wl,-z,muldefs
-Wl,-plugin-opt=-inline-threshold=1500 -xgraniterapids
-mprefer-vector-width=512 -O3 -ffp-model=fast -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-pthread -L/usr/local/jemalloc-5.3.0/lib -ljemalloc
```

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

(Continued on next page)



SPEC CPU®2026 Floating Point Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS XE130c M8 (Intel Xeon 6543P-B processor)

SPECspeed®2026_fp_base = 3.75

SPECspeed®2026_fp_peak = 3.75

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Aug-2026

Hardware Availability: Feb-2025

Software Availability: Apr-2026

Peak Compiler Invocation (Continued)

Benchmarks using both C and C++:

icpx icx

Peak Optimization Flags

C benchmarks:

```
811.tealeaf_s: -m64 -std=c18 -Wl, -z,muldefs
-Wl, -plugin-opt=-inline-threshold=1500
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xHost(pass 1)
-ffp-model=fast -xgraniterapids(pass 2) -flto
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -O3
-mfpmath=sse -funroll-loops -fiopenmp -DSPEC_OPENMP
-L/usr/local/jemalloc-5.3.0/lib -ljemalloc
```

816.nab_s: Same as 811.tealeaf_s

881.neutron_s: basepeak = yes

C++ benchmarks:

803.sph_exa_s: basepeak = yes

```
857.namd_s: -m64 -std=c++17 -Wl, -z,muldefs
-Wl, -plugin-opt=-inline-threshold=1500
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xHost(pass 1)
-ffp-model=fast -xgraniterapids(pass 2) -flto
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -O3
-mfpmath=sse -funroll-loops -pthread -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc-5.3.0/lib -ljemalloc
```

867.nest_s: basepeak = yes

872.marian_s: basepeak = yes

Fortran benchmarks:

800.pot3d_s: basepeak = yes

(Continued on next page)



SPEC CPU®2026 Floating Point Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS XE130c M8 (Intel Xeon 6543P-B processor)

SPECspeed®2026_fp_base = 3.75

SPECspeed®2026_fp_peak = 3.75

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Aug-2026

Hardware Availability: Feb-2025

Software Availability: Apr-2026

Peak Optimization Flags (Continued)

```
820.cloverleaf_s: -m64 -stand f18 -wl, -z, muldefs
-wl, -plugin-opt=-inline-threshold=1500
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xHost(pass 1)
-ffp-model=fast -xgraniterapids(pass 2) -flt0
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -03
-mfpmath=sse -funroll-loops -nostandard-realloc-lhs
-align array32byte -auto -DSPEC_OPENMP -fiopenmp
-L/usr/local/jemalloc-5.3.0/lib -ljemalloc
```

822.palm_s: basepeak = yes

849.fotonik3d_s: basepeak = yes

865.roms_s: basepeak = yes

Benchmarks using both C and C++:

809.cactus_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2026/results/flags/Intel-ic2026-official-linux64-v1.1.html>

<http://www.spec.org/cpu2026/results/flags/Cisco-Platform-Settings-V2.0-GNR-revP.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2026/results/flags/Intel-ic2026-official-linux64-v1.1.xml>

<http://www.spec.org/cpu2026/results/flags/Cisco-Platform-Settings-V2.0-GNR-revP.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2026 v1.0.1 on 2026-08-12 03:49:40-0400.

Report generated on 2026-09-10 10:48:43 by CPU2026 PDF formatter (unknown).

Originally published on 2026-09-10.