



# SPEChpc™ 2021 Tiny Result

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## Intel

Hatch: Intel Server D50DNP1SB (Xeon Platinum 8480+)

SPEChpc 2021\_tny\_base = 60.4

SPEChpc 2021\_tny\_peak = 64.5

hpc2021 License: 13

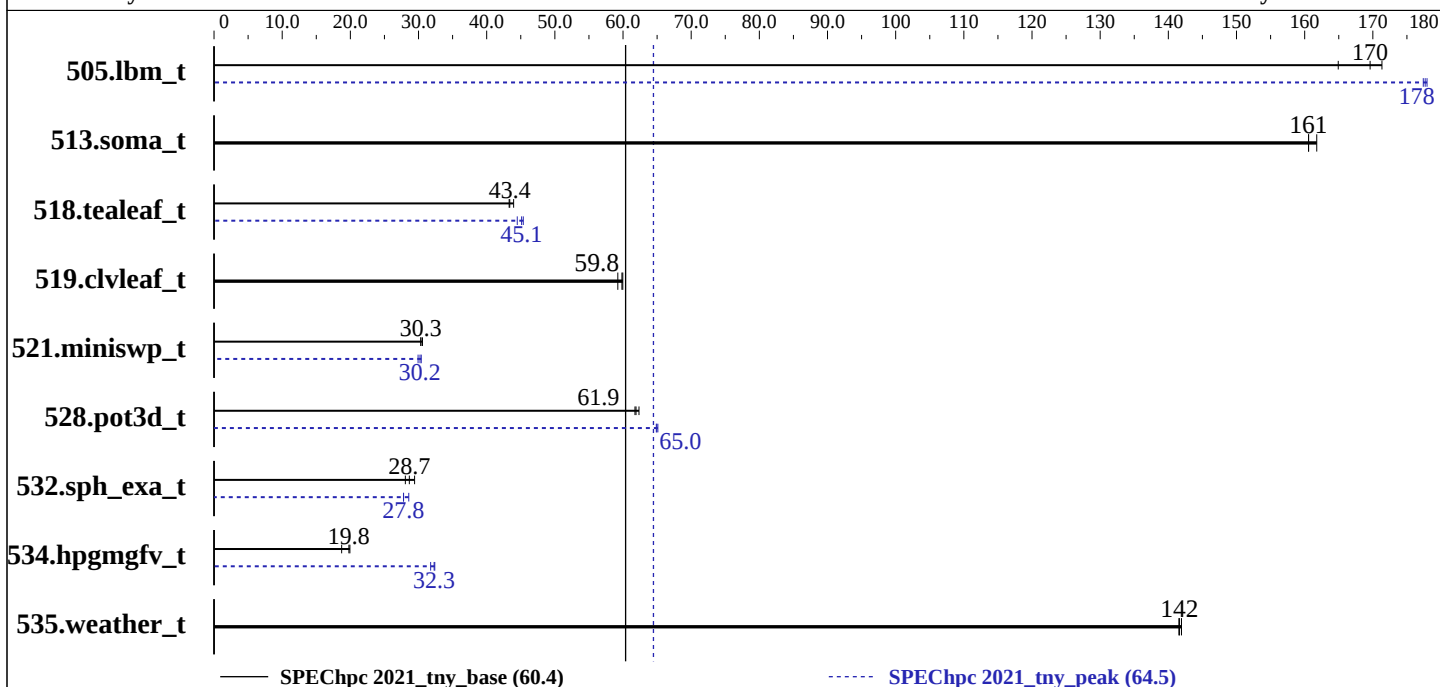
Test Sponsor: Intel

Tested by: Intel

Test Date: Apr-2025

Hardware Availability: Jan-2023

Software Availability: Mar-2025



## Results Table

| Benchmark     | Base  |       |           |         |       |             |             |             | Peak        |       |       |           |             |             |             |             |
|---------------|-------|-------|-----------|---------|-------|-------------|-------------|-------------|-------------|-------|-------|-----------|-------------|-------------|-------------|-------------|
|               | Model | Ranks | Thrds/Rnk | Seconds | Ratio | Seconds     | Ratio       | Seconds     | Ratio       | Model | Ranks | Thrds/Rnk | Seconds     | Ratio       | Seconds     | Ratio       |
| 505.lbm_t     | TGT   | 8     | 1         | 13.6    | 165   | 13.1        | 171         | <u>13.3</u> | <u>170</u>  | TGT   | 8     | 1         | <u>12.7</u> | <u>178</u>  | 12.7        | 177         |
| 513.soma_t    | TGT   | 8     | 1         | 22.9    | 162   | <u>23.0</u> | <u>161</u>  | 23.0        | 161         | TGT   | 8     | 1         | 22.9        | 162         | <u>23.0</u> | <u>161</u>  |
| 518.tealeaf_t | TGT   | 8     | 1         | 38.1    | 43.3  | <u>38.0</u> | <u>43.4</u> | 37.5        | 44.0        | TGT   | 8     | 1         | 37.1        | 44.5        | 36.4        | 45.4        |
| 519.clvleaf_t | TGT   | 8     | 1         | 27.5    | 60.0  | 27.9        | 59.2        | <u>27.6</u> | <u>59.8</u> | TGT   | 8     | 1         | 27.5        | 60.0        | 27.9        | 59.2        |
| 521.miniswp_t | TGT   | 8     | 1         | 52.3    | 30.6  | <u>52.7</u> | <u>30.3</u> | 52.7        | 30.3        | TGT   | 8     | 1         | 52.6        | 30.4        | <u>53.0</u> | <u>30.2</u> |
| 528.pot3d_t   | TGT   | 8     | 1         | 34.1    | 62.3  | 34.4        | 61.8        | <u>34.3</u> | <u>61.9</u> | TGT   | 8     | 1         | <u>32.7</u> | <u>65.0</u> | 32.8        | 64.9        |
| 532.sph_exa_t | TGT   | 8     | 1         | 66.3    | 29.4  | <u>68.1</u> | <u>28.7</u> | 69.5        | 28.1        | TGT   | 8     | 1         | <u>70.1</u> | <u>27.8</u> | 70.2        | 27.8        |
| 534.hpgmgfv_t | TGT   | 8     | 1         | 58.9    | 19.9  | 62.8        | 18.7        | <u>59.4</u> | <u>19.8</u> | TGT   | 8     | 1         | 37.0        | 31.8        | 36.3        | 32.4        |
| 535.weather_t | TGT   | 8     | 1         | 22.7    | 142   | 22.8        | 142         | <u>22.8</u> | <u>142</u>  | TGT   | 8     | 1         | 22.7        | 142         | 22.8        | 142         |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.



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### Hardware Summary

Type of System: Homogenous Cluster  
Compute Node: Intel Server D50DNP1SB (Xeon Platinum 8480+)  
Interconnect: Mellanox HDR  
Compute Nodes Used: 1  
Total Chips: 2  
Total Cores: 112  
Total Threads: 224  
Total Memory: 1 TB  
Max. Peak Threads: 1

### Software Summary

Compiler: Intel oneAPI Compiler 2025.1.0  
MPI Library: Intel MPI Library 2021.15 for Linux OS  
Other MPI Info: None  
Other Software: None  
Base Parallel Model: TGT  
Base Ranks Run: 8  
Base Threads Run: 1  
Peak Parallel Models: TGT  
Minimum Peak Ranks: 8  
Maximum Peak Ranks: 8  
Max. Peak Threads: 1  
Min. Peak Threads: 1

### Node Description: Intel Server D50DNP1SB (Xeon Platinum 8480+)

#### Hardware

Number of nodes: 1  
Uses of the node: Compute  
Vendor: Intel  
Model: Intel Server D50DNP1SB (2 x Intel Xeon Platinum 8480+, 2.0GHz)  
CPU Name: Intel Xeon Platinum 8480+  
CPU(s) orderable: 1, 2 chips  
Chips enabled: 2  
Cores enabled: 112  
Cores per chip: 56  
Threads per core: 2  
CPU Characteristics: Turbo Boost Technology up to 3.8 GHz  
CPU MHz: 2000  
Primary Cache: 32 KB I + 48 KB D on chip per core  
Secondary Cache: 2 MB I+D on chip per core  
L3 Cache: 105 MB I+D on chip per chip  
Other Cache: None  
Memory: 1 TB (16x64 GB DDR5 2Rx4 PC5-4800B-R)  
Disk Subsystem: 1 x 1 1TB NVMe M.2 INTEL SSDPELKH010T8  
Other Hardware: None  
Accel Count: 4  
Accel Model: Intel Data Center GPU Max 1550  
Accel Vendor: Intel  
Accel Type: GPU  
Accel Connection: PCIe Gen5 x16  
Accel ECC enabled: yes  
Accel Description: Intel Data Center GPU Max 1550  
Adapter: Mellanox ConnectX-6 HDR  
Number of Adapters: 1  
Slot Type: PCI-Express 4.0 x16  
Data Rate: 200Gbit/s  
Ports Used: 1

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#### Software

Accelerator Driver: 25.05.32567  
Adapter: Mellanox ConnectX-6 HDR  
Adapter Driver: --  
Adapter Firmware: 20.38.1900  
Operating System: SUSE Linux Enterprise Server 15 SP6  
6.4.0-150600.23.42-default  
Local File System: lustre  
Shared File System: LUSTRE FS  
System State: Run level 5  
Other Software: None



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### Node Description: Intel Server D50DNP1SB (Xeon Platinum 8480+)

#### Hardware (Continued)

Interconnect Type: Mellanox HDR

### Interconnect Description: Mellanox HDR

#### Hardware

Vendor: Mellanox  
Model: Mellanox HDR  
Switch Model: Mellanox Technologies MT28908 Family InfiniBand Switch  
Number of Switches: 12  
Number of Ports: 40  
Data Rate: 200 Gbit/s  
Firmware: 20.38.1900  
Topology: Fat-tree  
Primary Use: MPI Traffic, LustreFS traffic

#### Software

: --

### Submit Notes

The config file option 'submit' was used.

### General Notes

Environment variables set by runhpc before the start of the run:  
LIBOMPTARGET\_LEVEL\_ZERO\_USE\_IMMEDIATE\_COMMAND\_LIST = "all"  
I\_MPI\_FABRICS=shm:ofi  
I\_MPI\_OFFLOAD=1  
I\_MPI\_OFFLOAD\_CELL=tile  
I\_MPI\_OFFLOAD\_TOPOLIB=level\_zero  
I\_MPI\_OFFLOAD\_CELL\_LIST=0,1,2,3,4,5,6,7  
For the following tests src.alt was used in PEAK:  
518 521 528 532 534

### Platform Notes

|                            |                |
|----------------------------|----------------|
| Device Vendor              | Intel          |
| Device Version             | OpenCL 3.0 NEO |
| Driver Version             | 25.05.32567    |
| Base clock                 | 900MHZ         |
| Max clock frequency        | 1600MHZ        |
| Tiles                      | 2              |
| Slices per Tile            | 1              |
| Max compute units per Tile | 512            |

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### Platform Notes (Continued)

|                                    |                   |
|------------------------------------|-------------------|
| Sub-slices per slice               | 64                |
| EUs per sub-slice                  | 8                 |
| Threads per EU                     | 8                 |
| Max work item dimensions           | 3                 |
| Max work item sizes                | 1024x1024x1024    |
| Max work group size                | 1024              |
| Preferred work group size multiple | 32                |
| Max sub-groups per work group      | 64                |
| Sub-group sizes                    | 16, 32            |
| L1 Cache per EU                    | 65536             |
| L2 cache size                      | 427819008         |
| Global memory size                 | 137438953472      |
| Address bits                       | 64, Little-Endian |

### Compiler Version Notes

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CXXC 532.sph\_exa\_t(base, peak)

-----

Intel(R) oneAPI DPC++/C++ Compiler 2025.1.0 (2025.1.0.20250317)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir:  
/lfs/lfs17/mknyazev/intel/2025.1/oneapi/compiler/2025.1/bin/compiler  
Configuration file:  
/lfs/lfs17/mknyazev/intel/2025.1/oneapi/compiler/2025.1/bin/compiler/./icpx.cfg

-----

=====

CC 505.lbm\_t(base, peak) 513.soma\_t(base, peak) 518.tealeaf\_t(base, peak)  
521.miniswp\_t(base, peak) 534.hpgmgfv\_t(base, peak)

-----

Intel(R) oneAPI DPC++/C++ Compiler 2025.1.0 (2025.1.0.20250317)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir:  
/lfs/lfs17/mknyazev/intel/2025.1/oneapi/compiler/2025.1/bin/compiler  
Configuration file:  
/lfs/lfs17/mknyazev/intel/2025.1/oneapi/compiler/2025.1/bin/compiler/./icx.cfg

-----

=====

FC 519.clvleaf\_t(base, peak) 535.weather\_t(base, peak)

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ifx (IFX) 2025.1.0 20250317  
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Test Date: Apr-2025  
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## Compiler Version Notes (Continued)

FC 528.pot3d\_t(base, peak)

ifx: command line warning #10157: ignoring option '-W'; argument is of wrong type  
ifx (IFX) 2025.1.0 20250317  
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## Base Compiler Invocation

C benchmarks:  
mpiicc -cc=icx

C++ benchmarks:  
mpiicpc -cxx=icpx

Fortran benchmarks:  
mpiifort -fc=ifx

## Base Portability Flags

505.lbm\_t: -DUSE\_MPI  
513.soma\_t: -DUSE\_MPI -DSPEC\_NO\_VAR\_ARRAY\_REDUCE  
518.tealeaf\_t: -DUSE\_MPI  
519.clvleaf\_t: -DUSE\_MPI  
528.pot3d\_t: -DUSE\_MPI  
535.weather\_t: -DUSE\_MPI

## Base Optimization Flags

C benchmarks:  
-O3 -xCORE-AVX512 -flto -mprefer-vector-width=512 -ffast-math  
-fopenmp -fopenmp-targets=spir64\_gen  
-ftarget-register-alloc-mode=pvc:auto  
-Xopenmp-target-backend '-device pvc -revision\_id 0x2f' -DSPEC\_COLLAPSE  
-fopenmp-optimistic-collapse

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**Software Availability:** Mar-2025

## Base Optimization Flags (Continued)

C++ benchmarks:

-O3 -xCORE-AVX512 -flto -mprefer-vector-width=512 -ffast-math  
-fiopenmp -fopenmp-targets=spir64\_gen  
-ftarget-register-alloc-mode=pvc:auto  
-Xopenmp-target-backend '-device pvc -revision\_id 0x2f' -DSPEC\_COLLAPSE

Fortran benchmarks:

-DSPEC\_COLLAPSE -O3 -xCORE-AVX512 -flto -mprefer-vector-width=512  
-ffast-math -fiopenmp -fopenmp-targets=spir64\_gen  
-ftarget-register-alloc-mode=pvc:auto  
-Xopenmp-target-backend '-device pvc -revision\_id 0x2f'  
-DSPEC\_ACCEL\_AWARE\_MPI -fopenmp-target-loopopt

## Base Other Flags

Fortran benchmarks:

528.pot3d\_t: -Wno-incompatible-function-pointer-types

## Peak Compiler Invocation

C benchmarks:

mpiicc -cc=icx

C++ benchmarks:

mpiicpc -cxx=icpx

Fortran benchmarks:

mpiifort -fc=ifx

## Peak Portability Flags

505.lbm\_t: -DUSE\_MPI  
513.soma\_t: -DUSE\_MPI -DSPEC\_NO\_VAR\_ARRAY\_REDUCE  
518.tealeaf\_t: -DUSE\_MPI  
519.clvleaf\_t: -DUSE\_MPI  
528.pot3d\_t: -DUSE\_MPI  
535.weather\_t: -DUSE\_MPI



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## Peak Optimization Flags

C benchmarks:

505.lbm\_t: -O3 -xCORE-AVX512 -flto -mprefer-vector-width=512  
-ffast-math -fiopenmp -fopenmp-targets=spir64\_gen  
-ftarget-register-alloc-mode=pvc:large  
-Xopenmp-target-backend '-device pvc -revision\_id 0x2f'  
-DSPEC\_COLLAPSE -fopenmp-optimistic-collapse

513.soma\_t: basepeak = yes

518.tealeaf\_t: Same as 505.lbm\_t

521.miniswp\_t: -O3 -xCORE-AVX512 -flto -mprefer-vector-width=512  
-ffast-math -fiopenmp -fopenmp-targets=spir64\_gen  
-ftarget-register-alloc-mode=pvc:small  
-Xopenmp-target-backend '-device pvc -revision\_id 0x2f'  
-DSPEC\_COLLAPSE -fopenmp-optimistic-collapse

534.hpgmgfv\_t: -O3 -xCORE-AVX512 -flto -mprefer-vector-width=512  
-ffast-math -fiopenmp -fopenmp-targets=spir64\_gen  
-ftarget-register-alloc-mode=pvc:auto  
-Xopenmp-target-backend '-device pvc -revision\_id 0x2f'  
-DSPEC\_COLLAPSE -DSPEC\_ACCEL\_AWARE\_MPI  
-fopenmp-optimistic-collapse

C++ benchmarks:

-O3 -xCORE-AVX512 -flto -mprefer-vector-width=512 -ffast-math  
-fiopenmp -fopenmp-targets=spir64\_gen  
-ftarget-register-alloc-mode=pvc:auto  
-Xopenmp-target-backend '-device pvc -revision\_id 0x2f' -DSPEC\_COLLAPSE

Fortran benchmarks:

519.clvleaf\_t: basepeak = yes

528.pot3d\_t: -DSPEC\_COLLAPSE -O3 -xCORE-AVX512 -flto  
-mprefer-vector-width=512 -ffast-math -fiopenmp  
-fopenmp-targets=spir64\_gen  
-ftarget-register-alloc-mode=pvc:auto  
-Xopenmp-target-backend '-device pvc -revision\_id 0x2f'  
-DSPEC\_ACCEL\_AWARE\_MPI -fopenmp-target-loopopt

535.weather\_t: basepeak = yes



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## Peak Other Flags

Fortran benchmarks:

528.pot3d\_t: -Wno-incompatible-function-pointer-types

The flags file that was used to format this result can be browsed at  
[http://www.spec.org/hpc2021/flags/Intel\\_compiler\\_flags.2025-05-22.00.html](http://www.spec.org/hpc2021/flags/Intel_compiler_flags.2025-05-22.00.html)

You can also download the XML flags source by saving the following link:  
[http://www.spec.org/hpc2021/flags/Intel\\_compiler\\_flags.2025-05-22.00.xml](http://www.spec.org/hpc2021/flags/Intel_compiler_flags.2025-05-22.00.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

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