



SPEC® MPIL2007 Result

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Huawei
Huawei 2488H V5

SPECmpiL_peak2007 = Not Run

SPECmpiL_base2007 = NC

MPI2007 license: 27
Test sponsor: Huawei
Tested by: Huawei

Test date: Jun-2017
Hardware Availability: Jul-2017
Software Availability: Apr-2017

SPEC has determined that this result is not in compliance with the SPEC MPI2007 run and reporting rules. Specifically, the result does not meet the 90 day general availability requirement as the system was not available within 90 days of result publication.

- Ranks
- 121.pop2
- 122.tachyon
- 125.RAxML
- 126.lammps
- 128.GAPgeofem
- 129.tera_tf
- 132.zeusmp2
- 137.lu
- 142.dmilc
- 143.dleslie
- 145.IGemsFDTD
- 147.l2wrf2

Results Table

Benchmark	Base								Peak					
	Ranks	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Ranks	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
121.pop2	112	NC	NC	NC	NC	NC	NC							
122.tachyon	112	NC	NC	NC	NC	NC	NC							
125.RAxML	112	NC	NC	NC	NC	NC	NC							
126.lammps	112	NC	NC	NC	NC	NC	NC							

Table continues on next page. Results appear in the order in which they were run. Bold underlined text indicates a median measurement.



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Results Table (Continued)

Benchmark	Base							Peak						
	Ranks	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Ranks	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
128.GAPgeofem	112	NC	NC	NC	NC	NC	NC							
129.tera_tf	112	NC	NC	NC	NC	NC	NC							
132.zeusmp2	112	NC	NC	NC	NC	NC	NC							
137.lu	112	NC	NC	NC	NC	NC	NC							
142.dmilc	112	NC	NC	NC	NC	NC	NC							
143.dleslie	112	NC	NC	NC	NC	NC	NC							
145.lGemsFDTD	112	NC	NC	NC	NC	NC	NC							
147.l2wrf2	112	NC	NC	NC	NC	NC	NC							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Hardware Summary

Type of System: Homogeneous
Compute Node: 2488H V5 Node
File Server Node: 2488H V5 Node
Head Node: 2488H V5 Node
Total Compute Nodes: 1
Total Chips: 4
Total Cores: 112
Total Threads: 112
Total Memory: 768 GB
Base Ranks Run: 112
Minimum Peak Rank: --
Maximum Peak Rank: --

Software Summary

C Compiler: Intel C++ Composer XE 2017 for Linux, Version 17.0.4.196 Build 20170411
C++ Compiler: Intel C++ Composer XE 2017 for Linux, Version 17.0.4.196 Build 20170411
Fortran Compiler: Intel Fortran Composer XE 2017 for Linux, Version 17.0.4.196 Build 20170411
Base Pointers: 64-bit
Peak Pointers: 64-bit
MPI Library: Intel MPI Library for Linux, Version 2017 Update 3 Build 20170405
Other MPI Info: None
Pre-processors: No
Other Software: None

Node Description: 2488H V5 Node

Hardware

Number of nodes: 1
Uses of the node: head, compute, fileserver
Vendor: Huawei
Model: Huawei 2488H V5
CPU Name: Intel Xeon Platinum 8180 CPU

Software

Adapter: 0
Adapter Driver: 0
Adapter Firmware: --

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Node Description: 2488H V5 Node

CPU(s) orderable:	2, 4 chip	Operating System:	Red Hat Enterprise Linux Server 7.3 (Maipo)
Chips enabled:	4	Linux kernel:	Linux kernel 3.10.0-514.el7.x86_64
Cores enabled:	112	Local File System:	xfs
Cores per chip:	28	Shared File System:	None
Threads per core:	1	System State:	Multi-User
CPU Characteristics:	Intel Turbo Boost Technology on, Hyper-Threading Technology (SMT) disabled	Other Software:	None
CPU MHz:	2500		
Primary Cache:	32 KB I + 32 KB D on chip per core		
Secondary Cache:	1 MB I+D on chip per core		
L3 Cache:	38.5 MB I+D on chip per chip		
Other Cache:	None		
Memory:	768 GB (24 x 32 GB 2Rx4 PC16-2666V, running at 2666 MHz)		
Disk Subsystem:	2 x 600 GB 10K RPM SATA		
Other Hardware:	None		
Adapter:	0		
Number of Adapters:	0		
Slot Type:	0		
Data Rate:	0		
Ports Used:	0		
Interconnect Type:	0		

Submit Notes

The config file option 'submit' was used.

General Notes

MPI startup command:
mpiexec.hydra command was used to start MPI jobs.

BIOS settings:
Intel Hyper-Threading Technology (SMT):Disable
Intel Turbo Boost Technology (Turbo):Enabled (default is Enabled)



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Base Compiler Invocation

C benchmarks:
mpiicc

C++ benchmarks:
126.lammps: mpiicpc

Fortran benchmarks:
mpiifort

Benchmarks using both Fortran and C:
mpiicc mpiifort

Base Portability Flags

121.pop2: -DSPEC_MPI_CASE_FLAG
126.lammps: -DMPIT_IGNORE_CXX_SEEK

Base Optimization Flags

C benchmarks:
-O3 -xCORE-AVX2 -no-prec-div

C++ benchmarks:
126.lammps: -O3 -xCORE-AVX2 -no-prec-div

Fortran benchmarks:
-O3 -xCORE-AVX2 -no-prec-div

Benchmarks using both Fortran and C:
-O3 -xCORE-AVX2 -no-prec-div



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The flags file that was used to format this result can be browsed at http://www.spec.org/mpi2007/flags/Huawei_x86_64_Intel_linux.xml.

You can also download the XML flags source by saving the following link:
http://www.spec.org/mpi2007/flags/Huawei_x86_64_Intel_linux.xml

Non-Compliant

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC MPI2007 v2.0.1.
Report generated on Mon Jul 30 15:27:01 2018 by SPEC MPI2007 PS/PDF formatter v1463.
Originally published on 11 July 2017.