SPEC CINT95rate Results

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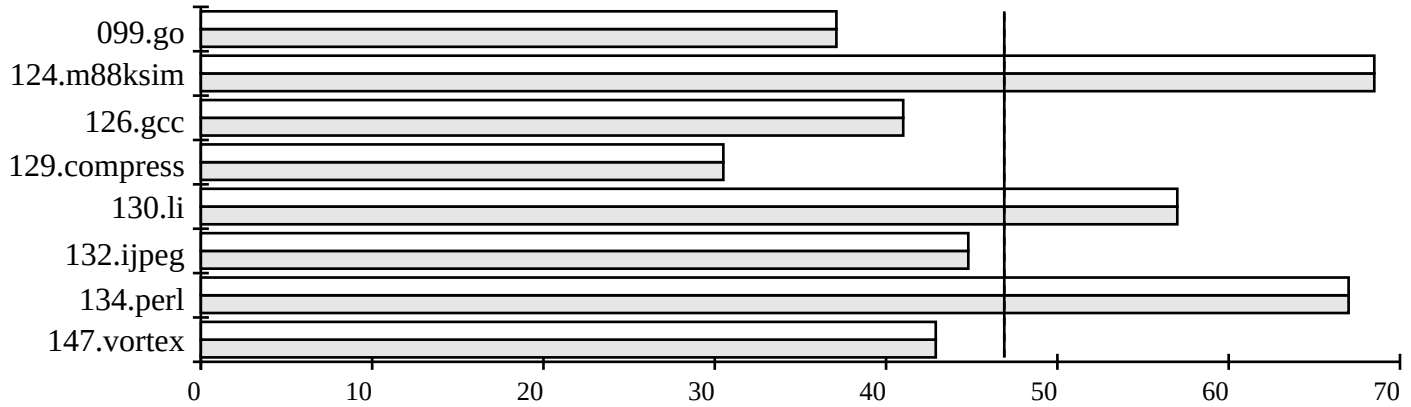
Siemens Nixdorf Informationssysteme PRIMERGY-550

SPECint_rate95	=	46.9
SPECint_rate_base95	=	46.9

SPEC license # 22	Tested By: SNI, Augsburg / Germany	Test Date: Feb-96	Hardware Avail: Mar-96	Software Avail: Feb-96



Hardware/Software Configuration for: PRIMERGY-550

Hardware

Model Name:	PRIMERGY-550
CPU:	133 MHz Pentium
FPU:	Integrated
Number of CPU(s):	2
Primary Cache:	8KBI+8KBD on chip
Secondary Cache:	512KB(I+D) off chip shared
Other Cache:	None
Memory:	128MB
Disk Subsystem:	2 x 2 GB
Other Hardware:	Mylex DAC960 Controller

Software

Operating System:	Unixware 2.1
Compiler:	Intel Reference Compiler 2.2 Beta
File System:	UFS,VXFS
System State:	Single User

Benchmark # and Name	Base Copies	Base Run Time	Base SPEC Ratio	Copies	Run Time	SPEC Ratio
099.go	2	2229	37.1	2	2229	37.1
124.m88ksim	2	499	68.5	2	499	68.5
126.gcc	2	747	41.0	2	747	41.0
129.compress	2	1064	30.5	2	1064	30.5
130.li	2	600	57.0	2	600	57.0
132.ijpeg	2	964	44.8	2	964	44.8
134.perl	2	510	67.0	2	510	67.0
147.vortex	2	1132	42.9	2	1132	42.9
SPECint_rate_base95 (G. Mean)			46.9			
SPECint_rate95 (G. Mean)						46.9

Notes/Tuning Information

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Compiled by Intel
Base and peak flags are the same and use Feedback Directed Optimization
Pass1: -tp p5 -ipo -prof_gen -ircdb_dir /tmp/IRCDB -dn
Pass2: -tp p5 -ipo -prof_use -ircdb_dir /tmp/IRCDB -dn
-ircdb_dir is a location flag and not an optimization flag
Portability: 124: -DSYSV -DLEHOST 130, 134, 147: -lm  132: -DSYSV  126: -lm -lc -L/usr/ucblib -lucb -lmalloc
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