SPEC CFP95 Results

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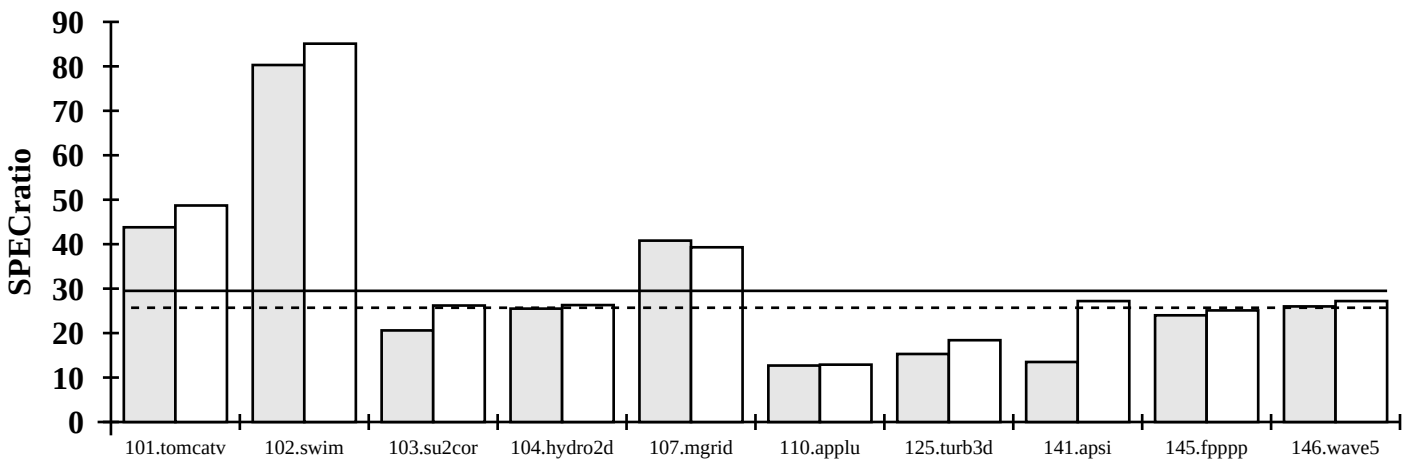
Sun Microsystems
Ultra 60 Model 2360

SPECfp95 = 29.5

SPECfp_base95 = 25.7

SPEC license # 6	Tested By: Sun, Palo Alto	Test Date: Apr-98	Hardware Avail: May-98	Software Avail: Oct-98



Hardware/Software Configuration for:
Ultra 60 Model 2360

Hardware

Model Name:	Ultra 60 Model 2360
CPU:	360 MHz UltraSPARC-II
FPU:	Integrated
Number of CPU(s):	2
Primary Cache:	16KBI+16KBD on chip
Secondary Cache:	4MB(I+D) off chip
Other Cache:	None
Memory:	512MB
Disk Subsystem:	2 x 4.0GB
Other Hardware:	Fast Ethernet System clock frequency: 120 MHz

Software

Operating System:	Solaris 2.6 Hardware 3/98
Compiler:	Sun f77 5.0 alpha
File System:	UFS
System State:	Single User
Kernel Extensions:	consistent_coloring=1

Benchmark # and Name	Reference Time	Base Run Time	Base SPEC Ratio	Run Time	SPEC Ratio
101.tomcatv	3700	84.5	43.8	76	48.7
102.swim	8600	107	80.3	101	85.1
103.su2cor	1400	67.9	20.6	53	26.2
104.hydro2d	2400	94.1	25.5	91	26.3
107.mgrid	2500	61.3	40.8	64	39.3
110.applu	2200	173	12.7	170	12.9
125.turb3d	4100	267	15.3	223	18.4
141.apsi	2100	155	13.5	77	27.2
145.fpppp	9600	400	24.0	383	25.1
146.wave5	3000	115	26.0	110	27.2
SPECfp_base95 (G. Mean)			25.7		
		SPECfp95 (G. Mean)			29.5

Notes/Tuning Information

```
P1= -xprofile=collect:‘pwd‘/../feedback
P2= -xprofile=use:‘pwd‘/../feedback
BCOPY=-Qoption iropt -O4+bcopy; INV=-Qoption iropt -O4+invccexp; BMR=-Qoption iropt -O4+bmerge
Baseline:    -fast -xarch=v8plusa -xprefetch -xparallel
Peak:    -fast -xO4 -xarch=v8plusa -xtarget=ultra2 -xcache=16/32/1:2048/64/1
101.tomcatv: -fast -xarch=v8plusa -xpad=local:23 -xprefetch -xparallel -xreduction
102.swim:    -fast -xarch=v8plusa -xpad=common:15 -xprefetch -fsimple=2 -xparallel
103.su2cor:  -fast -xarch=v8plusa -xvector -Qoption cg -Qms_pipe+nfll=5 -fsimple=2  ${BCOPY} -xparallel -xreduction
103.su2cor:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
104.hydro2d: -fast -xarch=v8plusa -xprefetch -Qoption iropt -O4+pde -fsimple=2 -xparallel
104.hydro2d:  PASS1= ${P1}; PASS2= ${P2};
107.mgrid:   -fast -xarch=v8plusa -xprefetch ${BCOPY} -xparallel
107.mgrid:   PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
110.applu:   -fast -xarch=v8plusa -Qoption iropt -O4+ddint+scalarrep,-ldstr -fsimple=2 -xparallel
125.turb3d:  -fast -xarch=v8plusa -stackvar -xpad -fsimple=2 ${INV}
125.turb3d:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
141.apsi:    -fast -xarch=v8plusa -fsimple=2 -stackvar  -Qoption iropt -Si36  ${BMR} -Qoption iropt -O4+algassoc
141.apsi:    PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
145.fpppp:   -fast -xarch=v8plusa -xO5 -xsafe=mem -fsimple=2 -Qoption iropt -O4+algassoc
145.fpppp:   PASS1= ${P1}; PASS2= ${P2};
146.wave5:   -fast -xarch=v8plusa  -stackvar -fsimple=2 -Qoption iropt -O4+unroll ${BCOPY}  -xparallel
Shell limits: # ulimit -n 1024  or % limit descriptors 1024
```

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